

FMH47N60S1

FUJI POWER MOSFET

Super J-MOS series

N-Channel enhancement mode power MOSFET

■ Features

Low on-state resistance Low switching loss easy to use (more controllabe switching dV/dt by Rg)

■ Applications

UPS

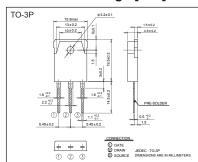
Server

Telecom

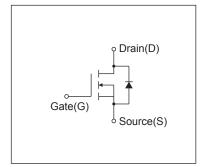
Power conditioner system

Power supply

■ Outline Drawings [mm]



■ Equivalent circuit schematic



■ Absolute Maximum Ratings at T_c=25°C (unless otherwise specified)

Description	Symbol	Characteristics	Unit	Remarks
Drain Sauras Valtaria	V _{DS}	600	V	
Drain-Source Voltage	V _{DSX}	600	V	V _{GS} =-30V
	In	±47	Α	Tc=25°C Note*1
Continuous Drain Current		±29.7	Α	Tc=100°C Note*1
Pulsed Drain Current	I _{DP}	±141	Α	
Gate-Source Voltage	V _{GS}	±30	V	
Repetitive and Non-Repetitive Maximum Avalanche Current	I _{AR}	9.5	Α	Note *2
Non-Repetitive Maximum Avalanche Energy	Eas	1267.4	mJ	Note *3
Maximum Drain-Source dV/dt	dV _{DS} /dt	50	kV/μs	V _{DS} ≤ 600V
Peak Diode Recovery dV/dt	dV/dt	13	kV/μs	Note *4
Peak Diode Recovery -di/dt	-di/dt	80	A/µs	Note *5
Maximum Bower Discination	PD	2.5	W	T _a =25°C
Maximum Power Dissipation		390	VV	Tc=25°C
Oneveting and Stevens Temperature range	Tch	150	°C	
Operating and Storage Temperature range	T _{stg}	-55 to +150	°C	

Note *1 : Limited by maximum channel temperature.

Note *2: T_{ch}≤150°C, See Fig.1 and Fig.2
Note *3: Starting T_{ch}=25°C, I_{AS}=7.6A, L=40.2mH, V_{DD}=60V, R_G=50Ω, See Fig.1 and Fig.2
E_{AS} limited by maximum channel temperature and avalanche current.

Note *4 : Ir≤-23.5A, -di/dt=80A/µs, V_{DD}≤300V, T_{ch}≤150°C. Note *5 : Ir≤-23.5A, dV/dt=13kV/µs, V_{DD}≤300V, T_{ch}≤150°C.

■ Electrical Characteristics at T_c=25°C (unless otherwise specified)

Static Ratings

Description	Symbol	Conditions		min.	typ.	max.	Unit
Drain-Source Breakdown Voltage	BV _{DSS}	I _D =250μA V _{GS} =0V		600	-	-	V
Gate Threshold Voltage	V _{GS(th)}	I _D =250µA V _{DS} =V _{GS}		2.5	3.0	3.5	V
Zero Gate Voltage Drain Current	Ipss	V _{DS} =600V V _{GS} =0V	T _{ch} =25°C	-	-	25	μА
		V _{DS} =480V V _{GS} =0V	T _{ch} =125°C	-	-	250	
Gate-Source Leakage Current	Igss	V _{GS} = ±30V V _{DS} =0V		-	10	100	nA
Drain-Source On-State Resistance	R _{DS(on)}	I _D =23.5A V _{GS} =10V		-	0.059	0.07	Ω
Gate resistance	R _G	f=1MHz, open drain		-	1.1	-	Ω

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Dynamic Ratings

Description	Symbol	Conditions	min.	typ.	max.	Unit
Forward Transconductance	g _{fs}	I _D =23.5A V _{DS} =25V	19	38	-	S
Input Capacitance	Ciss	V _{ps} =10V	-	4000	-	
Output Capacitance	Coss	V _{GS} =0V	-	8400	-	
Reverse Transfer Capacitance	Crss	f=1MHz	-	770	-	
Effective output capacitance, energy related (Note *6)	C _{o(er)}	V _{SS} =0V V _{DS} =0480V	-	210	-	pF
Effective output capacitance, time related (Note *7)	C _{o(tr)}	V _{cs} =0V V _{cs} =0480V ID=constant	-	790	-	
Turn-On Time	t _{d(on)}		-	36	-	
Turn-On Time	tr	$\begin{array}{c} V_{\text{DD}}\text{=}400\text{V}, V_{\text{GS}}\text{=}10\text{V} \\ I_{\text{D}}\text{=}23.5\text{A}, R_{\text{G}}\text{=}8.2\Omega \\ \text{See Fig.3 and Fig.4} \end{array}$	-	83	-	ns
Turn Off Time	t _{d(off)}		-	135	-	
Turn-Oil Time	urn-Off Time		-	17	-	
Total Gate Charge	Q _G		-	125	-	0
Gate-Source Charge	Q _{GS}	V _{DD} =480V, I _D =47A V _{SS} =10V See Fig.5	-	29	-	
Gate-Drain Charge	Q _{GD}		-	46	-	nC
Drain-Source crossover Charge	Qsw		-	18	-	

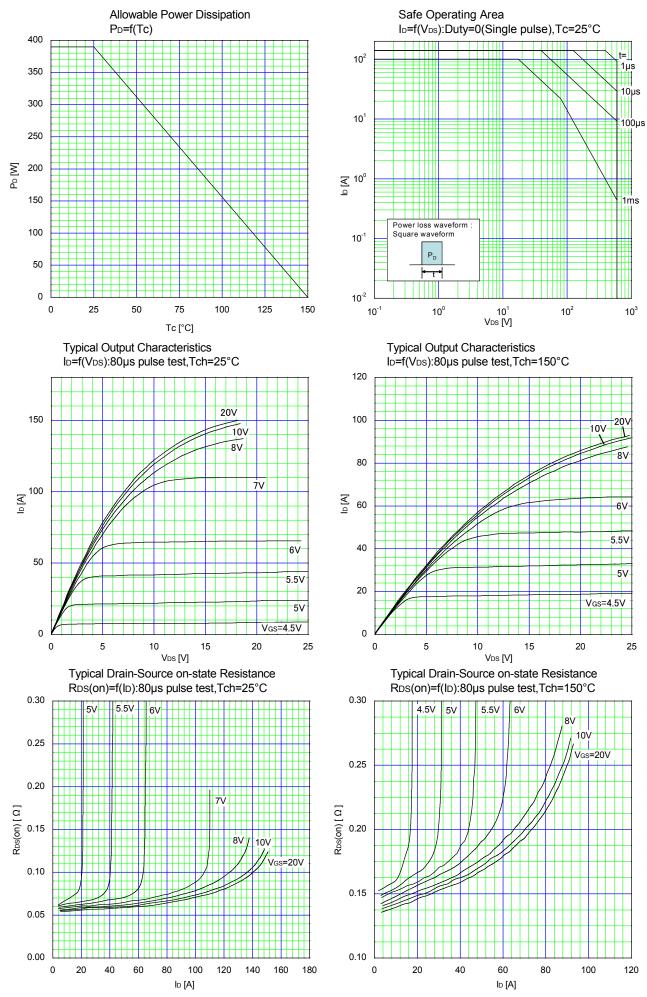
Note *6 : $C_{o(er)}$ is a fixed capacitance that gives the same stored energy as C_{oss} while V_{DS} is rising from 0 to 80% BV_{DSS}. Note *7 : $C_{o(tr)}$ is a fixed capacitance that gives the same charging times as C_{oss} while V_{DS} is rising from 0 to 80% BV_{DSS}.

Reverse Diode

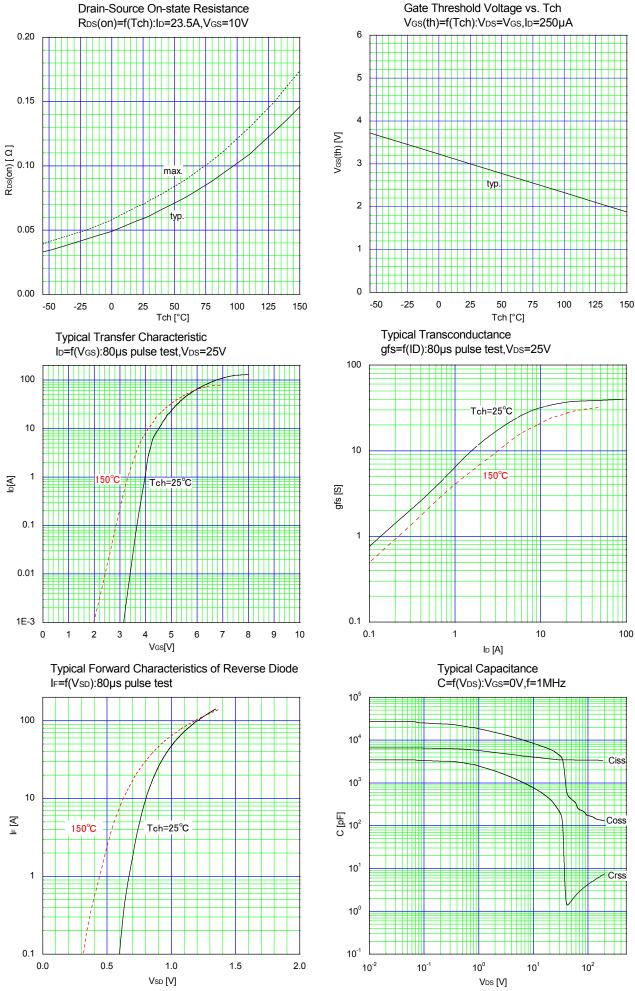
Description	Symbol	Conditions	min.	typ.	max.	Unit
Avalanche Capability	lav	L=20.6mH, T _{ch} =25°C See Fig.1 and Fig.2	9.5	-	-	Α
Diode Forward On-Voltage	V _{SD}	I _F =47A, V _{GS} =0V T _{ch} =25°C	-	1.0	1.35	V
Reverse Recovery Time	trr	I _F =23.5A, V _{GS} =0V V _{DD} =300V -di/dt=80A/µs T _{ch} =25°C See Fig.6	-	470	-	ns
Reverse Recovery Charge	Qrr		-	8.7	-	μC
Peak Reverse Recovery Current	I _{rp}		-	36	-	А

■ Thermal Characteristics

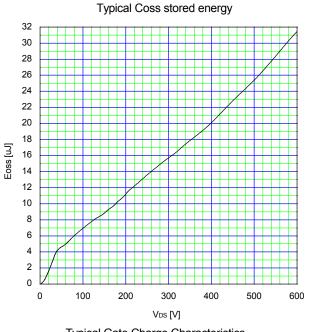
Description	Symbol	min.	typ.	max.	Unit
Channel to Case	R _{th(ch-c)}	-	-	0.32	°C/W
Channel to Ambient	R _{th(ch-a)}	-	-	50	°C/W

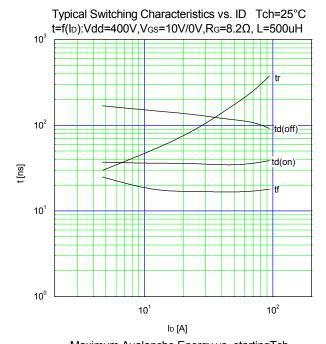


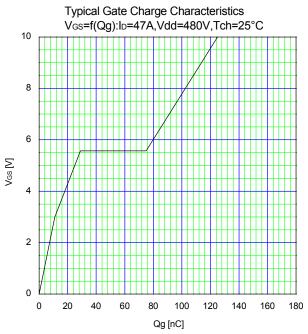
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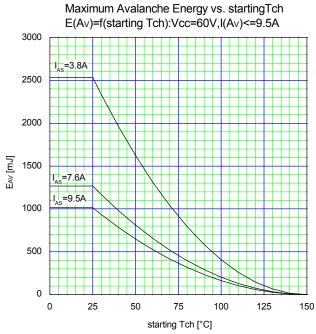


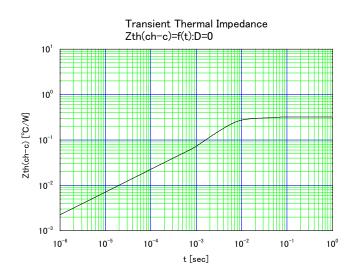
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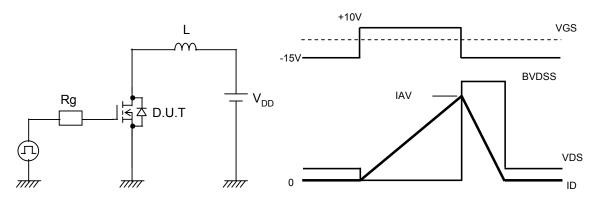


Fig.1 Avalanche Test circuit

Fig.2 Operating waveforms of Avalanche Test

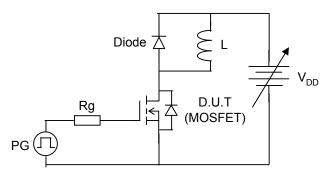


Fig.3 Switching Test circuit

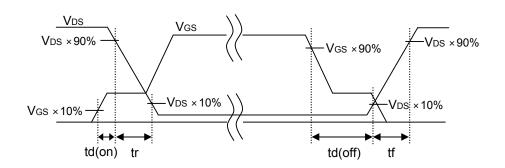


Fig.4 Operating waveform of Switching Test

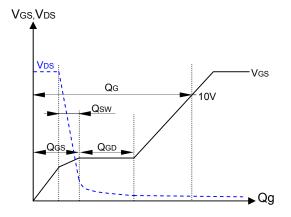


Fig.5 Operating waveform of Gate charge Test

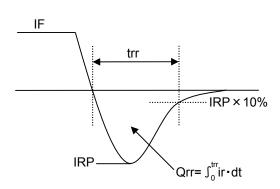
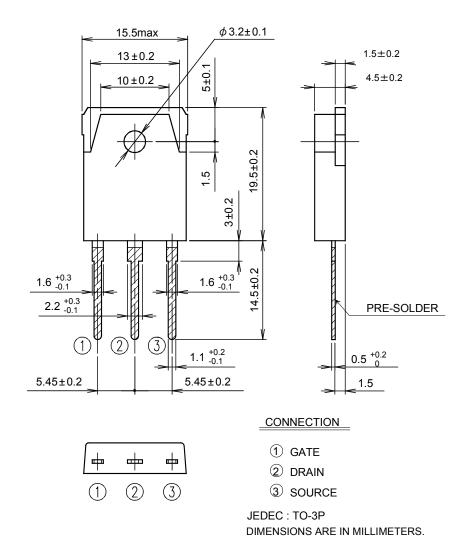
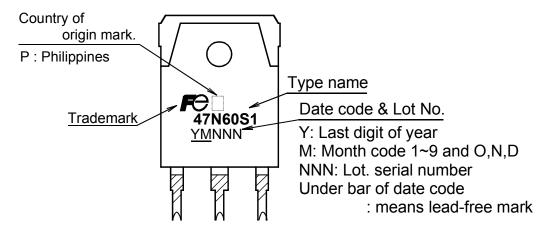


Fig.6 Operating waveform of Body diode Recovery Test

■ Outview: TO-3P Package



■ Marking



^{*} The font (font type, size) and the trademark-size might be actually different.

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