



POE-D11-02-E-18

CERAMIC DISC CAPACITOR SAFETY RECOGNIZED, AC SERIES

Ver: 18

Page: 1 / 20

PRODUCT SPECIFICATION

PRODUCT: CERAMIC DISC CAPACITOR SAFETY RECOGNIZED

TYPE: AC SERIES

CUSTOMER:

DOC. NO.: POE-D11-02-E-18

APPROVED BY CUSTOMER

VENDOR:

■ WALSIN TECHNOLOGY CORPORATION

566-1, KAO SHI ROAD,YANG-MEI TAO-YUAN, TAIWAN

1. PAN OVERSEAS (GUANGZHOU) ELECTRONIC CO.,LTD.

NO.277,HONG MING ROAD,EASTERN SECTION, GUANG ZHOU ECONOMIC AND TECHNOLOGY DEVELOPMENT ZONE,CHINA

1. DONGGUAN WALSIN TECHNOLOGY ELECTRONICS CO., LTD.

NO.638, MEI JING WEST ROAD,XINIUPO,ADMINISTRATIVE ZONE,DALANGTOWN,DONGGUAN CITY, GUANGDONG PROVINCE

MANUFACTURE SITE:

V PAN OVERSEAS (GUANGZHOU) ELECTRONIC CO.,LTD.

NO.277,HONG MING ROAD,EASTERN SECTION, GUANG ZHOU ECONOMIC AND TECHNOLOGY DEVELOPMENT ZONE,CHINA

V DONGGUAN WALSIN TECHNOLOGY ELECTRONICS CO., LTD.

 $NO.638, MEI\ JING\ WEST\ ROAD, XINIUPO, ADMINISTRATIVE$

ZONE, DALANGTOWN, DONGGUAN CITY, GUANGDONG PROVINCE



Record of change

3. Add test item "Temperature Cycle". 4. Add item 10 "Drawing of internal structure and material list" 20 2011/4/27 8 1. Add "1AC" type; 2. Delete "old P/N" 3. Define the marking of the type "0AC" and "1AC"; 4. Review the "Standard No. & Subclass & W.V. & Recognized No". 9 2012/2/7 9 1. Review the "Standard No. & Subclass & W.V. & Recognized No". 9 2. Review the "Operating Temperature Range" from "-25 to +125°C" to "-40 to +125°C" 3. Review the "Operating Temperature Range" from "-25 to +125°C" to "-40 to +125°C" 3. Review the temperature of Step 1 from "-25+0/-3" to "-40+0/-3" 2012/4/6 10 1. In order to improve the traceability of the product, change the date code on capacitor body, new date code can trace back to production "Lot No."	Date	Version						
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3. Delete "No marked with "_" stand for Pb free". Add "epoxy resin" 4. Review the Solderability time from 2±0.5s to 5±0.5s 1. Review the "Manufactured Date" to "Products ID" on the marking page 2013/10/16 2013/10/16 1. Review the "Manufactured Date" to "Products ID" on the marking page 2. Delete "The marking can be printed on either one side or two side of coating body.				8				
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1. Review the "Manufactured Date" to "Products ID" on the marking page 2. Delete "The marking can be printed on either one side or two side of coating body.			- ·					
2. Delete "The marking can be printed on either one side or two side of coating body.			-	11				
10010/10/16 10 1				8				
and add for SAI part number 10-11 digits < 0/ products to two sides	2013/10/16	12		8				
and "for SAP part number 11-12 digits ≥ '08' products" to one side.								



Record of change (continue)

		Record of change (continue)	l
Date	Version	Description	page
2014/11/5	13	 Review the terminal position of the lead wire. Review the product of ID, add the code "D" for the products of Dongguan Walsin Technology Electronics Co., Ltd. 	8 9
		3. Review the minimum packing quantity of taping code AM.	16
2014/12/25	14	1. Add"3.1Norminal parts&3.2 special for surge parts" for "3. Part numbering/T.C/Capacitance/ Tolerance/Diameter"	7
2015/5/27	15	Add the X1:440Vac/Y2:300Vac safety approval for CQC.	4,10
2015/8/4	16	Delete the H(Inside kink lead)	5,8
2015/11/12	17	Review the normal parts of Taping type Review Marking	6,7 9
2016/1/27	18	1. Review the Available lead code of Lead Configuration 2. Revised standard NO. of VDE, SEV, SEMKO, FIMKO, NEMKO, DEMKO and KTL.	5 10

Table of Contents

No.	Item	Page
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3	Part numbering/T.C/Capacitance/ Tolerance/Diameter	6
4	Taping Format	8
5	Marking	9
6	Scope	10
7	Specification and test method	11~15
8	Packing specification	16
9	Notices	17~19
10	Drawing of Internal Structure and material list	20



1. Part number for SAP system

(Ex.) \underline{YV} $\underline{0}$ \underline{AC} $\underline{472}$ \underline{M} $\underline{10}$ $\underline{0}$ \underline{L} $\underline{20}$ \underline{C} $\underline{7}$ \underline{H} $\underline{(1)}$ $\underline{(2)-1}$ $\underline{(2)-2}$ $\underline{(3)}$ $\underline{(4)}$ $\underline{(5)}$ $\underline{(6)}$ $\underline{(7)}$ $\underline{(8)}$ $\underline{(9)}$ $\underline{(10)}$ $\underline{(11)}$

(1)Temperature characteristic (identified code)

CODE	CH(NP0)	SL	YP (Y5P)	YV(Y5V)	YU (Y5U)
Cap. Change	0±60PPM/°C	-1000~+350PPM/°C (+20°C~+85°C)	±10%	-80% ~ +30%	-55% to +20%

- (2)-1 Rated voltage(identified by 1-figure code) : 0 = X1:400Vac/Y2:250Vac; 1 = X1:440Vac/Y2:300Vac (Only Approval by VDE/ENEC/UL/CSA/CQC, marking VDE/ENEC)
- (2)-2 Type(identified by 2-figure code): AC
- (3)Capacitance (identified by 3-figure code) : EX.221=220pF
- (4)Capacitance tolerance (identified by code) : C:±0.25pF,D:±0.5pF,J:±5%,K:±10%,M:±20%
- (5) Nominal body diameter dimension (identified by 2-figure code): 06--Dmax7.0mm, 07--Dmax8.0mm...
- (6)Internal code: 0--Normal, other code--Special control
- (7)Lead Style: Refer to "2. Mechanical".

(8)Packing mode and lead length (identified by 2-figure code)

Taping Code	Description
AF	Ammo box and product pitch: 15.0 mm
AM	Ammo box and product pitch: 25.4 mm

Bulk Code	Description
3E	Lead length: 3.5mm
04	Lead length: 4.0mm
4E	Lead length: 4.5mm
20	Lead length: 20.0mm

(9) Tolerance of lead length

Code	Description
A	±0.5 mm
	(only for kink lead type)
В	±1.0 mm
С	Min.
D	Taping special purpose

(10)Lead space

Code	Description
7	7.5±1.0 mm
M	7.5±0.5 mm
0	10±1.0 mm
A	10±0.5 mm

(11)Epoxy resin code

Code	Description
В	Pb free, Epoxy Resin
Н	Halogen and Pb free, epoxy resin.



2. Mechanical

Encapsulation: Epoxy resin, flammability UL94 V-0

Available lead code(unit: mm)

Available lead code(unit: mm)									
Lead type	SAP P/N (13-17)digits	Lead space (F)	Lead Length (L)	Packing	Lead Configuration				
Lead style:L	L20C7	7.5 ±1.0	20 min.	5.44	Dmax. Tmax.				
Type L Straight long lead	L20C0	10 ± 1.0	20 min.	Bulk	Ød L				
	BAFD7				Dmax. Tmax.				
Lead style:B Type B	BAMD7	Refer to "4. T	aping format"	Tap. Ammo	e e				
Straight long lead	BAMD0				Ød L				
	L03B7	7.5 ± 1.0	3.0 ± 1.0						
	L4EB7	7.5 ± 1.0	4.5 ± 1.0	=	Dmax. Tmax.				
Lead style: L	L05B7	7.5 ± 1.0	5.0 ± 1.0	-					
Lead Style · L	L03B0	10 ± 1.0	3.0 ± 1.0 3.0 ± 1.0	1	()				
Type L	L4EB0	10 ± 1.0 10 ± 1.0	4.5 ± 1.0	Bulk	e e				
Straight short lead	L05B0	10 ± 1.0	5.0± 1.0		<u>Ød</u>				
	D3EA7	7.5 ± 1.0	3.5 ± 0.5						
	D04A7	7.5 ± 1.0	4.0 ± 0.5	Bulk	Dmax. Tmax.				
Lead style: D	D3EA0	10 ± 1.0	3.5 ± 0.5	Duik					
	D04A0	10 ± 1.0	4.0 ± 0.5		4 m x x m x x m x x m x x m x x m x m x				
Type D	DAFD7				1 1 4				
• •	DAMD7	D . C	C	T	ød H				
Vertical kink lead	DAMD0	Refer to "4. T	aping format	Tap. Ammo					
	X3EA7	7.5 ± 1.0	3.5 ± 0.5						
	X04A7	7.5 ± 1.0	4.0 ± 0.5	1	Dmax. Tmax.				
Lead style: X	X05B7	7.5 ± 1.0	5.0 ± 1.0	Bulk	× - -				
	X3EA0	10 ± 1.0	3.5 ± 0.5	Duik	5.0max				
T V	X04A0	10 ± 1.0	4.0 ± 0.5						
Type X	X05B0	10 ± 1.0	5.0 ± 1.0						
Outside kink lead	XAFD7								
	XAMD7	Refer to "4. T	Taping format" Tap. Ammo		. Taping format" Tap. Ammo				
	XAMD0								

^{*} Lead diameter Φd: 0.55+/-0.05mm

^{*} Coating extension on leads): 3.0mmMax for straight lead style; Not exceed the kink for kink lead.



3. Part numbering/T.C/Capacitance/ Tolerance/Diameter: 3.1 Normal parts:

3.1 Normal parts:					Dimer	isions (ı	ınit: mm	1)
SAP Part. No.	T.C.	Capacitance	Tolerance	D	Т		F	
SAF Fait. No.	1.C.	Capacitance	Tolerance	(max)	(max)	Bulk type	Taping type	φd
CH*AC***C060*		2, 3,4, 5(pF)	±0.25pF	7.0				
CH*AC***D060*		6,7,8,9,10(pF)	±0.5pF	7.0				
CH*AC***J060*	CIT	12,15(pF)	±5%	7.0				
CH*AC***J070*	CH (NP0)	18,20,22, 24(pF)	±5%	8.0				
CH*AC***J080*		27,30,33,(pF)	±5%	9.0				
CH*AC***J090*		36,39(pF)	±5%	10.0				
CH*AC470J100*		47(pF)	±5%	11.0				
SL*AC***J060*		10,12,15,18,20,22,2 4,27,30,33, 36,39,47,50,51(pF)	±5%	7.0				
SL*AC***J070*	SL	56,62, 68,75(pF)	±5%	8.0			7.5±1	
SL*AC820J080*		82pF	±5%	9.0			(AFD7)	
SL*AC101J090*		100pF	±5%	10.0				
YP*AC101K060*		100 pF	±10%	7.0				
YP*AC151K060*		150 pF	±10%	7.0				
YP*AC221K060*		220 pF	±10%	7.0				
YP*AC331K060*		330 pF	±10%	7.0				
YP*AC471K060*	Y5P	470 pF	±10%	7.0				
YP*AC561K070*		560pF	±10%	8.0				
YP*AC681K070*		680 pF	±10%	8.0		7.5±1,		
YP*AC821K080*		820 pF	±10%	9.0	5.0	10±1		0.55+/-0.05
YP*AC102K080*		1000 pF	±10%	9.0				
YU*AC102M060*		1000 pF	±20%	7.0			7.5±1	
YU*AC152M080*		1500 pF	±20%	9.0			(AFD7) Or	
YU*AC222M080*		2200 pF	±20%	9.0			10±1	
YU*AC332M100*	Y5U	3300 pF	±20%	11.0			(AMD0)	
YU*AC392M120*		3900 pF	±20%	13.0			7.5±1 (AMD7) Or	
YU*AC472M120*		4700 pF	±20%	13.0			10±1 (AMD0)	
YV*AC102M060*		1000 pF	±20%	7.0				
YV*AC152M060*		1500 pF	±20%	7.0			7.5±1	
YV*AC222M060*		2200 pF	±20%	7.0			(AFD7) Or	
YV*AC332M080*		3300 pF	±20%	9.0			10±1	
YV*AC392M100*	Y5V	3900 pF	±20%	11.0			(AMD0)	
YV*AC472M100*	130	4700 pF	±20%	11.0	1			
YV*AC682M120*		6800 pF	±20%	13.0			7.5±1 (AMD7) Or	
YV*AC103M140*		10000 pF	±20%	15.0			10±1 (AMD0)	



3.2 Special design parts:

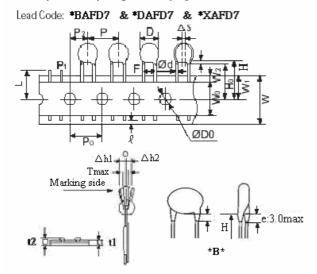
					Dimer	nsions (1	unit: mm	
SAP Part. No.	T.C.	Capacitance	Tolerance	D (max)	T (max)	Bulk type	F Taping type	φd
YP*AC101K06S*		100 pF	±10%	7.0				
YP*AC151K06S*		150 pF	±10%	7.0				
YP*AC221K06S*		220 pF	±10%	7.0			7.5±1	
YP*AC331K06S*	Y5P	330 pF	±10%	7.0			(AFD7)	
YP*AC471K07S*	131	470 pF	±10%	8.0			Or 10±1	
YP*AC561K08S*		560pF	±10%	9.0			(AMD0)	
YP*AC681K09S*		680 pF	±10%	10.0				
YP*AC102K10S*		1000 pF	±10%	11.0	5.0	7.5±1,		0.55+/-0.05
YU*AC102M07S*		1000 pF	±20%	8.0	3.0	10±1		0.5517-0.05
YU*AC152M08S*		1500 pF	±20%	9.0			7.5±1	
YU*AC222M09S*		2200 pF	±20%	10.0			(AFD7)	
YU*AC332M11S*	Y5U	3300 pF	±20%	12.0				
YU*AC392M12S*		3900 pF	±20%	13.0			7.5±1 (AMD7) Or	
YU*AC472M13S*		4700 pF	±20%	14.0			10±1 (AMD0)	

[•] The special parts only improve surge withstanding, but can't independently be used in protecting application against surge.

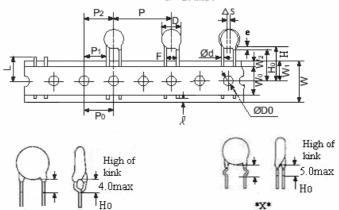


4. Taping Format

• 15mm pitch/lead spacing 7.5mm taping



- 25.4mm pitch/lead spacing 10.0mm taping
 Lead Code: *DAMD0 & *XAMD0 & *BAMD0
- 25.4mm pitch/lead spaceing 7.5mm taping Lead code: *DAMD7 & *XAMD7 & *BAMD7



POE Part Number	*BAFD7	*DAFD7 *XAFD7	*BAMD7 *DAMD7 *XAMD7	*BAMD0 *DAMD0 *XAMD0		
Item	Symbol	Dimensions (mm)	Dimensions (mm)	Dimensions (mm)	Dimensions (mm)	
Pitch of component	P	15.0	15.0	25.4	25.4	
Pitch of sprocket	P0	15.0±0.3	15.0±0.3	12.7±0.3	12.7±0.3	
Lead spacing	F	7.5±1.0	7.5±1.0	7.5±1.0	10.0±1.0	
Length from hole center to component center	P2	7.5±1.5	7.5±1.5	12.7±1.5	12.7 ± 1.5	
Length from hole center to lead	P1	3.75±1.0	3.75±1.0	8.95±1.0	7.7±1.5	
Body diameter	D	See the "3. Pa	art numbering/T.C	/Capacitance/ Tole	erance/Diameter"	
Deviation along tape, left or right	△S		0	±2.0		
Carrier tape width	W		18.0	+1/-0.5		
Position of sprocket hole	W1		9.	0±0.5		
Lead distance between the kink and center of sprocket hole	Н0		18.0+2.0/-0	18.0+2.0/-0 (For: *DAMD7 / *XAMD7)	18.0+2.0/-0 (For: *DAMD0 / *XAMD0)	
Lead distance between the bottom of body and the center of sprocket hole	Н	20.0+1.5/-1.0		20.0+1.5/-1.0 (For: *BAMD7)	20.0+1.5/-1.0 (For: *BAMD0)	
Length from the terminal of the lead wire to the edge of carrier tape	l	2.0min (Or t	the end of lead wire	may be inside the ho	ole-down tape.)	
Diameter of sprocket hole	D0		4.	.0±0.2		
Lead diameter	φd		0.5	5±0.05		
Total tape thickness	t1		0.	6±0.3		
Total thickness, tape and lead wire	t2		1.5	max.		
Deviation across tape	$\triangle h1/\triangle h2$		2.0) max.		
Portion to cut in case of defect	L	11.0 max.				
Hole-down tape width	W0	8.0 min				
Hole-down tape distortion	W2	1.5±1.5				
Coating extension on leads	e	3.0 max for stra	ight lead style; No	ot exceed the kink	leads for kink lead.	
Body thickness	T	See the "3. Pa	art numbering/T.C	/Capacitance/ Tole	erance/Diameter"	



5.Markii	ng:									
1.Type Des	signation	AC								
2.Nominal	Capacitance	Identifie	d by 3-F	Figure Code. E	Ex. 47p	F → "47",47	70pF → ′	'471"		
3.Capacitar	nce Tolerance	C:±0.25	pF,D:±0	.5pF,J:±5%,K	:±10%	,M:±20%				
4.Company	y Name Code(Trade mar	rk) K								
5. Products	s ID	Abbrevia	ntion ex							
6.Approved	d monogram:		I : _		T		T			
6.1 VDE	1 00 or (A 10)	6.3 CSA	(1)	6.5 NEMKO	N	6.7 FIMKO	FI	6.9 CQC	@	
6.2 UL	<i>FU</i>	6.4 SEMKO	(S)	6.6 DEMKO	(D)	6.8 SEV	(t S)			
	Туре	(for SAP	Two sides marking (for SAP part number 10-11 digits ≤ "07" products)					One side marking (for SAP part number 10-11 digits ≥ "08" products)		
Marking	0AC (X1:400Vac/ Y2:250Vac)	((() () () () () () () () ()	71K	(N) (S) (S) (S)	\ Y2:	UK AC472 (D) (O) (O) (O) (O) (O) (O) (O) (O) (O) (O) (\$ (\$)			
Ex.:	Туре	(for SAP p	Two sides marking (for SAP part number 10-11 digits ≤ "07" products)					One side marking (for SAP part number 10-11 digits ≥ "08" products)		
	1AC (X1:440Vac/ Y2:300Vac)	AC4	UK AC471K X1:440V~ Y2:300V~ 6 <u>C</u> 09876						300V~	
*The mark	(X1:440Vac/	AC4 x1:440V-	71K			_	(×	4	AC472N AC472N 440V~ Y2: 6 <u>C</u> 09876	

^{*&}quot;C", Marked with code "_" stand for Halogen and Pb free epoxy resin.



6. Scope

THIS SPECIFICATION APPLIES TO CERAMIC INSULATED CAPACITORS DISK TYPE USED IN ELECTRONIC EQUIPMENT.

1. VDE/SEV/SEMKO/FIMKO/NEMKO/DEMKO/ UL/CSA recognized capacitor for Antenna coupling and AC line-by-pass.X1, Y2 Capacitor based on IEC 60384-14 "UL, CSA recognized for across-the-line, line-by-pass" and antenna-isolation.

2. Approval Standard and Recognized No.

Safety Standard	Standard No.	Subclass	w.v.	Recognized No.	
UL	ANSI/UL	X1	400VAC or 440VAC	E146544	
UL	60384-14:2009	Y2	250VAC or 300VAC	E140544	
CSA	CAN/CSA	X1	400VAC or 440VAC	2347969	
CSA	E60384-14:2009	Y2	250VAC or 300VAC	2347909	
VDE	EN 60384-14:2013	X1	400VAC or 440VAC	40001920	
(ENEC)	IEC60384-14:2013	Y2	250VAC or 300VAC	40001829	
CEM	IECC0204 14-2012	X1	400VAC	14.0554	
SEV	IEC60384-14:2013	Y2	250VAC		
CEMIZO	EN 60204 14.2012	X1	400VAC	1411212	
SEMKO	EN 60384-14:2013	Y2	250VAC	1411212	
FIMKO	EN 60384-14:2013	X1	400VAC	NCS/FI 28679A1	
FINIKO	EN 00364-14.2013	Y2	250VAC	NCS/F1 200/9A1	
NEMKO	EN 60384-14:2013	X1	400VAC	P14219060	
NEMICO	EN 00364-14.2013	Y2	250VAC	F 14219000	
DEMKO	EN 60384-14:2013	X1	400VAC	D-03994 A1	
DEWIKO	EN 00364-14.2013	Y2	250VAC	D-03994 A1	
COC	GB/T 14472-1998	X1:40	0VAC /Y1:250VAC	CQC08001026519	
CQC	IEC60384-14 2005	X1: 44	0VAC /Y2:300VAC	CQC15001121984	
		X1	400VAC or 440VAC	SU03065-14001	
KTL	K60384-14 2006	Y2	250VAC	SU03065-14002	
		Y2	300VAC	SU03065-14003A	



7. Specification and test method

7.1 Operating Temperature Range:

-40 to +125°C

7.2 Test condition:

Test and measurement shall be made at the standard condition. (temperature $15 \sim 35^{\circ}$ C, relative humidity $45 \sim 75\%$ and atmospheric pressure $860 \sim 1060$ hpa). Unless otherwise specified herein.

If doubt occurred on the value of measurement, and measurement was requested by customer capacitors shall be measured at the reference condition. (temperature $20\pm2^{\circ}\text{C}$ or $25\pm2^{\circ}\text{C}$, relative humidity $60\sim70\%$ and atmospheric pressure $860\sim1060$ hpa.)

7.3 Performance:

	Item		Specification	Testing Method		
		The capacitors shall not be damage when AC2600V(rms.) are applied between the lead wires for 60 sec. (Charge/Discharge current ☐ 50mA.)				
1	Dielectric Strength	Body Insulation	No failure.	First the terminal of capacitor shall be connected together. Then a metal foil shall be closely wrapped around the body of the capacitor distance of about 3 to 4 mm from each terminal. Then the capacitor shall be inserted into a container filled with metal balls of about 1 mm diameter. Finally. AC2600V(rms.) is applied for 60 sec. between the capacitor lead wires and metal balls. (Charge/Discharge current 50mA.		
2	Insulation Resis	tance(I.R.)	10000MΩ min.	The insulation resistance shall be measured with 500±50VDC with 60±5sec. of charging.		
3	Capacitance		Within specified tolerance			
4	4 Dissipation Factor(D.F.) or Q		$\begin{array}{c c} Char. & Specification \\ Y5P, \\ Y5U & D.F \leqq 2.5\% \\ Y5V & D.F \leqq 5.0\% \\ \hline CH,SL & Q: \\ CH,SL & 30pF\&above: \geqq 1000\\ Below \\ 30PF: \geqq 400+20\times C \\ \end{array}$	B&E&F: The capacitance shall be measured at 20±2°Cwith 1kHz±20% and 5V(rms.) or less. CH&SL: The capacitance shall be measured at 25°C with 1MHz±20% and 1.0±0.2Vrms		
5	Temperature Characteristic Y5P W Y5U W Y5V W CH 0 SL P		Y5P Within $\pm 10\%$ Y5U Within $\pm \frac{20}{55}\%$ Y5V Within $-80 \sim +30\%$ CH 0 ± 60 ppm/°C $-1000 \sim +350$	The capacitance measurement shall be made at each step specified in table 1. (Table 1) Step Temperature 1 +20±2°C 2 -25±2°C 3 +20±2°C 4 +85±2°C 5 +20±2°C Pr-treatment: Capacitor shall be stored at 85 ± 2 °C for 1 hour. Then placed at room condition for $1(3)24\pm2$ hours before measurement		
6	Tensile Robustness of Termination		Lead wire shall not cut off capacitor shall not be broken	sec.		
<u>*</u>		Bending	Lead wire shall not cut off capacitor shall not be broken	W Each lead wire should be subjected to 5N of weight and bent 90° at the point of egress, in one direction, then returned to its original position and bent 90° in the opposite direction at the rate of one bend in 2 to 3 sec.		



	Item		Specification	Testing Method	
7	Solderability	Lead wire should be soldered with uniform coating on the axial direction over 3/4 of the circumferential direction.		The lead wire of capacitor should be dipped into mol solder for 5 ± 0.5 sec. The depth of immersion is up to about 1.5 to 2.0 mm fr the root of lead wires.	
		Amagaranga	No morked defeat	Temp. of solder : Lead free solder (Sn-3Ag -0.5 Cu) 245 ± 5 °C As shown in figure, the lead wires should be immersed in solder of	
		Appearance	No marked defect	350 ± 10 °C or 260 ± 5 °C up to 1.5 to 2.0mm from the root of	
		I.R.	1000MΩ min.	Terminal for 3.5 ± 0.5 sec (10 ± 1 sec for 260 ± 5 °C)	
		Dielectric Strength	Per Item 1.		
	Soldering Effect (Non-Preheat)	Capacitance	Y5P,Y5U,Y5V: Within ±10% SL,CH: Within±2.5% or ±0.25pF,Whichever is large.	Thermal Capacitor Screen 1.5 1.5 The Thermal Capacitor Screen 1.5 The Thermal Capacitor Solder 1.5 Pre-treatment: Capacitor shall be stored at 85±2°C for 1hour then placed at *1 room condition for 24±2 hours before initial measurements. Post-treatment: Capacitor shall be stored for 1 to 2 hours at *1 room condition.	
8		Appearance	No marked defect.	First the capacitor should be stored at $120 + 0 / -5$ °C for $60 + 0 /$ 5sec.	
		I.R.	1000MΩ min.	Then, as in figure , the lead wires should be immersed solder of $260 + / -5$ °C up to 1.5 to 2.0 mm from the root of terminal for 7.5 $+0/-1$ sec.	
	Soldering Effect (On-Preheat)	Dielectric Strength	Per Item 1.	Thermal Capacitor Screen 1.5 Thermal Capacitor Screen 1.5 Thermal Capacitor Screen 1.5 Thermal Capacitor Solder	
		Capacitance	Y5P,Y5U,Y5V: Within ±10% SL,CH: Within±2.5% or ±0.25pF,Whichever is large.	Pre-treatment: Capacitor shall be stored at 85±2°C for 1hour.then placed at **1room condition for 24±2hours before initial measurements. Post-treatment: Capacitor shall be stored for 1 to 2hours at **1room condition.	





POE-D11-02-E-18

CERAMIC DISC CAPACITOR SAFETY RECOGNIZED, AC SERIES

Ver : 18

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Item		Specification	Testing Method		
Humidity (Under Steady State)	Appearance Capacitance	No marked defect. Y5P: Within ±10% Y5U: Within ±20% Y5V: Within ±30% SL&CH: Within±2.5% or ±0.25pF,Whichever is large.	Set the capacitor for 500 ± 12 hours at $40\pm2^{\circ}\mathbb{C}$, in 90 to 95% humidity. Then capacitor shall be stored for 1 to 2 hours at room condition.		
	D.F.	Y5P,Y5U: 5.0% max. Y5V: 7.5% max.			
Humidity Loading	Q	SL&CH: Less than 30pF=> $Q \ge 100+10 \times C/3$ More than 30pF=> $Q \ge 200$	Apply the rated voltage for 500 ± 12 hours at $40\pm2^{\circ}$ C, in 90 to 95% humidity and set it for 1 to 2 hours at room condition.		
	I.R.	B,E,F: 3000MΩ min. SL&CH: 1000MΩ min.			
	Appearance	No marked defect.	Impulse Voltage:		
	Capacitance	Y5P,Y5U,Y5V: Within ±20% SL&CH: Within±3% or ±0.3pF,Whichever is large.	Each individual capacitor shall be subjected to a 5kv impulses for three times. After the capacitors are applied to life test. Fig. 100 (%) 90 Front time (T1) =1.2µs=1.67T Time to half-value (T2) =50µs		
	I.R.	3000MΩ min. SL&CH: 1000MΩ min.	30		
Life	Dielectric Strength	Per Item 1.	The specimen capacitors are placed in a circulating air oven for a period of 1000 hrs. The air in the oven is maintained at a temperature of $125\pm2^{\circ}\text{C}$. Throughout the test. The capacitors are subjected to an AC425Vrms.(for 2AC type) or AC510Vrms.(for 3AC type) alternating voltage of mains frequency. Except that once each hour the voltage id increased to 1000Vrms for 0.1sec.		
Flame Test			The capacitor shall subject to applied for 15 sec And then removed for 15 sec, until 5 cycles. Fig. Capacitor Flame Gas Burmer (Unit: mm)		
	Humidity (Under Steady State) Humidity Loading Life	Humidity (Under Steady State) D.F. Humidity Q I.R. Appearance Capacitance I.R. Appearance Capacitance The capacitor fi Cycle 1~4 5	$\begin{array}{c ccccccccccccccccccccccccccccccccccc$		



	Item	Specification	Testing Method
13	Active Flammability	The cheesecloth shall not be on fire.	The specimens shall be individually wrapped in at least one but more then two complete layers of cheesecloth. The specimens shall be subjected to 20 discharges. The interval between successive discharges shall be 5sec. The Uac shall be maintained for 2 min. after the last discharge. Fig. F L1 L2 C3 R F L1 L2 C3 R Oacilloscpoe C1,2: 1Mf±10% C3: 0.03Mf±5% 10KV L1-4: 1.5Mh±20% 16A Rod core choke R: 100Ω±2% Ct: 3Mf±5% 10KV Uac: Ur±5% Ur: Rated working voltage Cx: Capacitor F: Fuse, Rated 10A Ut: Voltage applied to Ct
14	Passive Flammability	The burning time shall not be exceeded the time 30 sec. The tissue paper shall not ignite.	The capacitor under test shall be held in the flame in the position, which best promotes burning. Each specimen shall only be exposed once to the flame. Time of exposure to flame: 30 sec Length of flame: 12±1 mm Gas burner: Length 35 mm min. Inside Dia.: 0.5±0.1 mm Outside Dia.: 0.9 mm max. Gas: Butane gas Purity 95% min. Fig. Test specimen Test specimen



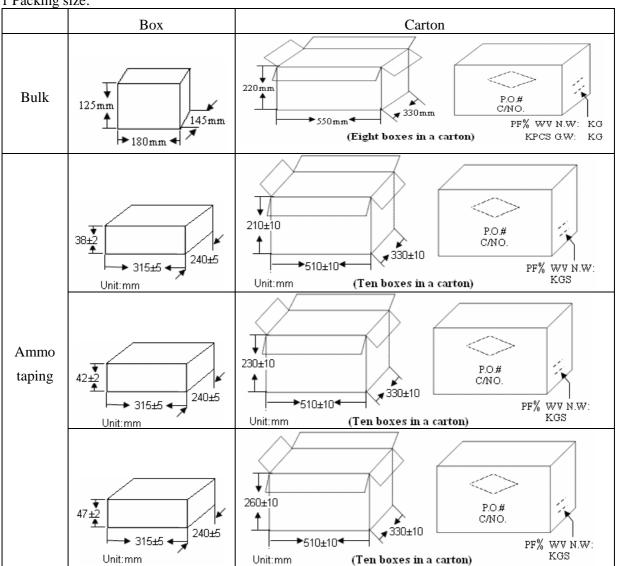
	Item Specification			Testing Method					
	Temperature	Appeara Char. SL, CH Y5P Y5U,	Cap. Change ≤±5% ≤±10%	No marked defect DF/Q $Q \ge 275+5/2C$ $(C < 30pF)$ $Q \ge 350$ $(C \ge 30pF)$ $DF \le 5.0\%$ $DF \le 7.5\%$	The capacito		nld be subjected to 5 nperature Cycle tim Temperature(°C) -40+0/-3 Room temp.	•	cycles,
15	Cycle			DF ≥ 7.5%		3	125+3/-0	30	
			I.R.	3000MΩ min.		4	Room temp.	3	
				Per Item 1	Pre-treatment: Capacitor shall be stored at 85±2°C for 1hour.then placed at *1 room condition for 24±2hours. Post-treatment: Capacitor shall be stored for 1 to 2hours at *1 room condition.				

 $[\]mbox{\%}$ "room condition" temperature \div 15~35°C , humidity \div 45~75% ,atmospheric pressure \div 86~106kPa



8. Packing specification:

8.1 Packing size:



8.2 Packing quantity:

3.2 1 acking	quantity.	
Packing ty	pe The code of 14th to15th in SAP P/N	MPQ(Kpcs/Box)
	AF	1
Taping	AM (The size code ≤ 11)	1
	AM (The size code \geq 12)	0.5

Packing type	Lead length	Size code of 10th to 11th in SAP P/N	MPQ (Kpcs/Bag)	Kpcs/Box
	Long lead	Long lead 06~12		1.5
	$(L \ge 20 \text{mm})$	13-15	0.5	1
Bulk	Short lead (L<20mm)	06~14	0.5	2
		15	0.2	1
		16	0.2	1



9. Notices:

9.1 Caution(Rating):

(1). Operating Voltage

Be sure to maintain the Vp-p value of the applied voltage or the Vo-p which contains DC bias within the rated voltage range.

When the voltage is started to apply to the circuit or it is stopped applying, the irregular voltage may be generated for a transit period because of resonance or switching. Be sure to use a capacitor within rated voltage containing this irregular voltage.

Voltage	DC Voltage	DC+AC Voltage	AC Voltage	Pulse Voltage (1)	Pulse Voltage (2)
Positional Measurement	Vo-p	V0-p	Vp-p	Vp-p	Vp-p

(2). Operating Temperature and Self-generated Heat

Keep the surface temperature of a capacitor below the upper limit of its rated operating temperature range. Be sure to take into account the heat generated by the capacitor itself. When the capacitor is used in a high-frequency current, pulse current or the like, it may have the self-generated heat due to dielectric-loss. Applied voltage should be the load such as self-generated heat is within 20°C on the condition of atmosphere temperature 25°C . When measuring, use a thermocouple of small thermal capacity-K of $\phi 0.1 \text{mm}$ and be in the condition where capacitor is not affected by radiant heat of other components and wind of surroundings. Excessive heat my lead to deterioration of the capacitor's characteristics and reliability.

(3). Test condition for withstanding Voltage

I. Test Equipment

Test equipment for AC withstanding voltage shall be used with the performance of the wave similar to 50/60 Hz sine waves.

If the distorted sine wave or over load exceeding the specified voltage value is applied, the defective may be caused.



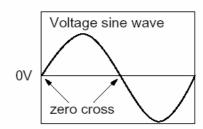
II. Voltage Applied Method

When the withstanding voltage is applied, capacitor's lead or terminal shall be firmly connected to the output of the withstanding voltage test equipment, and then the voltage shall be raised from near zero to the test voltage.

If the test voltage without the raise from near zero voltage would be applied directly to capacitor, test voltage should be applied with the *zero cross. At the end of the test time, the test voltage shall be reduced to near zero, and then capacitor's lead or terminal shall be taken off the output of the withstanding voltage test equipment.

If the test voltage without the raise from near zero voltage would be applied directly to capacitor, the surge voltage may arise, and therefore, the defective may be caused.

ZERO CROSS is the point where voltage sine wave pass 0V.- See the right figure.



(4). Fail-Safe

When capacitor would be broken, failure may result in a short circuit. Be sure to provide an appropriate fail-safe function like a fuse on your product if failure would follow an electric shock, fire or fume.

Failure to follow the above cautions may result, worst case, in a short circuit and cause fuming or partial dispersion when the product is used.

9.2 Caution (Storage and operating condition):

Operating and storage environment

The insulating coating of capacitors does not form a perfect seal; therefore, do not use or store capacitors in a corrosive atmosphere, especially where chloride gas, sulfide gas, acid, alkali, salt or the like are present. And avoid exposure to moisture. Before cleaning, bonding, or molding this product, verify that these processes do not affect product quality by testing the performance of a cleaned, bonded or molded product in the intended equipment. Store the capacitors where the temperature and relative humidity do not exceed –10 to 40 degrees centigrade and 15 to 85 %. Use capacitors within 6 months.

"Failure to follow the above cautions may result, worst case, in a short circuit and cause fuming or partial dispersion when the product is used."

9.3 Caution (Soldering and Mounting):

9.3.1 Vibration and impact:

Do not expose a capacitor or its leads to excessive shock or vibration during use.



9.3.2 Soldering:

When soldering this product to a PCB/PWB, do not exceed the solder heat resistance specification of the capacitor. Subjecting this product to excessive heating could melt the internal junction solder and may result in thermal shocks that can crack the ceramic element.

When soldering capacitor with a soldering iron, it should be performed in following conditions.

Temperature of iron-tip: 400 degrees C. max.

Soldering iron wattage: 50W max.

Soldering time: 3.5 sec. max.

9.3.3 Cleaning (ultrasonic cleaning):

To perform ultrasonic cleaning, observe the following conditions.

Rinse bath capacity: Output of 20 watts per liter or less.

Rinsing time: 5 min maximum.

Do not vibrate the PCB/PWB directly.

Excessive ultrasonic cleaning may lead to fatigue destruction of the lead wires.

"Failure to follow the above cautions may result, worst case, in a short circuit and cause fuming or partial dispersion when the product is used."

9.4 Caution (Handling):

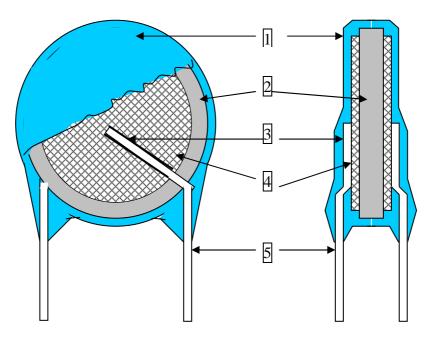
Vibration and impact

Do not expose a capacitor or its leads to excessive shock or vibration during use.

"Failure to follow the above cautions may result, worst case, in a short circuit and cause fuming or partial dispersion when the product is used."



10. Drawing of internal structure and material list:



Remarks:

No.	Part name	Material	Model/Type	Component
1			1.EF-150	Epoxy resin, Pigment
1	Insulation Coating	Epoxy polymer	2.PCE-300	(Blue / UL 94 V-0)
2	Dielectric Element	Ceramic	CH/SL/Y5P/Y5U/Y5V	BaTiO ₃
3	Solder	Tin-silver	Sn96.5-Ag3-Cu0.5	Sn96.5-Ag3-Cu0.5
4	Electrodes	Ag	1.SP-160PL 2.SP-260PL	Silver · Glass frit
_	T and a serior	Tinned copper clad	0.55±0.05 mm	Substrate metal: Fe & Cu
5	Leads wire	steel wire	0.55±0.05 IIIII	Surface plating: Sn 100%(3~7μm)

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0603B563J500CT WLPN303015M470PB 1206B683K201 WR25X361JTL WR25X1R8JTL YP1AH471K070BAMD0H 1206B473K251CT

WK12V155 JTL 0603N8R0D500CT 1206B184K101CT SH32B225K101CT RFCBA100607SA6B701 0603N510J500CT 1812N680G202CT

0805N152J201CT WLPN303015M560PB