N-channel TrenchMOS standard level FET

11 September 2012

Product data sheet

1. Product profile

1.1 General description

Standard level N-channel MOSFET in a SOT78 package using TrenchMOS technology. This product has been designed and qualified to AEC Q101 standard for use in high performance automotive applications.

1.2 Features and benefits

- AEC Q101 compliant
- Repetitive avalanche rated
- Suitable for thermally demanding environments due to 175 °C rating
- True standard level gate with VGS(th) rating of greater than 1V at 175 °C

1.3 Applications

- 12 V Automotive systems
- Motors, lamps and solenoid control
- Start-Stop micro-hybrid applications
- Transmission control
- Ultra high performance power switching

1.4 Quick reference data

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Table 1. Qu	ick reference data					
Symbol	Parameter	Conditions	Min	Тур	Мах	Unit
V _{DS}	drain-source voltage	T _j ≥ 25 °C; T _j ≤ 175 °C	-	-	60	V
I _D	drain current	V _{GS} = 10 V; T _{mb} = 25 °C; <u>Fig. 1</u>	-	-	58	А
P _{tot}	total power dissipation	T _{mb} = 25 °C; <u>Fig. 2</u>	-	-	96	W
Static charact	teristics					
R _{DSon}	drain-source on-state resistance	V _{GS} = 10 V; I _D = 15 A; T _j = 25 °C; <u>Fig. 11</u>	-	9.6	13	mΩ
Dynamic cha	racteristics	·				
Q _{GD}	gate-drain charge	I _D = 15 A; V _{DS} = 48 V; V _{GS} = 10 V; Fig. 13; Fig. 14	-	6.9	-	nC





N-channel TrenchMOS standard level FET

2. Pinning information

Table 2.	Pinning	information		
Pin	Symbol	Description	Simplified outline	Graphic symbol
1	G	gate	mb	D
2	D	drain		
3	S	source		G C C C C C C C C C C C C C C C C C C C
mb	D	mounting base; connected to drain		mbb076 S
			TO-220AB (SOT78A)	

3. Ordering information

Table 3. Ordering inf	formation		
Type number	Package		
	Name	Description	Version
BUK7514-60E	TO-220AB	plastic single-ended package; heatsink mounted; 1 mounting hole; 3-lead TO-220AB	SOT78A

4. Limiting values

Table 4. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions	Min	Мах	Unit
V _{DS}	drain-source voltage	T _j ≥ 25 °C; T _j ≤ 175 °C	-	60	V
V _{DGR}	drain-gate voltage	R _{GS} = 20 kΩ	-	60	V
V _{GS}	gate-source voltage	T _j ≤ 175 °C; DC	-20	20	V
I _D	drain current	T _{mb} = 25 °C; V _{GS} = 10 V; <u>Fig. 1</u>	-	58	А
		T _{mb} = 100 °C; V _{GS} = 10 V; <u>Fig. 1</u>	-	41	А
I _{DM}	peak drain current	T_{mb} = 25 °C; pulsed; $t_p \le 10 \ \mu$ s; Fig. 4	-	234	Α
P _{tot}	total power dissipation	T _{mb} = 25 °C; <u>Fig. 2</u>	-	96	W
T _{stg}	storage temperature		-55	175	°C
Tj	junction temperature		-55	175	°C
Source-dra	in diode		I		
I _S	source current	T _{mb} = 25 °C	-	58	Α
I _{SM}	peak source current	pulsed; $t_p \le 10 \ \mu s$; $T_{mb} = 25 \ ^{\circ}C$	-	234	А
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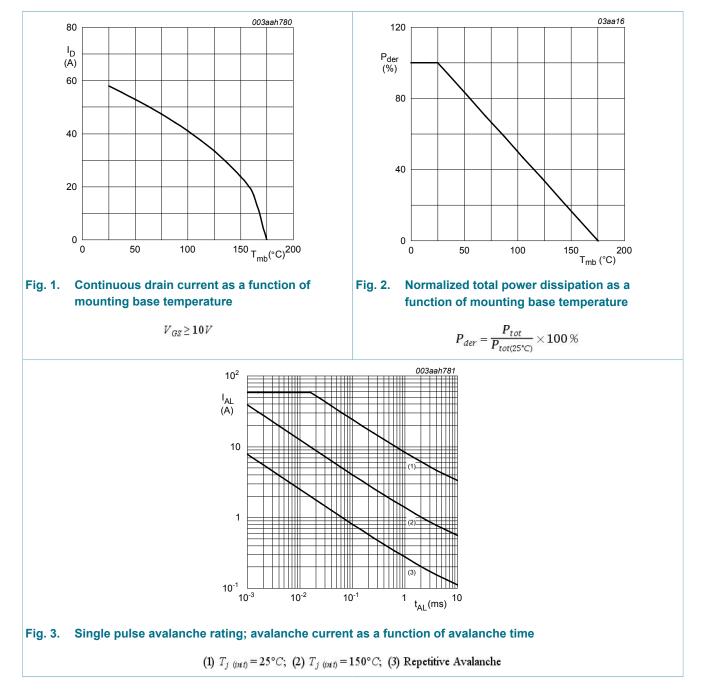
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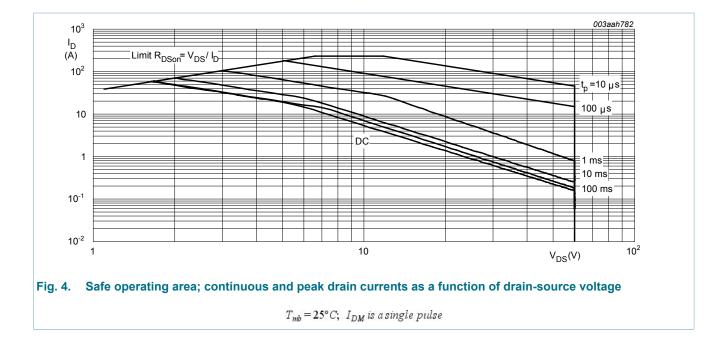
Symbol	Parameter	Conditions		Min	Max	Unit
Avalanche rug	ggedness					
E _{DS(AL)S}	non-repetitive drain-source avalanche energy	$ I_D = 58 \text{ A}; V_{sup} \le 60 \text{ V}; \text{ R}_{GS} = 50 \Omega; V_{GS} = 60 \text{ V}; \text{ T}_{j(init)} = 25 \text{ °C}; \text{ unclamped}; Fig. 3 $	[1][2]	-	37	mJ

Single-pulse avalanche rating limited by maximum junction temperature of 175 °C.
 Refer to application note AN10273 for further information.



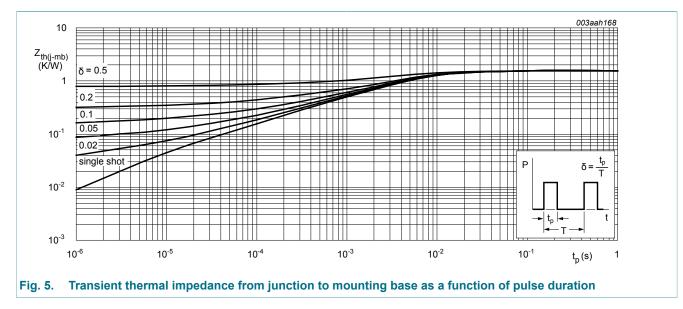
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5. Thermal characteristics

Table 5. The	rmal characteristics					
Symbol	Parameter	Conditions	Min	Тур	Max	Unit
R _{th(j-mb)}	thermal resistance from junction to mounting base	<u>Fig. 5</u>	-	-	1.56	K/W
R _{th(j-a)}	thermal resistance from junction to ambient	vertical in still air	-	60	-	K/W



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6. Characteristics

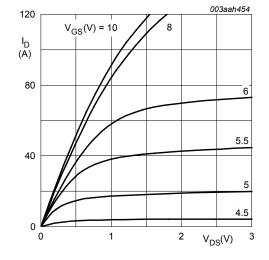
Symbol	Parameter	Conditions	Min	Тур	Мах	Unit
Static char	acteristics	· · · · ·				_
V _{(BR)DSS}	drain-source	I_D = 250 µA; V_{GS} = 0 V; T_j = 25 °C	60	-	-	V
	breakdown voltage	I_D = 250 µA; V_{GS} = 0 V; T_j = -55 °C	54	-	-	V
$V_{GS(th)}$	gate-source threshold voltage	I _D = 1 mA; V _{DS} = V _{GS} ; T _j = 25 °C; Fig. 9; Fig. 10	2.4	3	4	V
		I_D = 1 mA; V_{DS} = V_{GS} ; T_j = 175 °C; Fig. 9	1	-	-	V
		I_D = 1 mA; V_{DS} = V_{GS} ; T_j = -55 °C; Fig. 9	-	-	4.5	V
I _{DSS}	drain leakage current	V_{DS} = 60 V; V_{GS} = 0 V; T_j = 25 °C	-	0.025	1	μA
		V _{DS} = 60 V; V _{GS} = 0 V; T _j = 175 °C	-	-	500	μA
I _{GSS}	gate leakage current	V_{GS} = 20 V; V_{DS} = 0 V; T_j = 25 °C	-	2	100	nA
		V_{GS} = -20 V; V_{DS} = 0 V; T_j = 25 °C	-	2	100	nA
R _{DSon}	drain-source on-state resistance	V _{GS} = 10 V; I _D = 15 A; T _j = 25 °C; Fig. 11	-	9.6	13	mΩ
		V _{GS} = 10 V; I _D = 15 A; T _j = 175 °C; Fig. 11; Fig. 12	-	-	28.2	mΩ
Dynamic cl	haracteristics	· · · · · · · · · · · · · · · · · · ·				
Q _{G(tot)}	total gate charge	I _D = 15 A; V _{DS} = 48 V; V _{GS} = 10 V;	-	22.9	-	nC
Q _{GS}	gate-source charge	Fig. 13; Fig. 14	-	5	-	nC
Q _{GD}	gate-drain charge		-	6.9	-	nC
C _{iss}	input capacitance	V _{GS} = 0 V; V _{DS} = 25 V; f = 1 MHz;	-	1298	1730	pF
C _{oss}	output capacitance	T _j = 25 °C; <u>Fig. 15</u>	-	197	237	pF
C _{rss}	reverse transfer capacitance		-	122	162	pF
t _{d(on)}	turn-on delay time	V_{DS} = 45 V; R_L = 3 Ω ; V_{GS} = 10 V;	-	10.8	-	ns
t _r	rise time	$R_{G(ext)} = 5 \Omega$	-	9.2	-	ns
t _{d(off)}	turn-off delay time		-	21.9	-	ns
t _f	fall time		-	9.8	-	ns
L _D	internal drain inductance	from upper edge of mounting base to centre of die ; T_j = 25 °C	-	2.5	-	nH
		T_j = 25 °C; from drain lead 6mm from package to centre of die	-	4.5	-	nH
L _S	internal source inductance	measured from source lead to source bond pad ; T _i = 25 °C	-	7.5	-	nH

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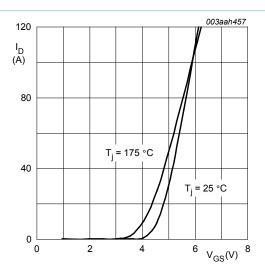
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Symbol	Parameter	Conditions	Min	Тур	Мах	Unit
Source-drain o	liode					
V _{SD}	source-drain voltage	I_{S} = 20 A; V_{GS} = 0 V; T_{j} = 25 °C; <u>Fig. 16</u>	-	0.84	1.2	V
t _{rr}	reverse recovery time	$I_{\rm S}$ = 15 A; dI_{\rm S}/dt = -100 A/µs; V_{\rm GS} = 0 V;	-	21.3	-	ns
Q _r	recovered charge	V _{DS} = 25 V	-	18.1	-	nC



T_j = 25 °C; t_p = 300 μs







 $V_{DS} = 10V$

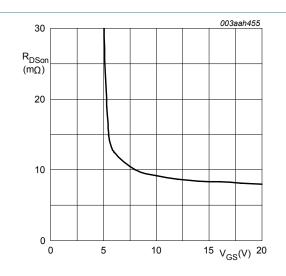
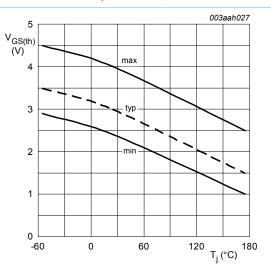


Fig. 7. Drain-source on-state resistance as a function of gate-source voltage; typical values

 $T_j = 25^{\circ}C; I_D = 15A$



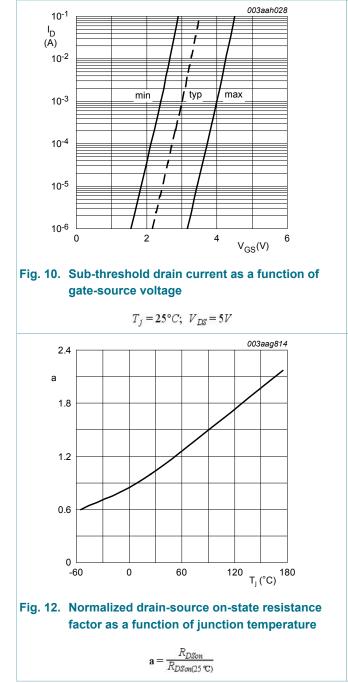


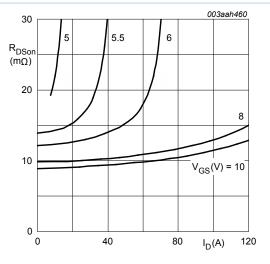
 $I_D = 1 \text{ mA}; V_{DS} = V_{GS}$

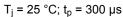
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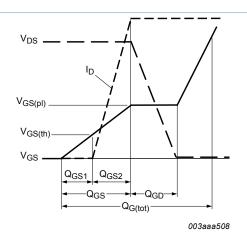
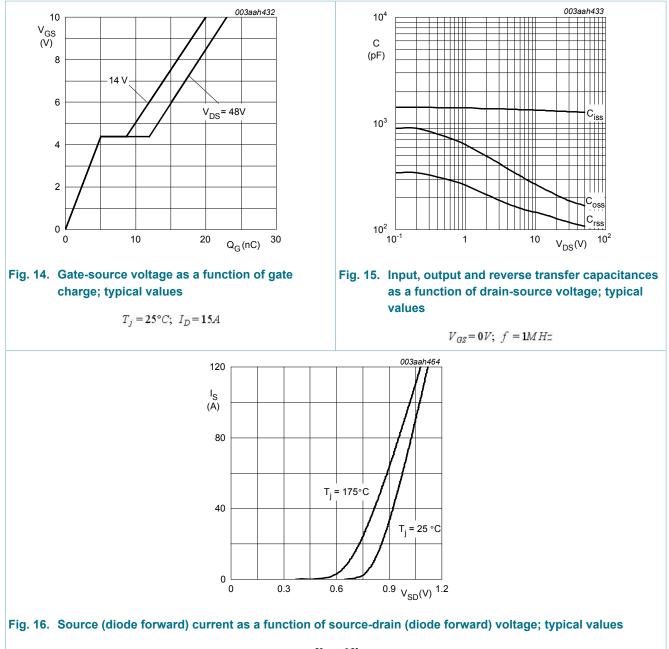


Fig. 13. Gate charge waveform definitions

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 $V_{GS} = \mathbf{0} V$

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7. Package outline

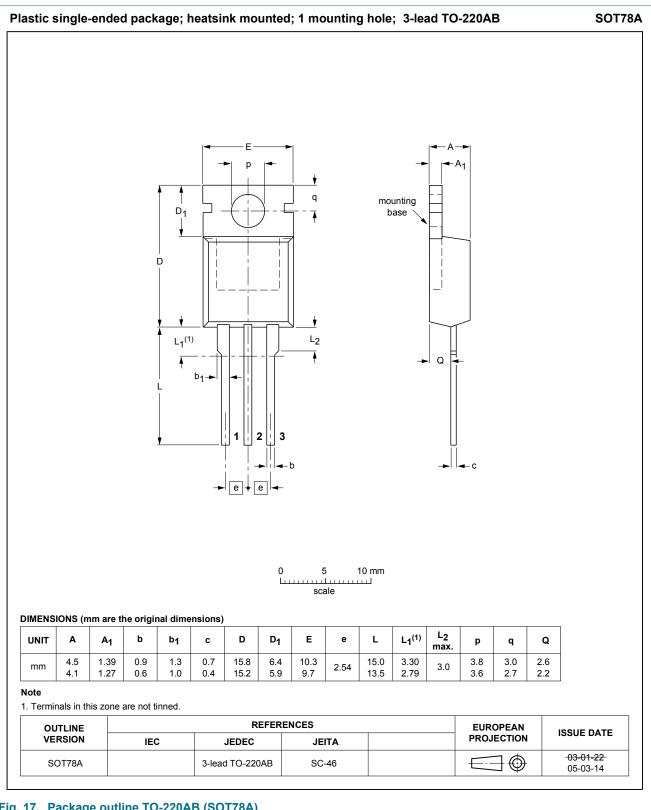


Fig. 17. Package outline TO-220AB (SOT78A) BUK7514-60E All information provided in this document is subject to legal disclaimers.

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N-channel TrenchMOS standard level FET

9. Contents

1	Product profile	1
1.1	General description	1
1.2	Features and benefits	1
1.3	Applications	1
1.4	Quick reference data	1
2	Pinning information	2
3	Ordering information	2
4	Limiting values	2
5	Thermal characteristics	4
5 6	Thermal characteristics Characteristics	
•		5
6	Characteristics	5 9
6 7	Characteristics Package outline	5
6 7 8	Characteristics Package outline Legal information	5 9
6 7 8 8.1	Characteristics Package outline Legal information Data sheet status	

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