PNP -100mA -50V Digital Transistors (Bias Resistor Built-in Transistors)

Datasheet

Parameter	Value
V _{CC}	-50V
I _{C(MAX.)}	-100mA
R ₁	1kΩ
R ₂	10kΩ

Features

- 1) Built-In Biasing Resistors
- 2) Built-in bias resistors enable the configuration of an inverter circuit without connecting external input resistors (see inner circuit).
- 3) The bias resistors consist of thin-film resistors with complete isolation to allow negative biasing of the input. They also have the advantage of completely eliminating parasitic effects.
- 4) Only the on/off conditions need to be set for operation, making the circuit design easy.
- 5) Complementary NPN Types: DTC113ZUA/ DTC113ZKA
- 6) Lead Free/RoHS Compliant.

● Outline EMT3



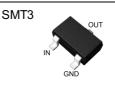
DTA113ZE

SOT-416(SC-75A)

D7

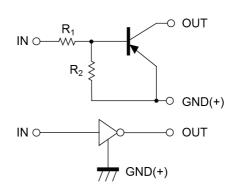
UMT3

DTA113ZUA SOT-323(SC-70)



DTA113ZKA SOT-346(SC-59)

•Inner circuit



Application

Switching circuit, Inverter circuit, Interface circuit, Driver circuit

Packaging specifications

Part No.	Package	Package size	Taping code	Reel size (mm)	Tape width (mm)	Basic ordering unit.(pcs)	Marking
DTA113ZE	EMT3	1616	TL	180	8	3000	E11
DTA113ZUA	UMT3	2021	T106	180	8	3000	111
DTA113ZKA	SMT3	2928	T146	180	8	3000	E11

● Absolute maximum ratings (T_a = 25°C)

Parameter			Values	Unit
Supply voltage			-50	V
Input voltage			-10 to 5	V
Output current			-100	mA
Collector current			-100	mA
	DTA113ZE		150	
Power dissipation	DTA113ZUA	P _D *2	200	mW
	DTA113ZKA		200	
Junction temperature		Tj	150	°C
Range of storage temperature		T _{stg}	-55 to +150	°C

● Electrical characteristics (T_a = 25°C)

Downwater	Cymah ol	Canditions	Values			Lloit	
Parameter	Symbol Conditions -		Min.	Тур.	Max.	Unit	
langut valtaga	$V_{l(off)}$	$V_{CC} = -5V, I_{O} = -100\mu A$	-	-	-0.3	V	
Input voltage	V _{I(on)}	$V_O = -0.3V$, $I_O = -20$ mA	-3	-	-		
Output voltage	V _{O(on)}	$I_{O}/I_{I} = -10 \text{mA} / -0.5 \text{mA}$	-	-0.1	-0.3	V	
Input current	I _I	V _I = -5V	-	-	-7.2	mA	
Output current	I _{O(off)}	$V_{CC} = -50V, V_{I} = 0V$	-	-	-0.5	μA	
DC current gain	G _I	$V_{O} = -5V, I_{O} = -5mA$	33	-	ı	-	
Input resistance	R ₁	-	0.7	1	1.3	kΩ	
Resistance ratio	R ₂ /R ₁	-	8	10	12	-	
Transition frequency	f _T *1	V _{CE} = -10V, I _E = 5mA, f = 100MHz	-	250	-	MHz	

^{*1} Characteristics of built-in transistor

^{*2} Each terminal mounted on a reference footprint

● Electrical characteristic curves (T_a =25°C)

Fig.1 Input voltage vs. output current (ON characteristics)

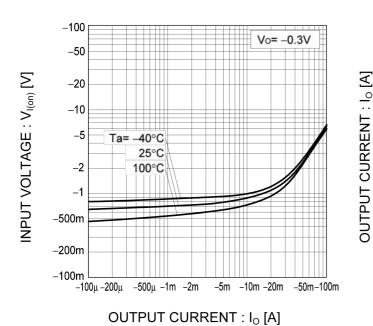
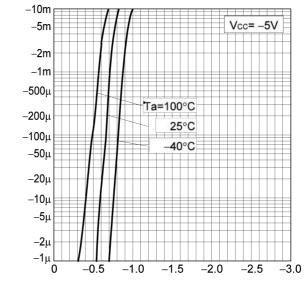


Fig.2 Output current vs. input voltage (OFF characteristics)



INPUT VOLTAGE: V_{I(off)} [V]

Fig.3 Output current vs. output voltage

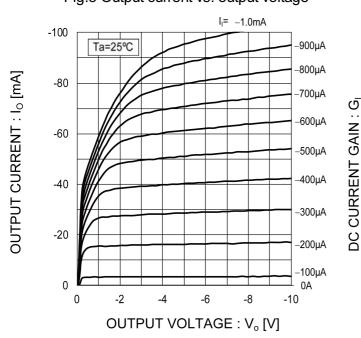
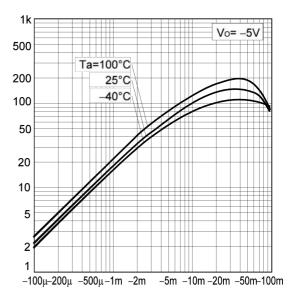


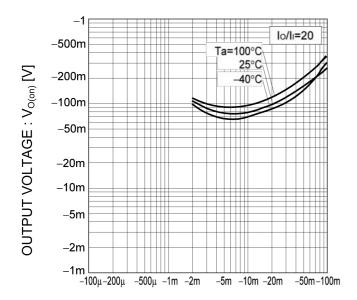
Fig.4 DC current gain vs. output current



OUTPUT CURRENT: Io [A]

● Electrical characteristic curves (T_a =25°C)

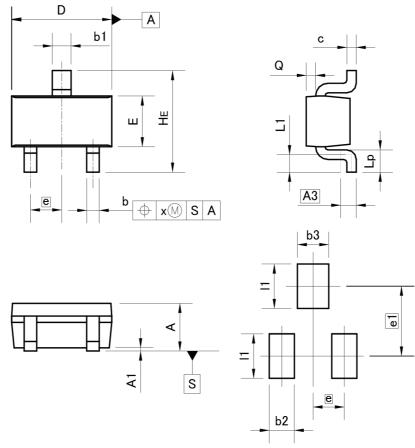
Fig.5 Output voltage vs. output current



OUTPUT CURRENT : Io [A]

Dimensions

EMT3



Pattern of terminal position areas [Not a recommended pattern of soldering pads]

DIM	MILIM	ETERS	INCHES	
DIM	MIN	MAX	MIN	MAX
Α	0.60	0.80	0.024	0.031
A1	0.00	0.10	0.000	0.004
A3	0.	25	0.0	10
b	0.15	0.30	0.006	0.012
b1	0.25	0.40	0.010	0.016
С	0.10	0.20	0.004	0.008
D	1.50	1.70	0.059	0.067
E	0.70	0.90	0.028	0.035
е	0.	50	0.020	
HE	1.40	1.80	0.055	0.071
L1	0.10	 :	0.004	
Lp	0.15	55 3:	0.006	TT.
Q	0.05	0.25	0.002	0.010
x	— 1	0.10	_	0.004

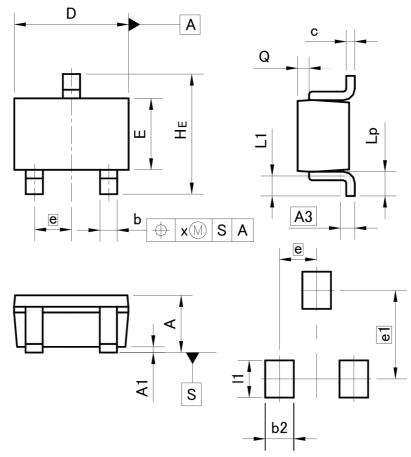
DIM	MILIMETERS		INCHES	
	MIN	MAX	MIN	MAX
b2	77.L	0.40	-32	0.016
b3		0.50	÷.	0.020
e1	1.	10	0.0	043
11	49	0.70	-	0.028

Dimension in mm/inches



Dimensions

UMT3



Pattern of terminal position areas [Not a recommended pattern of soldering pads]

DIM	MILIM	ETERS	INC	HES
DIM	MIN	MAX	MIN	MAX
Α	0.80	1.00	0.031	0.039
A1	0.00	0.10	0.000	0.004
A3	0.3	25	0.0	10
b	0.15	0.30	0.006	0.012
С	0.10	0.20	0.004	0.008
D	1.90	2.10	0.075	0.083
E	1.15	1.35	0.045	0.053
е	0.0	65	0.0	26
HE	2.00	2.20	0.079	0.087
L1	0.20	0.50	0.008	0.020
Lp	0.25	0.55	0.010	0.022
Q	0.10	0.30	0.004	0.012
х	=	0.10	=	0.004

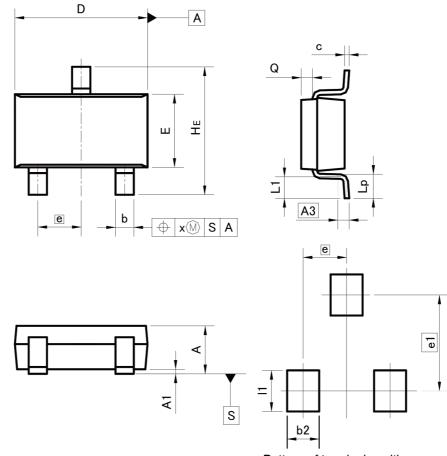
DIM	MILIMETERS		INCHES	
DIM	MIN	MAX	MIN	MAX
b2	_	0.50	_	0.020
e1	1.5	1.55		061
11	_	0.65	_	0.026

Dimension in mm/inches



Dimensions

SMT3



Pattern of terminal position areas [Not a recommended pattern of soldering pads]

DIM	MILIM	ETERS	INCHES	
DIM	MIN	MAX	MIN	MAX
Α	1.00	1.30	0.039	0.051
A1	0.00	0.10	0.000	0.004
A3	0.:	25	0.0	10
b	0.35	0.50	0.014	0.020
С	0.09	0.25	0.004	0.010
D	2.80	3.00	0.110	0.118
E	1.50	1.80	0.059	0.071
е	0.	95	0.037	
HE	2.60	3.00	0.102	0.118
L1	0.30	0.60	0.012	0.024
Lp	0.40	0.70	0.016	0.028
Q	0.20	0.30	0.008	0.012
x	2	0.10		0.004
у	Ψ'	0.10	_	0.004
5 114	MILIMETERS		INC	HES
DIM	MIN	MAX	MIN	MAX
b2	=	0.60	_	0.024
e1	2 10		0.0	183

Dimension in mm/inches

11



0.035

0.90

Notes

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