







**Pin Definition:** 

1. Gate 2. Source

3. Drain

PRODUCT SUMMARY				
	_			

V <sub>DS</sub> (V)	R <sub>DS(on)</sub> (mΩ)	I <sub>D</sub> (A)
-20	55 @ V <sub>GS</sub> =-4.5V	-3.2
	80 @ V <sub>GS</sub> =-2.5V	-2.7
	130 @ V <sub>GS</sub> =-1.8V	-2.0

### **Features**

- Advance Trench Process Technology •
- High Density Cell Design for Ultra Low On-resistance •

### **Application**

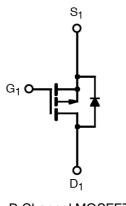
- **Battery Management** •
- High Speed Switch •

### **Ordering Information**

Part No.	Package Packing	
TSM2305CX RFG	SOT-23	3Kpcs / 7" Reel

Note: "G" denotes Halogen Free Product.

### **Block Diagram**



P-Channel MOSFET

Absolute Maximum Rating (T <sub>A</sub> =25°C unless otherwise noted)						
Parameter	Symbol	Limit	Unit			
Drain-Source Voltage		V <sub>DS</sub>	-20	V		
Gate-Source Voltage		V <sub>GS</sub>	±8	V		
Continuous Drain Current		I <sub>D</sub>	-3.2	А		
Pulsed Drain Current		I <sub>DM</sub>	-10	А		
Continuous Source Current (Diode Conduction) <sup>a,b</sup>		I <sub>S</sub>	-1	А		
	T <sub>A</sub> =25°C		1.25	W		
Maximum Power Dissipation	T <sub>A</sub> =75°C	P <sub>D</sub>	0.8			
Operating Junction Temperature		TJ	+150	°C		
Operating Junction and Storage Temperature Range		T <sub>J</sub> , T <sub>STG</sub>	-55 to +150	°C		

### **Thermal Performance**

Parameter	Symbol	Limit	Unit
Junction to Case Thermal Resistance	Rθ <sub>JC</sub>	80	°C/W
Junction to Ambient Thermal Resistance (PCB mounted)	RƏ <sub>JA</sub>	150	°C/W

#### Notes:

a. Pulse width limited by the Maximum junction temperature

b. Surface Mounted on a 1 in<sup>2</sup> pad of 2oz Cu, t  $\leq$  10 sec.



#### Electrical Specifications (Ta = 25°C unless otherwise noted)

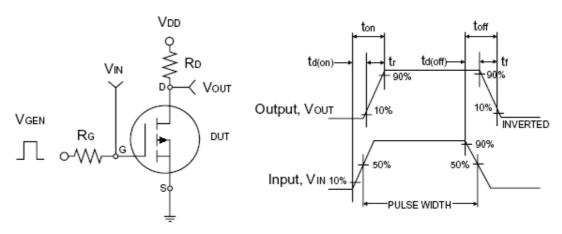
Parameter	Conditions Symbol		Min	Тур	Max	Unit	
Static							
Drain-Source Breakdown Voltage	$V_{GS} = 0V, I_D = 250 \mu A$	$BV_{DSS}$	-20			V	
Gate Threshold Voltage	$V_{DS} = V_{GS}, I_{D} = 250 \mu A$	$V_{GS(TH)}$	-0.45	-0.7	-1	V	
Gate Body Leakage	$V_{GS} = \pm 8V, V_{DS} = 0V$	I <sub>GSS</sub>			±100	nA	
Zero Gate Voltage Drain Current	$V_{DS} = -16V, V_{GS} = 0V$	I <sub>DSS</sub>			1.0	μA	
	$V_{GS}$ = -4.5V, $I_{D}$ = -3.2A			44	55		
Drain-Source On-State Resistance	$V_{GS}$ = -2.5V, $I_{D}$ = -2.7A	R <sub>DS(ON)</sub>		65	80	mΩ	
	$V_{GS} = -1.8V, I_{D} = -2.0A$			90	130	1	
Diode Forward Voltage $I_{s} = -1A, V_{Gs} = 0V$		$V_{SD}$		-0.8	-1.3	V	
Dynamic <sup>ь</sup>	-			-	-		
Gate Resistance	$V_{GS} = V_{DS} = 0V$ , f=1MHz	$R_{g}$		10		Ω	
Total Gate Charge	V 10V I 2.24	$Q_g$		10	20		
Gate-Source Charge	$V_{DS} = -10V, I_D = -3.2A,$	$Q_{gs}$		0.7		nC	
Gate-Drain Charge	V <sub>GS</sub> = -4.5V	$Q_{gd}$		4			
Input Capacitance		C <sub>iss</sub>		990			
Output Capacitance	$V_{DS} = -10V, V_{GS} = 0V,$	C <sub>oss</sub>		125		pF	
Reverse Transfer Capacitance	f = 1.0MHz	C <sub>rss</sub>		100			
Switching <sup>b.c</sup>							
Turn-On Delay Time		t <sub>d(on)</sub>		12	24		
Turn-On Rise Time	$V_{DD} = -10V, R_L = 15\Omega,$	t <sub>r</sub>		23	50		
Turn-Off Delay Time	$I_D = -1A, V_{GEN} = -4.5V,$	t <sub>d(off)</sub>		50	100	nS	
Turn-Off Fall Time	$R_{G} = 6\Omega$	t <sub>f</sub>		18	35		

#### Notes:

a. pulse test: PW  $\leq$  300µS, duty cycle  $\leq$  2%

b. For DESIGN AID ONLY, not subject to production testing.

c. Switching time is essentially independent of operating temperature.

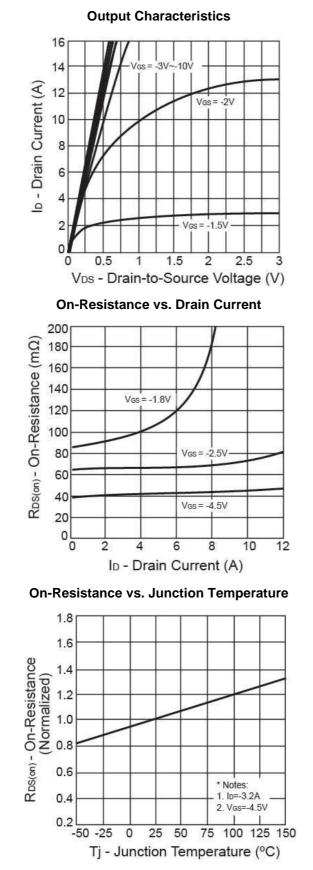


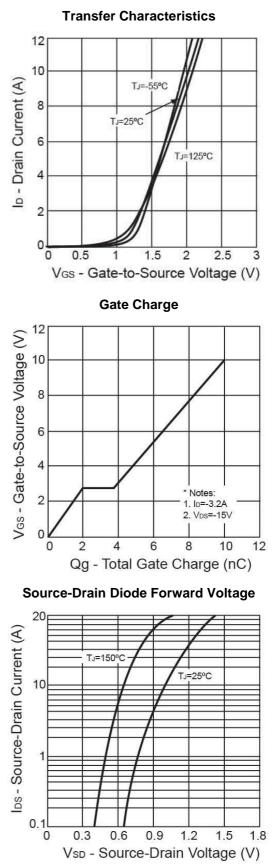
Switching Test Circuit

Switchin Waveforms



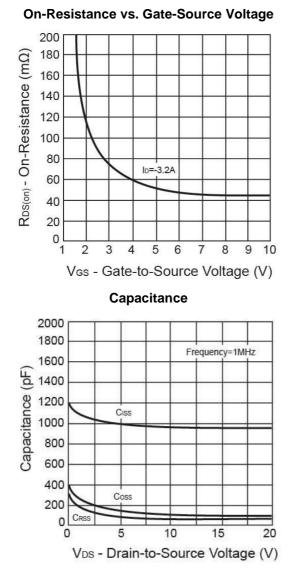
### Electrical Characteristics Curve (Ta = 25°C, unless otherwise noted)

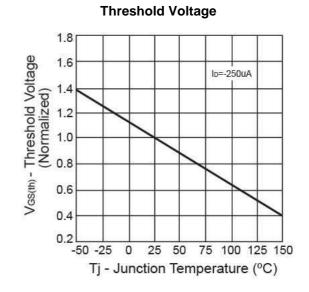




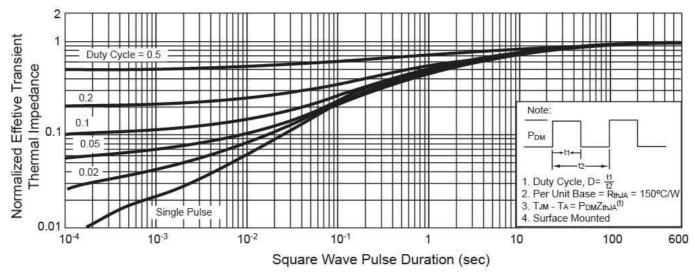


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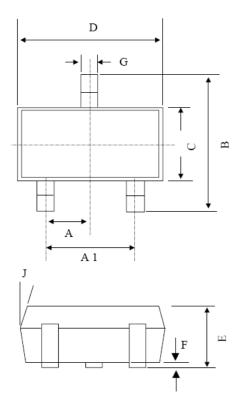


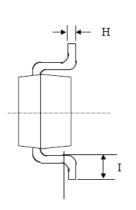
#### Normalized Thermal Transient Impedance, Junction-to-Ambient





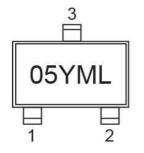
## SOT-23 Mechanical Drawing





SOT-23 DIMENSION						
DIM	MILLIM	ETERS	INCHES			
DIN	MIN	MAX	MIN	MAX.		
Α	0.95	BSC	0.037 BSC			
A1	1.9	BSC	0.074	BSC		
В	2.60	3.00	0.102	0.118		
С	1.40	1.70	0.055	0.067		
D	2.80	3.10	0.110	0.122		
E	1.00	1.30	0.039	0.051		
F	0.00	0.10	0.000	0.004		
G	0.35	0.50	0.014	0.020		
Н	0.10	0.20	0.004	0.008		
	0.30	0.60	0.012 0.024			
J	5°	10º	5°	10º		

### **Marking Diagram**



05	= Device Cod	е					
Y	= Year Code						
Μ	= Month Code	e for	Haloge	en Fr	ee Proc	luct	
	<b>O</b> =Jan	Ρ	=Feb	Q	=Mar	R	=Apr
	<b>S</b> =May	Т	=Jun	U	=Jul	V	=Aug
	W =Sep	Х	=Oct	Υ	=Nov	Ζ	=Dec
L	= Lot Code						



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