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April 1<sup>st</sup>, 2010 Renesas Electronics Corporation

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# **2SJ534** Silicon P Channel MOS FET

REJ03G0884-0500 (Previous: ADE-208-589C) Rev.5.00 Sep 07, 2005

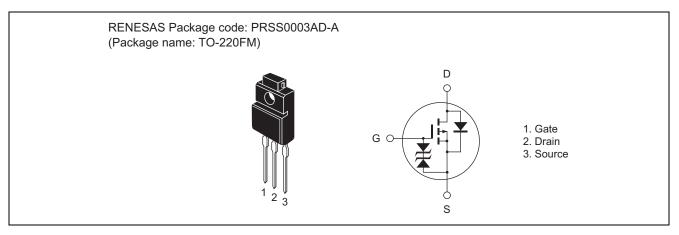
### Description

High speed power switching

### Features

- Low on-resistance
- $R_{DS (on)} = 0.050 \ \Omega$  typ.
- Low drive current.
- 4 V gate drive devices.
- High speed switching.

### Outline





# Absolute Maximum Ratings

			(Ta = 25°C)
Item	Symbol	Value	Unit
Drain to source voltage	V <sub>DSS</sub>	-60	V
Gate to source voltage	V <sub>GSS</sub>	±20	V
Drain current	ID	-18	A
Drain peak current	I <sub>D (pulse)</sub> Note 1	-72	A
Body to drain diode reverse drain current	I <sub>DR</sub>	-18	A
Avalanche current	I <sub>AP</sub> Note 3	-18	A
Avalanche energy	E <sub>AR</sub> Note 3	27	mJ
Channel dissipation	Pch Note 2	30	W
Channel temperature	Tch	150	°C
Storage temperature	Tstg	-55 to +150	°C

Notes: 1. PW  $\leq$  10  $\mu$ s, duty cycle  $\leq$  1%

2. Value at  $Tc = 25^{\circ}C$ 

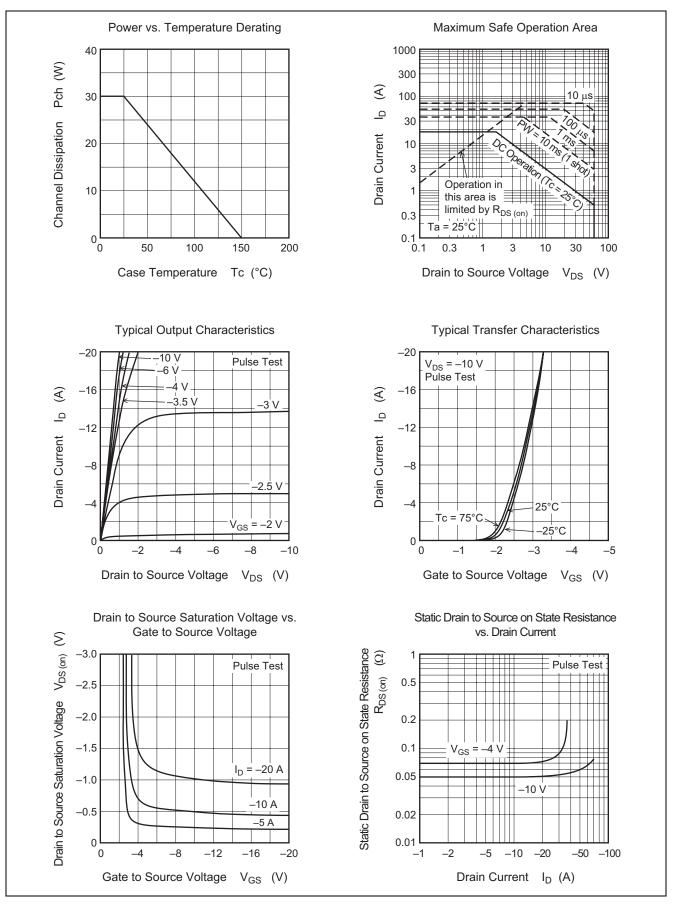
3. Value at Tch =  $25^{\circ}$ C, Rg  $\geq 50 \Omega$ 

### **Electrical Characteristics**

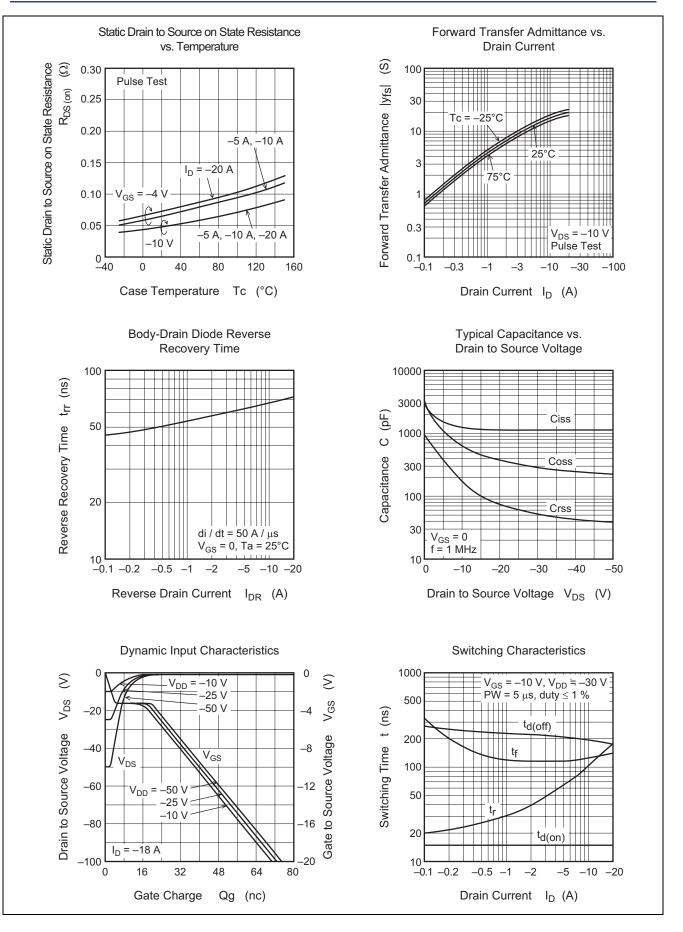
						$(Ta = 25^{\circ}C)$
ltem	Symbol	Min	Тур	Max	Unit	Test Conditions
Drain to source breakdown voltage	V (BR) DSS	-60	—		V	$I_D = -10 \text{ mA}, V_{GS} = 0$
Gate to source breakdown voltage	V (BR) GSS	±20	—	—	V	$I_G = \pm 100 \ \mu A, \ V_{DS} = 0$
Zero gate voltage drain current	I <sub>DSS</sub>	—	—	-10	μA	$V_{DS} = -60 V, V_{GS} = 0$
Gate to source leak current	I <sub>GSS</sub>	—	—	±10	μA	$V_{GS} = \pm 16 \text{ V},  V_{DS} = 0$
Gate to source cutoff voltage	V <sub>GS (off)</sub>	-1.0	—	-2.0	V	$I_D = -1 \text{ mA}, V_{DS} = -10 \text{ V}$
Static drain to source on state resistance	R <sub>DS (on)</sub>	—	0.050	0.065	Ω	$I_D = -9 \text{ A}, V_{GS} = -10 \text{ V}^{Note 4}$
	R <sub>DS (on)</sub>	—	0.070	0.110	Ω	$I_D = -9 \text{ A}, V_{GS} = -4 \text{ V}^{Note 4}$
Forward transfer admittance	y <sub>fs</sub>	10	16	—	S	$I_D = -9 \text{ A}, V_{DS} = -10 \text{ V}^{\text{Note 4}}$
Input capacitance	Ciss		1300		pF	$V_{DS} = -10 V$
Output capacitance	Coss	—	650	—	pF	$V_{GS} = 0$
Reverse transfer capacitance	Crss	—	180	—	pF	f = 1 MHz
Turn-on delay time	t <sub>d (on)</sub>	—	14	—	ns	$V_{GS} = -10 \text{ V}$
Rise time	tr	—	95	—	ns	$I_D = -9 A$
Turn-off delay time	t <sub>d (off)</sub>	—	190	—	ns	$R_L = 3.33 \Omega$
Fall time	t <sub>f</sub>		135		ns	
Body to drain diode forward voltage	V <sub>DF</sub>	—	-1.0		V	$I_F = -18 \text{ A}, V_{GS} = 0$
Body to drain diode reverse recovery time	t <sub>rr</sub>	—	70	—	ns	$I_F = -18 \text{ A}, V_{GS} = 0$
						di <sub>F</sub> /dt = 50 A/µs

Note: 4. Pulse test

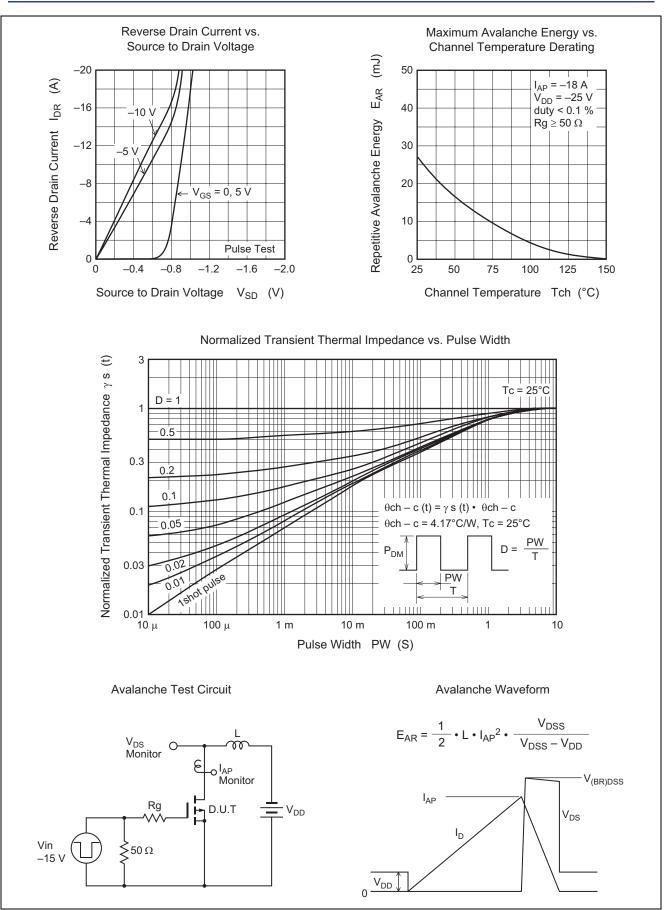
### **Main Characteristics**



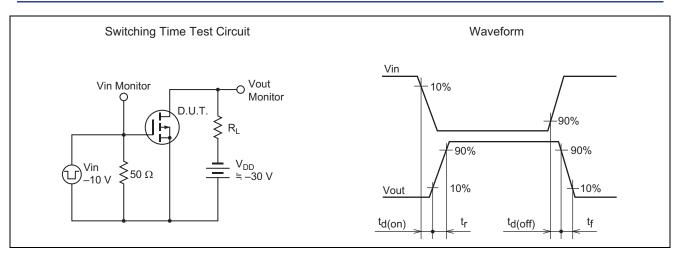
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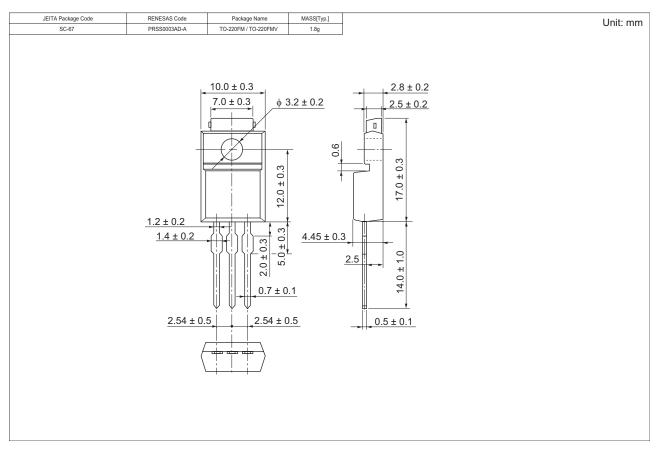








### **Package Dimensions**



### **Ordering Information**

Part Name	Quantity	Shipping Container
2SJ534-E	500 pcs	Box (Sack)

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