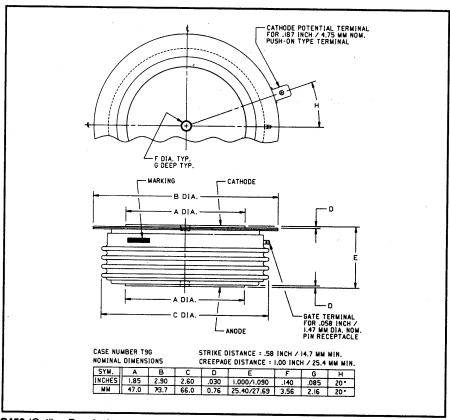


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Phase Control SCR 1640 Amperes Average 1600 Volts



C450 (Outline Drawing)

Ordering Information:

Select the complete five or six digit part number you desire from the table, i.e. C450PM is a 1600 Volt, 1640 Ampere Phase Control SCR.

Туре	Voltage		Current	
	V _{DRM} V _{RRM}	Code	l _{T(av)}	
C450	600	M	1640	
	800	N		
	1000	Р		
	1200	PB		
	1400	PD		
	1600	PM		



C450 Phase Control SCR 1640 Amperes Average, 1600 Volts

Description:

Powerex Silicon Controlled Rectifiers (SCR) are designed for phase control applications. These are all-diffused, Press-Pak, hermetic Pow-R-Disc devices employing the field proven amplifying gate.

Features:

- ☐ Low On-State Voltage
- ☐ High di/dt Capability
- ☐ High dv/dt Capability
- ☐ Hermetic Packaging
- Excellent Surge and I²t Ratings

Applications:

- ☐ Power Supplies



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C450 Phase Control SCR 1640 Amperes Average, 1600 Volts

Absolute Maximum Ratings

Characteristics	Symbol	C450	Units Volts	
Non-repetitive Transient Peak Reverse Voltage	V _{RSM}	V _{RRM} + 100V		
RMS On-state Current, T _C = 65°C	^I T(rms)	2575	Amperes	
Average Current 180° Sine Wave, T _C = 65°C	I _{T(av)}	1640	Amperes	
RMS On-state Current, T _C = 55°C	I _{T(rms)}	2790	Amperes	
Average Current 180° Sine Wave, T _C = 55°C	I _{T(av)}	1780	Amperes	
Peak One Cycle Surge On-state Current (Non-repetitive) 60Hz	l _{tsm}	28500	Amperes	
Peak One Cycle Surge On-state Current (Non-repetitive) 50Hz	ltsm.	26000	Amperes	
Critical Rate-of-rise of On-state Current (Non-repetitive)	di/dt	800	A/μsec	
Critical Rate-of-rise of On-state Current (Repetitive)	di/dt	400	A/μsec	
I ² t (for Fusing) for One Cycle, 60Hz	ı ² t	3.4 x 10 ⁶	A ² sec	
Peak Gate Power Dissipation	P _{GM}	200	Watts	
Average Gate Power Dissipation	. PG(av)	5	Watts	
Operating Temperature	T _i	-40 to +125°C	°C	
Storage Temperature	T _{stg}	-40 to +150°C	°C	
Approximate Weight		1	lb.	
		454	g	
Mounting Force		5500 to 6000	lb.	
•		2450 to 2670	kg.	



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C450

Phase Control SCR

1640 Amperes Average, 1600 Volts

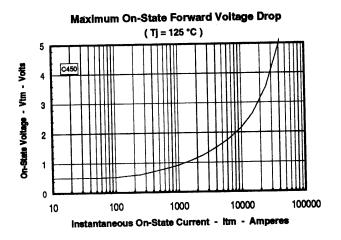
Electrical Characteristics, $T_j = 25^{\circ}C$ Unless Otherwise Specified

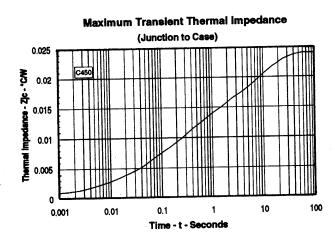
Characteristics	Symbol	Test Conditions	Min.	Тур.	Max.	Unit
Repetitive Peak Reverse Leakage Current	IRRM	T _j = 125°C, V _R = V _{RRM}		·yp.		
Repetitive Peak Forward Leakage Current	IDRM	$T_j = 125^{\circ}C, V_D = V_{DRM}$			45	mA
Peak On-state Voltage	V _{TM}	I _{TM} = 3000A Peak			45	mA
	• • • • • • • • • • • • • • • • • • • •	Duty Cycle < 0.1%			1.4	Volts
Threshold Voltage, Low-level	V _{(TO)1}	$T_j = 125^{\circ}C$, $I = 15\%$, $I_{T(av)}$ to $\pi I_{T(av)}$				
Slope Resistance, Low-level		- 120 0, 12 13%, 1T(av) 10 π1T(av)			0.6768	Volts
Threshold Voltage, High-level	^r T1 V _{(TO)2}	$T_j = 125$ °C, $I = \pi I_{T(av)}$ to I_{TSM}			0.1925	mΩ
Slope Resistance, High-level	^r T2	$r_j = 123 \text{ G}, \ r = mT(av) \text{ to TSM}$			1.1978	Volts
V _{TM} Coefficients, Low-level	12	$T_j = 125^{\circ}C$, $I = 15\% I_{T(av)} to \pi I_{T(av)}$			0.0937	mΩ
) 122 3,1 10,011(av) to hill (av)		,	0.400	05
					$N_1 = 0.106$ $N_1 = 0.047$	
					$r_1 = 0.047$ $r_1 = 9.845$	
					$y_1 = 9.045$ $y_1 = 0.015$	
V _{TM} Coefficients, High-level		$T_j = 125$ °C, $I = \pi I_{T(av)}$ to ITSM			1 = 0.015	
		, (av) Tow		А	2 = -2.571	۵.
					2 = 0.5470	
					2 = 1.157I	
Fundad Data Tra					2 = -0.014	
Typical Delay Time	^t d	$I_T = 50A$, Gate = 20V, 20 Ω ,		0.7		μsec
Funital T		0.1μsec Rise				proce
Typical Turn-off Time	tq	$T_j = 125$ °C, $I_T = 2000$ A,		150		μsec
		di _R /dt = 25A/μsec Reapplied				дооо
		$dv/dt = 200V/\mu sec$ Linear to				
		V_{DRM} , $V_{R} = 50V$,				
Ainimum Critical duide Form		Gate = 0V, $R_{GK} = 100\Omega$				
Minimum Critical dv/dt - Exponential to VDRM	dv/dt	T _j = 125°C	400			V/µsec
Sate Trigger Current	^Į GT	T _j = 25°C,			200	mA
		$V_D = 20V_{DC}, R_L = 3\Omega$				
Gate Trigger Voltage	V _{GT}	$T_j = -40^{\circ}\text{C to } +125^{\circ}\text{C},$			5.0	Volts
		$V_D = 20V, R_L = 3\Omega$			5.0	VOILS
Ion-Triggering Gate Voltage	V _{GDM}	T _i = 125°C,			0.45	
	00111	$V_D = V_{DRM}$, $R_L = 1000\Omega$			0.15	Volts
eak Forward Gate Current	^I GTM	D - VDRM, I'L - 100052				
eak Reverse Gate Voltage					10	A
	VGRM				5	Volts
hermal Characteristics						
aximum Thermal Resistance, Double Sided Coo	oling					
unction-to-Case	_					
ase-to-Sink	R _θ (j-c)				0.025	°C/W
ado to offic	$R_{\theta(c-s)}$				0.0075	°C/W

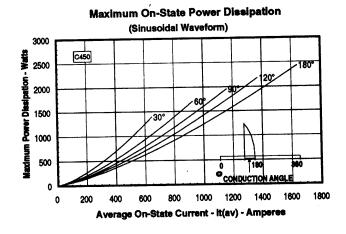


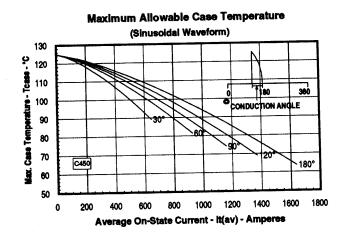
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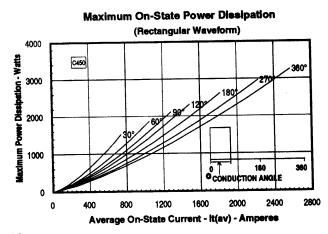
C450
Phase Control SCR
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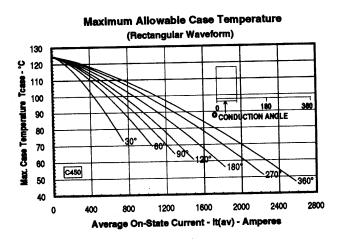












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