AEC-Q101 Qualified

2.5V Drive Pch MOS FET

RTR020P02FRA

Structure

Silicon P-channel MOS FET

Features

- 1) Low On-resistance.
- 2) Built-in G-S Protection Diode.
- 3) Small Surface Mount Package (TSMT3).

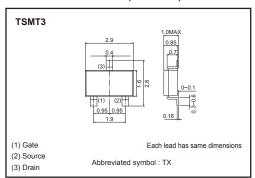
Application

Power switching, DC / DC converter.

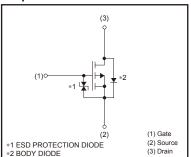
Packaging specifications

	Package	Taping
Туре	Code	TL
	Basic ordering unit (pieces)	3000
RTR020P02	\bigcirc	

●External dimensions (Unit : mm)



Equivalent circuit



● Absolute maximum ratings (Ta=25°C)

Parameter		Symbol	Limits	Unit		
Drain-source voltage		V _{DSS}	-20	V		
Gate-source voltage		V _{GSS}	±12	V		
Drain current	Continuous	ID	±2.0	Α		
Drain current	Pulsed	I _{DP} *1	±8.0	Α		
Source current	Continuous	Is	-0.8	Α		
(Body diode)	Pulsed	I _{SP} *1	-3.2	Α		
Total power dissipation		P _D *2	1.0	W		
Channel temperature		Tch	150	°C		
Range of Storage temperature		Tstg	-55 to +150	°C		

^{*1} Pw≤10μs, Duty cycle≤1% *2 Mounted on a ceramic board

•Thermal resistance

Parameter	Symbol	Limits	Unit	
Channel to ambient	Rth (ch-a)*	125	°C / W	

^{*} Mounted on a ceramic board.

●Electrical characteristics (Ta=25°C)

Parameter	Symbol	Min.	Тур.	Max.	Unit	Conditions	
Gate-source leakage	I _{GSS}	-	-	±10	μА	V _{GS} =±12V, V _{DS} =0V	
Drain-source breakdown voltage	V _{(BR) DSS}	-20	-	_	٧	I _D = -1mA, V _{GS} =0V	
Zero gate voltage drain current	I _{DSS}	-	-	-1	μА	V _{DS} = -20V, V _{GS} =0V	
Gate threshold voltage	V _{GS (th)}	-0.7	-	-2.0	V	V _{DS} = -10V, I _D = -1mA	
0		_	100	135	mΩ	I _D = -2.0A, V _{GS} = -4.5V	
Static drain-source on-state resistance	R _{DS (on)} *	_	110	150	mΩ	I _D = -2.0A, V _{GS} = -4.0V	
resistance		_	180	250	mΩ	I _D = -1.0A, V _{GS} = -2.5V	
Forward transfer admittance	Yfs *	1.2	_	_	S	V _{DS} = -10V, I _D = -1.0A	
Input capacitance	Ciss	_	430	_	pF	V _{DS} = -10V	
Output capacitance	Coss	_	80	_	pF	V _{GS} =0V	
Reverse transfer capacitance	Crss	-	55	_	pF	f=1MHz	
Turn-on delay time	t d (on) *	-	11	_	ns	Ip= -1.0A	
Rise time	tr *	-	13	_	ns	V _{DD} ≒ −15V V _{GS} = −4.5V R _I =15Ω	
Turn-off delay time	td (off) *	_	38	_	ns		
Fall time	t _f *	_	12	-	ns	R _G =10Ω	
Total gate charge	Qg	_	4.9	-	nC	V _{DD} ≒−15V	
Gate-source charge	Qgs	_	1.2	_	nC	V _{GS} = -4.5V	
Gate-drain charge	Qgd	_	1.3	_	nC	I _D =-2.0A	

^{*}Pulsed

●Body diode characteristics (Source-drain) (Ta=25°C)

	-		•			
Parameter	Symbol	Min.	Тур.	Max.	Unit	Conditions
Forward voltage	Vsp	_	_	-1.2	V	Is= -0.8A, V _{GS} =0V

•Electrical characteristic curves

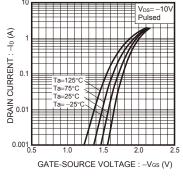


Fig.1 Typical Transfer Characteristics

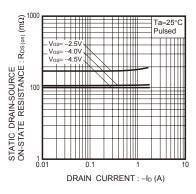


Fig.2 Static Drain-Source On-State Resistance vs. Drain Current

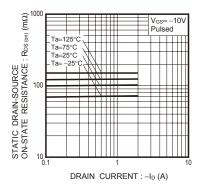


Fig.3 Static Drain-Source On-State Resistance vs. Drain Current

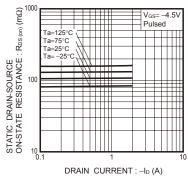


Fig.4 Static Drain-Source On-State Resistance vs. Drain Current

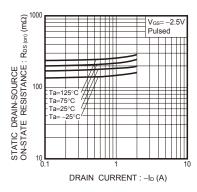


Fig.5 Static Drain-Source On-State Resistance vs. Drain Current

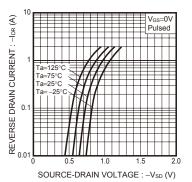


Fig.6 Reverse Drain Current vs.Source-Drain Voltage

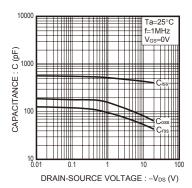


Fig.7 Typical Capacitance vs. Drain-Source Voltage

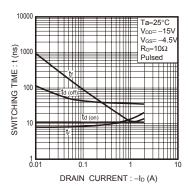


Fig.8 Switching Characteristics

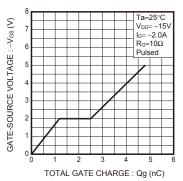


Fig.9 Dynamic Input Characteristics

Measurement circuits

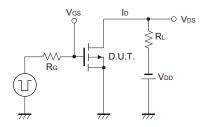


Fig.10 Switching Time Test Circuit

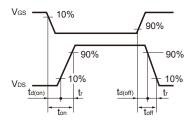


Fig.11 Switching Time Waveforms

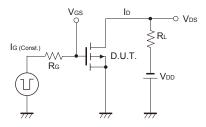


Fig.12 Gate Charge Test Circuit

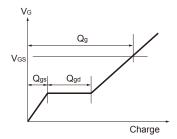


Fig.13 Gate Charge Waveform

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CLASSⅢ	CL ACCIII	CLASSIIb	CLASSIII	
CLASSIV	CLASSⅢ	CLASSⅢ	CLASSIII	

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 - [h] Use of the Products in places subject to dew condensation
- 4. The Products are not subject to radiation-proof design.
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- 6. In particular, if a transient load (a large amount of load applied in a short period of time, such as pulse. is applied, confirmation of performance characteristics after on-board mounting is strongly recommended. Avoid applying power exceeding normal rated power; exceeding the power rating under steady-state loading condition may negatively affect product performance and reliability.
- 7. De-rate Power Dissipation (Pd) depending on Ambient temperature (Ta). When used in sealed area, confirm the actual ambient temperature.
- 8. Confirm that operation temperature is within the specified range described in the product specification.
- 9. ROHM shall not be in any way responsible or liable for failure induced under deviant condition from what is defined in this document.

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This Product is electrostatic sensitive product, which may be damaged due to electrostatic discharge. Please take proper caution in your manufacturing process and storage so that voltage exceeding the Products maximum rating will not be applied to Products. Please take special care under dry condition (e.g. Grounding of human body / equipment / solder iron, isolation from charged objects, setting of lonizer, friction prevention and temperature / humidity control).

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- 1. Product performance and soldered connections may deteriorate if the Products are stored in the places where:
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 - [b] the temperature or humidity exceeds those recommended by ROHM
 - [c] the Products are exposed to direct sunshine or condensation
 - [d] the Products are exposed to high Electrostatic
- Even under ROHM recommended storage condition, solderability of products out of recommended storage time period
 may be degraded. It is strongly recommended to confirm solderability before using Products of which storage time is
 exceeding the recommended storage time period.
- 3. Store / transport cartons in the correct direction, which is indicated on a carton with a symbol. Otherwise bent leads may occur due to excessive stress applied when dropping of a carton.
- 4. Use Products within the specified time after opening a humidity barrier bag. Baking is required before using Products of which storage time is exceeding the recommended storage time period.

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