

NLU3G17

Triple Non-Inverting Schmitt-Trigger Buffer

The NLU3G17 MiniGate™ is an advanced high-speed CMOS triple non-inverting Schmitt-trigger buffer in ultra-small footprint.

The NLU3G17 input and output structures provide protection when voltages up to 7.0 V are applied, regardless of the supply voltage.

The NLU3G17 can be used to enhance noise immunity or to square up slowly changing waveforms.

Features

- High Speed: $t_{PD} = 4.0$ ns (Typ) @ $V_{CC} = 5.0$ V
- Low Power Dissipation: $I_{CC} = 1$ μ A (Max) at $T_A = 25^\circ$ C
- Power Down Protection Provided on inputs
- Balanced Propagation Delays
- Overvoltage Tolerant (OVT) Input and Output Pins
- Ultra-Small Packages
- These are Pb-Free Devices

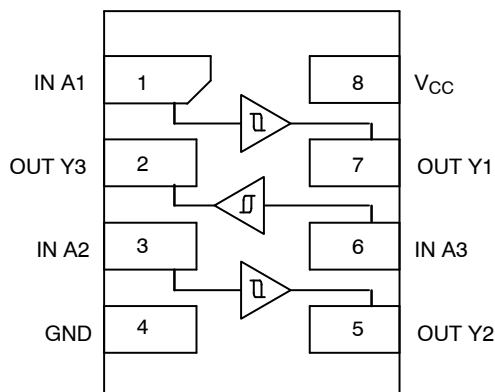


Figure 1. Pinout (Top View)

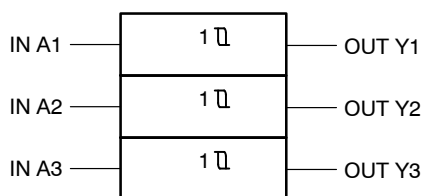


Figure 2. Logic Symbol

FUNCTION TABLE	
A	Y
L	L
H	H

PIN ASSIGNMENT

1	IN A1
2	OUT Y3
3	IN A2
4	GND
5	OUT Y2
6	IN A3
7	OUT Y1
8	V_{CC}



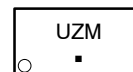
ON Semiconductor®

<http://onsemi.com>

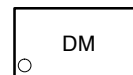
MARKING DIAGRAMS



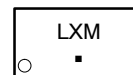
UDFN8
CASE 517AJ



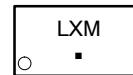
ULLGA8
1.45 x 1.0
CASE 613AA



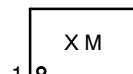
ULLGA8
1.6 x 1.0
CASE 613AB



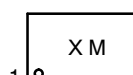
ULLGA8
1.95 x 1.0
CASE 613AC



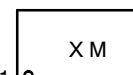
UDFN8
1.45 x 1.0
CASE 517BZ



UDFN8
1.6 x 1.0
CASE 517BY



UDFN8
1.95 x 1.0
CASE 517CA



UZ, D or LX = Specific Device Code
M = Date Code
▪ = Pb-Free Package

ORDERING INFORMATION

See detailed ordering and shipping information in the package dimensions section on page 4 of this data sheet.

NLU3G17

MAXIMUM RATINGS

Symbol	Parameter	Value	Unit
V _{CC}	DC Supply Voltage	-0.5 to +7.0	V
V _{IN}	DC Input Voltage	-0.5 to +7.0	V
V _{OUT}	DC Output Voltage	-0.5 to +7.0	V
I _{IK}	DC Input Diode Current V _{IN} < GND	-20	mA
I _{OK}	DC Output Diode Current V _{OUT} < GND	±20	mA
I _O	DC Output Source/Sink Current	±12.5	mA
I _{CC}	DC Supply Current Per Supply Pin	±25	mA
I _{GND}	DC Ground Current per Ground Pin	±25	mA
T _{STG}	Storage Temperature Range	-65 to +150	°C
T _L	Lead Temperature, 1 mm from Case for 10 Seconds	260	°C
T _J	Junction Temperature Under Bias	150	°C
MSL	Moisture Sensitivity	Level 1	
F _R	Flammability Rating Oxygen Index: 28 to 34	UL 94 V-0 @ 0.125 in	
I _{LATCHUP}	Latchup Performance Above V _{CC} and Below GND at 125°C (Note 2)	±500	mA

Stresses exceeding Maximum Ratings may damage the device. Maximum Ratings are stress ratings only. Functional operation above the Recommended Operating Conditions is not implied. Extended exposure to stresses above the Recommended Operating Conditions may affect device reliability.

1. Measured with minimum pad spacing on an FR4 board, using 10 mm-by-1 inch, 2 ounce copper trace no air flow.
2. Tested to EIA / JESD78.

RECOMMENDED OPERATING CONDITIONS

Symbol	Parameter	Min	Max	Unit
V _{CC}	Positive DC Supply Voltage	1.65	5.5	V
V _{IN}	Digital Input Voltage	0	5.5	V
V _{OUT}	Output Voltage	0	5.5	V
T _A	Operating Free-Air Temperature	-55	+125	°C
Δt/ΔV	Input Transition Rise or Fall Rate V _{CC} = 3.3 V ± 0.3 V V _{CC} = 5.0 V ± 0.5 V	0 0	No Limit No Limit	ns/V

NLU3G17

DC ELECTRICAL CHARACTERISTICS

Symbol	Parameter	Conditions	V _{CC} (V)	T _A = 25 °C			T _A = +85°C		T _A = -55°C to +125°C		Unit
				Min	Typ	Max	Min	Max	Min	Max	
V _{T+}	Positive Threshold Voltage		3.0	1.85	2.0	2.2		2.2		2.2	V
			4.5	2.86	3.0	3.15		3.15		3.15	
			5.5	3.50	3.6	3.85		3.85		3.85	
V _{T-}	Negative Threshold Voltage		3.0	0.9	1.5	1.65	0.9		0.9		V
			4.5	1.35	2.3	2.46	1.35		1.35		
			5.5	1.65	2.9	3.05	1.65		1.65		
V _H	Hysteresis Voltage		3.0	0.30	0.57	1.20	0.30	1.20	0.30	1.20	V
			4.5	0.40	0.67	1.40	0.40	1.40	0.40	1.40	
			5.5	0.50	0.74	1.60	0.50	1.60	0.50	1.60	
V _{OH}	Minimum High-Level Output Voltage	V _{IN} ≥ V _{T+} MAX I _{OH} = -50 μA	2.0	1.9	2.0		1.9		1.9		V
			3.0	2.9	3.0		2.9		2.9		
		4.5	4.4	4.5		4.4		4.4			
		V _{IN} ≥ V _{T+} MAX I _{OH} = -4 mA I _{OH} = -8 mA	3.0	2.58			2.48		2.34		
4.5	3.94			3.80		3.66					
V _{OL}	Maximum Low-Level Output Voltage	V _{IN} ≤ V _{T-} MIN I _{OL} = 50 μA	2.0		0	0.1		0.1		0.1	V
			3.0		0	0.1		0.1		0.1	
			4.5		0	0.1		0.1		0.1	
		V _{IN} ≤ V _{T-} MIN I _{OL} = 4 mA I _{OL} = 8 mA	3.0			0.36		0.44		0.52	
4.5				0.36		0.44		0.52			
I _{IN}	Input Leakage Current	0 ≤ V _{IN} ≤ 5.5 V	0 to 5.5			±0.1		±1.0		μA	
I _{CC}	Quiescent Supply Current	0 ≤ V _{IN} ≤ V _{CC}	5.5			1.0		10		μA	

AC ELECTRICAL CHARACTERISTICS (Input t_r = t_f = 3.0 ns)

Symbol	Parameter	V _{CC} (V)	Test Condition	T _A = 25 °C			T _A = +85°C		T _A = -55°C to +125°C		Unit
				Min	Typ	Max	Min	Max	Min	Max	
t _{pLH} , t _{pHL}	Propagation Delay, Input A to Output \bar{Y}	3.0 to 3.6	C _L = 15 pF		7.0	12.8	1.0	15	1.0	17	ns
			C _L = 50 pF		8.5	16.3	1.0	18.5	1.0	20.5	
		4.5 to 5.5	C _L = 15 pF		4.0	8.6	1.0	10	1.0	11.5	
			C _L = 50 pF		5.5	10.6	1.0	12	1.0	13.5	
C _{IN}	Input Capacitance				5.0	10		10		pF	
C _{PD}	Power Dissipation Capacitance (Note 3)	5.0			7.0					pF	

3. C_{PD} is defined as the value of the internal equivalent capacitance which is calculated from the dynamic operating current consumption without load. Average operating current can be obtained by the equation I_{CC(OPR)} = C_{PD} • V_{CC} • f_{in} + I_{CC}. C_{PD} is used to determine the no-load dynamic power consumption: P_D = C_{PD} • V_{CC}² • f_{in} + I_{CC} • V_{CC}.

NLU3G17

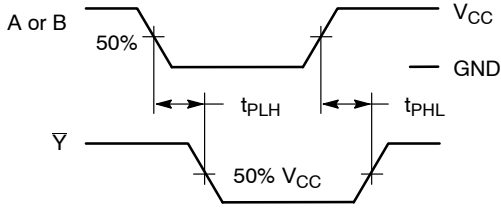
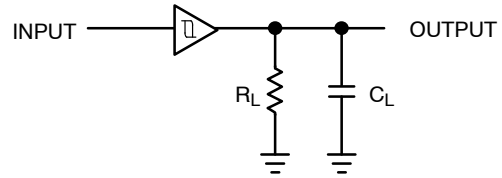
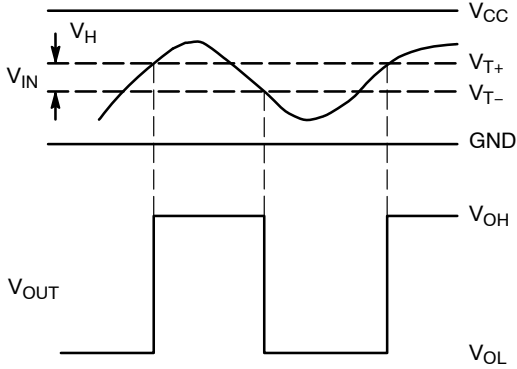


Figure 3. Switching Waveforms

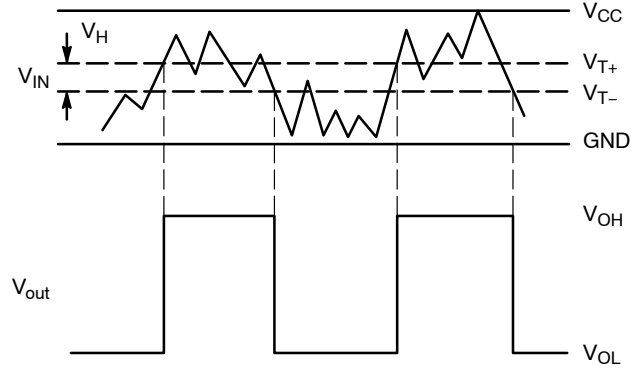


A 1-MHz square input wave is recommended for propagation delay tests.

Figure 4. Test Circuit



(a) A Schmitt-Trigger Squares Up Inputs With Slow Rise and Fall Times



(b) A Schmitt-Trigger Offers Maximum Noise Immunity

Figure 5. Typical Schmitt-Trigger Applications

ORDERING INFORMATION

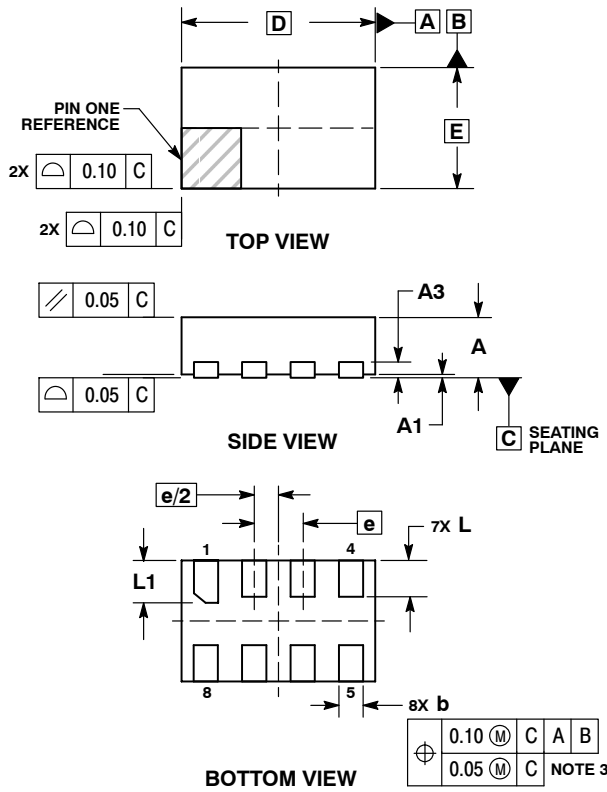
Device	Package	Shipping†
NLU3G17MUTAG	UDFN8 (Pb-Free)	3000 / Tape & Reel
NLU3G17AMX1TCG	ULLGA8, 1.95 x 1.0, 0.5P (Pb-Free)	3000 / Tape & Reel
NLU3G17BMX1TCG	ULLGA8, 1.6 x 1.0, 0.4P (Pb-Free)	3000 / Tape & Reel
NLU3G17CMX1TCG	ULLGA8, 1.45 x 1.0, 0.35P (Pb-Free)	3000 / Tape & Reel
NLU3G17DMUTCG	UDFN8, 1.95 x 1.0, 0.5P (Pb-Free)	3000 / Tape & Reel
NLU3G17EMUTCG	UDFN8, 1.6 x 1.0, 0.4P (Pb-Free)	3000 / Tape & Reel
NLU3G17FMUTCG	UDFN8, 1.45 x 1.0, 0.35P (Pb-Free)	3000 / Tape & Reel

†For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

NLU3G17

PACKAGE DIMENSIONS

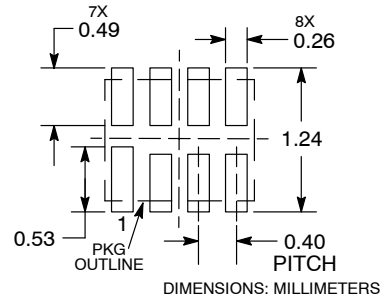
UDFN8 1.6x1.0, 0.4P
CASE 517BY
ISSUE O



- NOTES:
1. DIMENSIONING AND TOLERANCING PER ASME Y14.5M, 1994.
 2. CONTROLLING DIMENSION: MILLIMETERS.
 3. DIMENSION b APPLIES TO PLATED TERMINAL AND IS MEASURED BETWEEN 0.15 AND 0.20 MM FROM TERMINAL TIP.
 4. PACKAGE DIMENSIONS EXCLUSIVE OF BURRS AND MOLD FLASH.

DIM	MILLIMETERS	
	MIN	MAX
A	0.45	0.55
A1	0.00	0.05
A3	0.13	REF
b	0.15	0.25
D	1.60	BSC
E	1.00	BSC
e	0.40	BSC
L	0.25	0.35
L1	0.30	0.40

RECOMMENDED SOLDERING FOOTPRINT*

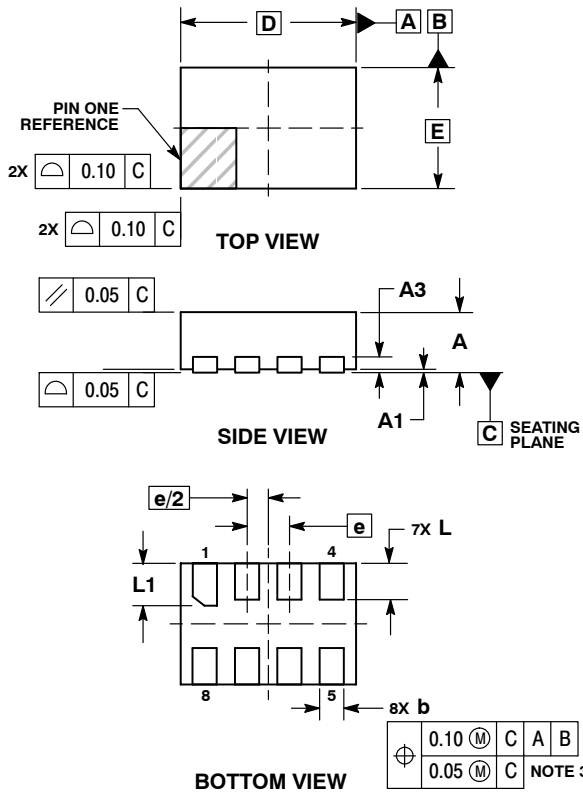


*For additional information on our Pb-Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

NLU3G17

PACKAGE DIMENSIONS

UDFN8 1.45x1.0, 0.35P
CASE 517BZ
ISSUE O

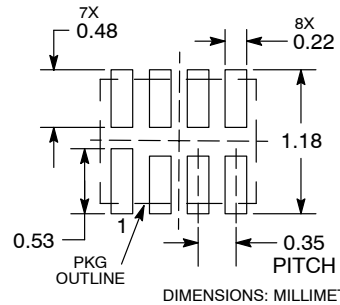


NOTES:

1. DIMENSIONING AND TOLERANCING PER ASME Y14.5M, 1994.
2. CONTROLLING DIMENSION: MILLIMETERS.
3. DIMENSION b APPLIES TO PLATED TERMINAL AND IS MEASURED BETWEEN 0.15 AND 0.20 MM FROM TERMINAL TIP.
4. PACKAGE DIMENSIONS EXCLUSIVE OF BURRS AND MOLD FLASH.

DIM	MILLIMETERS	
	MIN	MAX
A	0.45	0.55
A1	0.00	0.05
A3	0.13 REF	
b	0.15	0.25
D	1.45 BSC	
E	1.00 BSC	
e	0.35 BSC	
L	0.25	0.35
L1	0.30	0.40

RECOMMENDED SOLDERING FOOTPRINT*

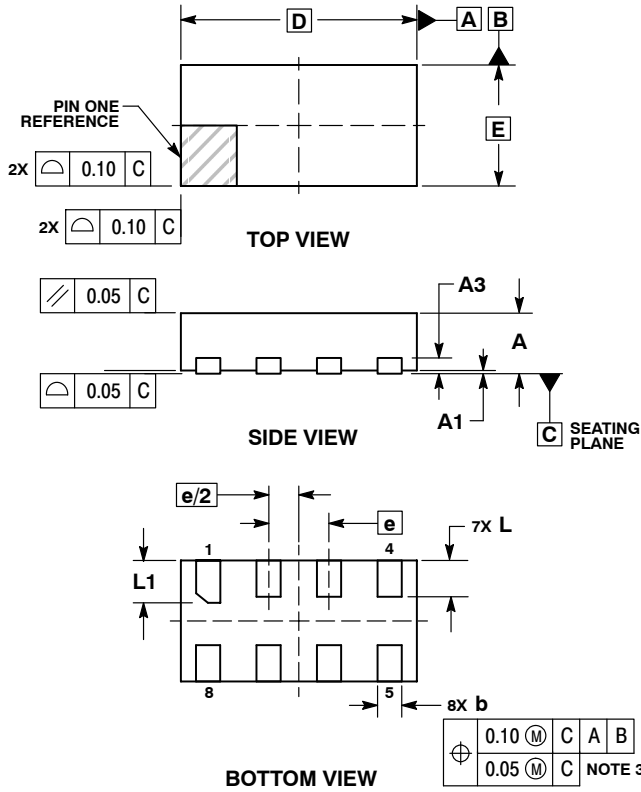


*For additional information on our Pb-Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

NLU3G17

PACKAGE DIMENSIONS

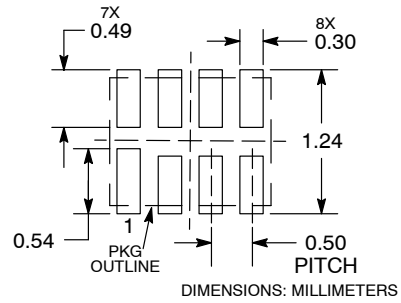
UDFN8 1.95x1.0, 0.5P
CASE 517CA
ISSUE O



- NOTES:
1. DIMENSIONING AND TOLERANCING PER ASME Y14.5M, 1994.
 2. CONTROLLING DIMENSION: MILLIMETERS.
 3. DIMENSION b APPLIES TO PLATED TERMINAL AND IS MEASURED BETWEEN 0.15 AND 0.20 MM FROM TERMINAL TIP.
 4. PACKAGE DIMENSIONS EXCLUSIVE OF BURRS AND MOLD FLASH.

MILLIMETERS		
DIM	MIN	MAX
A	0.45	0.55
A1	0.00	0.05
A3	0.13 REF	
b	0.15	0.25
D	1.95 BSC	
E	1.00 BSC	
e	0.50 BSC	
L	0.25	0.35
L1	0.30	0.40

RECOMMENDED SOLDERING FOOTPRINT*

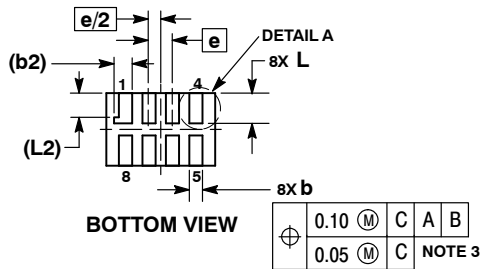
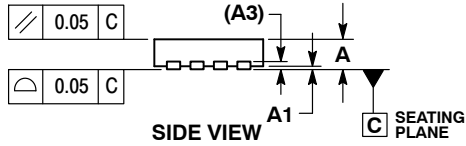
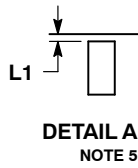
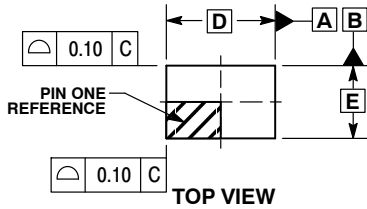


*For additional information on our Pb-Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

NLU3G17

PACKAGE DIMENSIONS

UDFN8 1.8x1.2, 0.4P
CASE 517AJ
ISSUE O

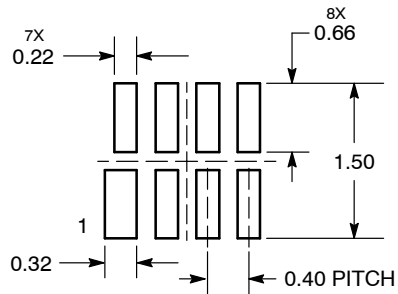


NOTES:

1. DIMENSIONING AND TOLERANCING PER ASME Y14.5M, 1994.
2. CONTROLLING DIMENSION: MILLIMETERS.
3. DIMENSION b APPLIES TO PLATED TERMINAL AND IS MEASURED BETWEEN 0.15 AND 0.30 mm FROM TERMINAL TIP.
4. MOLD FLASH ALLOWED ON TERMINALS ALONG EDGE OF PACKAGE. FLASH MAY NOT EXCEED 0.03 ONTO BOTTOM SURFACE OF TERMINALS.
5. DETAIL A SHOWS OPTIONAL CONSTRUCTION FOR TERMINALS.

MILLIMETERS		
DIM	MIN	MAX
A	0.45	0.55
A1	0.00	0.05
A3	0.127	REF
b	0.15	0.25
b2	0.30	REF
D	1.80	BSC
E	1.20	BSC
e	0.40	BSC
L	0.45	0.55
L1	0.00	0.03
L2	0.40	REF

MOUNTING FOOTPRINT SOLDERMASK DEFINED



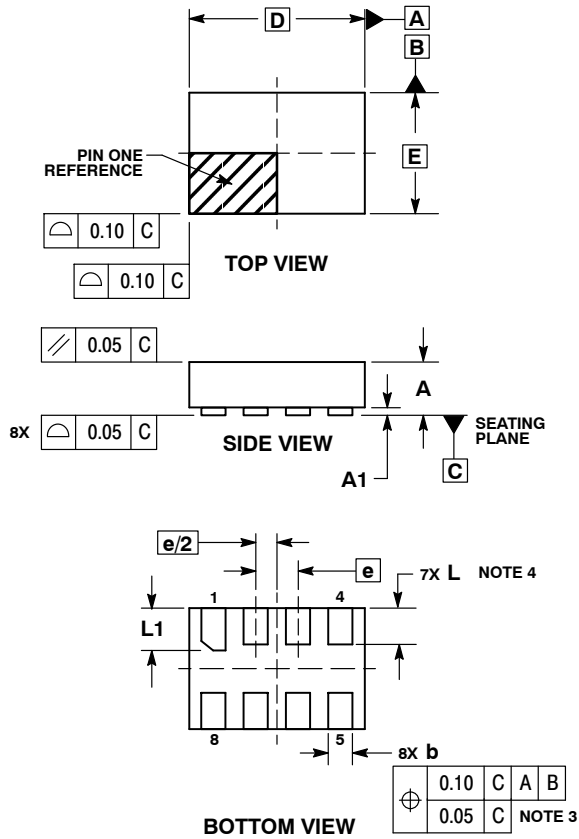
DIMENSIONS: MILLIMETERS

*For additional information on our Pb-Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

NLU3G17

PACKAGE DIMENSIONS

ULLGA8 1.45x1.0, 0.35P
CASE 613AA
ISSUE A

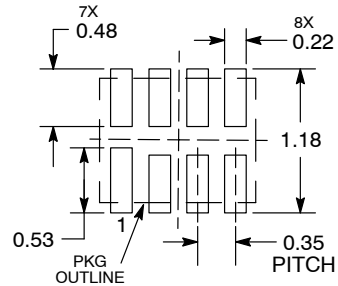


NOTES:

1. DIMENSIONING AND TOLERANCING PER ASME Y14.5M, 1994.
2. CONTROLLING DIMENSION: MILLIMETERS.
3. DIMENSION b APPLIES TO PLATED TERMINAL AND IS MEASURED BETWEEN 0.15 AND 0.30 mm FROM THE TERMINAL TIP.
4. A MAXIMUM OF 0.05 PULL BACK OF THE PLATED TERMINAL FROM THE EDGE OF THE PACKAGE IS ALLOWED.

DIM	MILLIMETERS	
	MIN	MAX
A	---	0.40
A1	0.00	0.05
b	0.15	0.25
D	1.45 BSC	
E	1.00 BSC	
e	0.35 BSC	
L	0.25	0.35
L1	0.30	0.40

**MOUNTING FOOTPRINT
SOLDERMASK DEFINED***



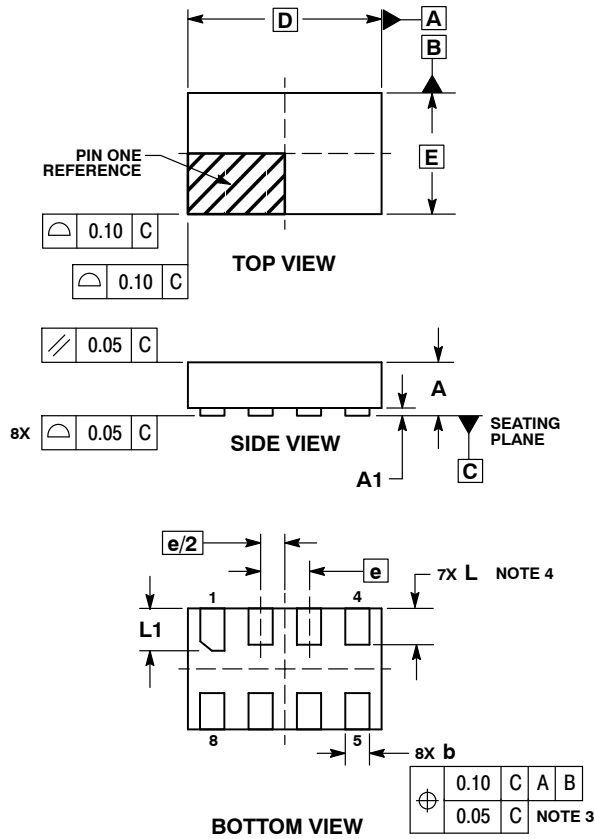
DIMENSIONS: MILLIMETERS

*For additional information on our Pb-Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

NLU3G17

PACKAGE DIMENSIONS

ULLGA8 1.6x1.0, 0.4P
CASE 613AB
ISSUE A

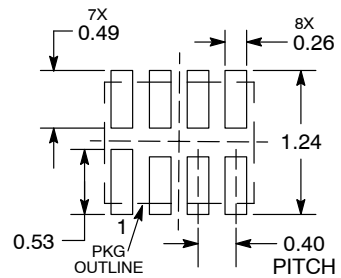


NOTES:

1. DIMENSIONING AND TOLERANCING PER ASME Y14.5M, 1994.
2. CONTROLLING DIMENSION: MILLIMETERS.
3. DIMENSION b APPLIES TO PLATED TERMINAL AND IS MEASURED BETWEEN 0.15 AND 0.30 mm FROM THE TERMINAL TIP.
4. A MAXIMUM OF 0.05 PULL BACK OF THE PLATED TERMINAL FROM THE EDGE OF THE PACKAGE IS ALLOWED.

MILLIMETERS		
DIM	MIN	MAX
A	---	0.40
A1	0.00	0.05
b	0.15	0.25
D	1.60 BSC	
E	1.00 BSC	
e	0.40 BSC	
L	0.25	0.35
L1	0.30	0.40

**MOUNTING FOOTPRINT
SOLDERMASK DEFINED***



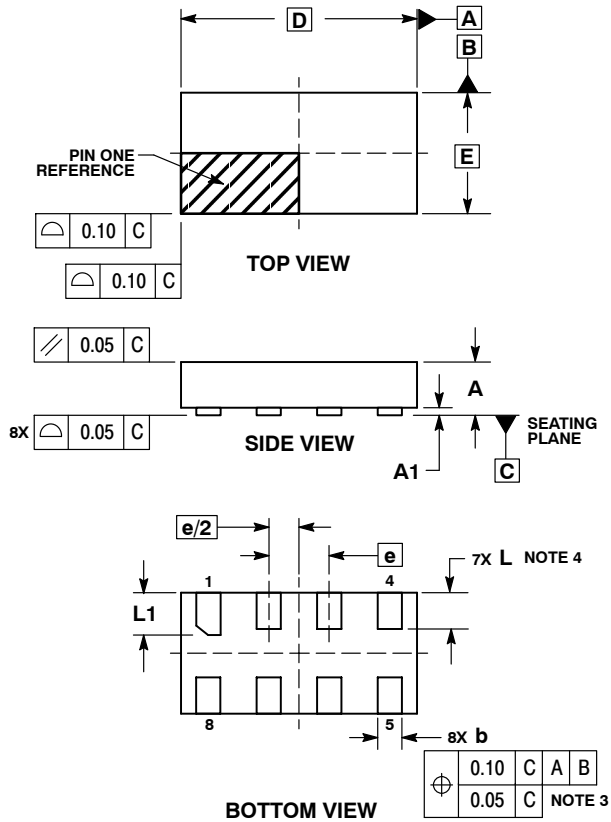
DIMENSIONS: MILLIMETERS

*For additional information on our Pb-Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

NLU3G17

PACKAGE DIMENSIONS

ULLGA8 1.95x1.0, 0.5P
CASE 613AC
ISSUE A

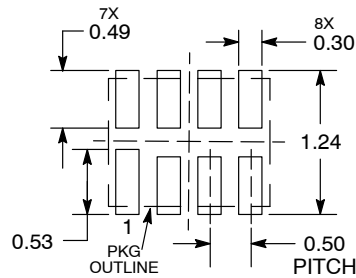


NOTES:

1. DIMENSIONING AND TOLERANCING PER ASME Y14.5M, 1994.
2. CONTROLLING DIMENSION: MILLIMETERS.
3. DIMENSION b APPLIES TO PLATED TERMINAL AND IS MEASURED BETWEEN 0.15 AND 0.30 mm FROM THE TERMINAL TIP.
4. A MAXIMUM OF 0.05 PULL BACK OF THE PLATED TERMINAL FROM THE EDGE OF THE PACKAGE IS ALLOWED.

MILLIMETERS		
DIM	MIN	MAX
A	---	0.40
A1	0.00	0.05
b	0.15	0.25
D	1.95 BSC	
E	1.00 BSC	
e	0.50 BSC	
L	0.25	0.35
L1	0.30	0.40

**MOUNTING FOOTPRINT
SOLDERMASK DEFINED***



DIMENSIONS: MILLIMETERS

*For additional information on our Pb-Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

MiniGate is a trademark of Semiconductor Components Industries, LLC (SCILLC).

ON Semiconductor and are registered trademarks of Semiconductor Components Industries, LLC (SCILLC). SCILLC reserves the right to make changes without further notice to any products herein. SCILLC makes no warranty, representation or guarantee regarding the suitability of its products for any particular purpose, nor does SCILLC assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation special, consequential or incidental damages. "Typical" parameters which may be provided in SCILLC data sheets and/or specifications can and do vary in different applications and actual performance may vary over time. All operating parameters, including "Typicals" must be validated for each customer application by customer's technical experts. SCILLC does not convey any license under its patent rights nor the rights of others. SCILLC products are not designed, intended, or authorized for use as components in systems intended for surgical implant into the body, or other applications intended to support or sustain life, or for any other application in which the failure of the SCILLC product could create a situation where personal injury or death may occur. Should Buyer purchase or use SCILLC products for any such unintended or unauthorized application, Buyer shall indemnify and hold SCILLC and its officers, employees, subsidiaries, affiliates, and distributors harmless against all claims, costs, damages, and expenses, and reasonable attorney fees arising out of, directly or indirectly, any claim of personal injury or death associated with such unintended or unauthorized use, even if such claim alleges that SCILLC was negligent regarding the design or manufacture of the part. SCILLC is an Equal Opportunity/Affirmative Action Employer. This literature is subject to all applicable copyright laws and is not for resale in any manner.

PUBLICATION ORDERING INFORMATION

LITERATURE FULFILLMENT:
Literature Distribution Center for ON Semiconductor
P.O. Box 5163, Denver, Colorado 80217 USA
Phone: 303-675-2175 or 800-344-3860 Toll Free USA/Canada
Fax: 303-675-2176 or 800-344-3867 Toll Free USA/Canada
Email: orderlit@onsemi.com

N. American Technical Support: 800-282-9855 Toll Free USA/Canada
Europe, Middle East and Africa Technical Support:
Phone: 421 33 790 2910
Japan Customer Focus Center
Phone: 81-3-5817-1050

ON Semiconductor Website: www.onsemi.com
Order Literature: <http://www.onsemi.com/orderlit>
For additional information, please contact your local Sales Representative

X-ON Electronics

Largest Supplier of Electrical and Electronic Components

Click to view similar products for [Buffers & Line Drivers](#) category:

Click to view products by [ON Semiconductor](#) manufacturer:

Other Similar products are found below :

[5962-9217601MSA](#) [634810D](#) [875140G](#) [HEF4022BP](#) [HEF4043BP](#) [NL17SG125DFT2G](#) [NL17SZ126P5T5G](#) [NLU1GT126CMUTCG](#)
[NLU3G16AMX1TCG](#) [NLV27WZ125USG](#) [MC74HCT365ADTR2G](#) [BCM6306KMLG](#) [54FCT240CTDB](#) [Le87401NQC](#) [Le87402MQC](#)
[028192B](#) [042140C](#) [051117G](#) [070519XB](#) [065312DB](#) [091056E](#) [098456D](#) [NL17SG07DFT2G](#) [NL17SG17DFT2G](#) [NL17SG34DFT2G](#)
[NL17SZ07P5T5G](#) [NL17SZ125P5T5G](#) [NLU1GT126AMUTCG](#) [NLV27WZ16DFT2G](#) [5962-8982101PA](#) [5962-9052201PA](#) [74LVC07ADR2G](#)
[MC74VHC1G125DFT1G](#) [NL17SH17P5T5G](#) [NL17SZ125CMUTCG](#) [NLV17SZ07DFT2G](#) [NLV37WZ17USG](#) [NLVHCT244ADTR2G](#)
[NC7WZ17FHX](#) [74HCT126T14-13](#) [NL17SH125P5T5G](#) [NLV14049UBDTR2G](#) [NLV37WZ07USG](#) [74VHC541FT\(BE\)](#) [RHFAC244K1](#)
[74LVC1G17FW4-7](#) [74LVC1G126FZ4-7](#) [BCM6302KMLG](#) [74LVC1G07FZ4-7](#) [74LVC1G125FW4-7](#)