Datasheet

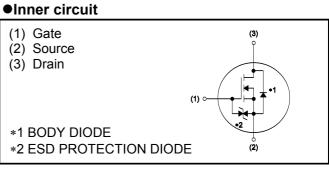
Nch 45V 2.5A Power MOSFET

AEC-C	101	Qual	lified
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V_{DSS}	45V
$R_{DS(on)}(Max.)$	100m $Ω$
I _D	2.5A
P_D	1.0W

Features

- 1) Low on resistance.
- 2) Built-in G-S Protection Diode.
- 3) Small Surface Mount Package (TSMT3).
- 4) Pb-free lead plating; RoHS compliant



Application

DC/DC converters, Relay drive

Packaging specifications

Outline TSMT3

	Packaging	Taping
	Reel size (mm)	180
Typo	Tape width (mm)	8
Туре	Basic ordering unit (pcs)	3,000
	Taping code	TL
	Marking	ZF

●Absolute maximum ratings(T_a = 25°C)

Parameter	Symbol	Value	Unit
Drain - Source voltage	V_{DSS}	45	V
Continuous drain current	I _D *1	±2.5	А
Pulsed drain current	I _{D,pulse} *2	±10	А
Gate - Source voltage	V_{GSS}	±20	V
Avalanche energy, single pulse	E _{AS} *3	4.8	mJ
Dower dissination	P _D *4	0.54	W
Power dissipation	P_{D}^{*5}	1.0	W
Junction temperature	T _j	150	°C
Range of storage temperature	T _{stg}	−55 to +150	°C

●Thermal resistance

Parameter	Symbol	Values			Unit
- Farametei	Зуппоп	Min.	Тур.	Max.	Offic
Thermal resistance, junction - ambient	R _{thJA} *4	-	-	125	°C/W
Thermal resistance, junction - ambient	R _{thJA} *5	-	-	231	°C/W

•Electrical characteristics($T_a = 25$ °C)

Parameter	Symbol	Conditions	Values			Unit
r ai ai nietei	Symbol	Conditions	Min.	Тур.	Max.	Offic
Drain - Source breakdown voltage	$V_{(BR)DSS}$	$V_{GS} = 0V$, $I_D = 1mA$	45	-	-	V
Breakdown voltage temperature coefficient	$\frac{\Delta V_{(BR)DSS}}{\Delta T_{j}}$	I _D = 1mA referenced to 25°C	ı	42	ı	mV/°C
Zero gate voltage drain current	I _{DSS}	$V_{DS} = 45V, V_{GS} = 0V$	ı	1	1	μΑ
Gate - Source leakage current	I_{GSS}	$V_{GS} = \pm 20V, V_{DS} = 0V$	ı	ı	±10	μΑ
Gate threshold voltage	V _{GS (th)}	V_{DS} = 10V, I_D = 1mA	1.0	ı	3.0	V
Gate threshold voltage temperature coefficient	$\frac{\Delta V_{(GS)th}}{\Delta T_{j}}$	I _D = 1mA referenced to 25°C	ı	-4.2	-	mV/°C
		V _{GS} =10V, I _D =2.5A	-	70	100	
Static drain - source	D *6	V _{GS} =4.5V, I _D =2.5A	-	95	150	m()
on - state resistance	$R_{DS(on)}$	V _{GS} =4.0V, I _D =2.5A	1	105	160	mΩ
		V _{GS} =10V, I _D =2.5A, T _j =125°C	ı	110	154	
Gate input resistannce	R_{G}	f = 1MHz, open drain	-	7	1	Ω
Transconductance	9 _{fs} *6	$V_{DS} = 10V, I_{D} = 2.5A$	2.0	4.2	-	S

^{*1} Limited only by maximum temperature allowed.

^{*2} Pw $\leq 10 \mu s, \, Duty \, cycle \leq 1\%$

^{*3} L \simeq 1mH, V_{DD} = 25V, Rg = 25 Ω , starting T_j = 25°C

^{*4} Mounted on a ceramic board (30×30×0.8mm)

^{*5} Mounted on a FR4 (12×20×0.8mm)

^{*6} Pulsed

•Electrical characteristics($T_a = 25^{\circ}C$)

Parameter	Symbol	Conditions	Values			Unit
- Farameter	Symbol	Conditions	Min.	Тур.	Max.	Offic
Input capacitance	C _{iss}	V _{GS} = 0V	-	260	-	
Output capacitance	C _{oss}	V _{DS} = 10V	-	90	-	pF
Reverse transfer capacitance	C_{rss}	f = 1MHz	-	30	-	
Turn - on delay time	t _{d(on)} *6	$V_{DD} \simeq 25V$, $V_{GS} = 10V$	-	9	-	
Rise time	t _r *6	I _D = 1.25A	-	11	-	no
Turn - off delay time	t _{d(off)} *6	$R_L = 20\Omega$	-	25	-	ns
Fall time	t _f *6	$R_G = 10\Omega$	-	8	-	

•Gate Charge characteristics($T_a = 25$ °C)

Parameter	Symbol	Conditions	Values			Unit
- Farameter	Symbol	Conditions	Min.	Тур.	Max.	Offic
Total gate charge Q _g	O *6	$V_{DD} \simeq 25V$, $I_D = 2.5A$ $V_{GS} = 5V$	-	3.6	1	
	Q_{g}	$V_{DD} = 25V, I_D = 2.5A$ $V_{GS} = 10V$	-	6	12	nC
Gate - Source charge	Q _{gs} *6	$V_{DD}^{\sim} 25V, I_{D} = 2.5A$ $V_{GS} = 5V$	-	1.5	-	
Gate - Drain charge	Q _{gd} *6	$V_{GS} = 5V$	-	0.8	-	

●Body diode electrical characteristics (Source-Drain)(T_a = 25°C)

Parameter	Parameter Symbol Conditions -		Values			Unit
r ai ai ii etei			Min.	Тур.	Max.	Offic
Inverse diode continuous, forward current	l _S *1	T _a = 25°C	-	-	0.8	А
Forward voltage	V _{SD} *6	$V_{GS} = 0V, I_s = 2.5A$	-	-	1.2	V

Fig.1 Power Dissipation Derating Curve

120

100

xem
040
0
0
50

100

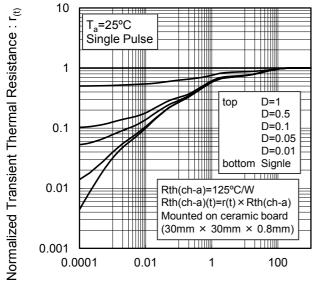
150
200

Junction Temperature: Tj [°C]

Fig.2 Maximum Safe Operating Area 100 Operation in this area P_W = 1ms is limited by $R_{DS}(on)$ / ($V_{GS} = 10V$) $P_{W} = 100 \mu s$ 10 Drain Current : I_D [A] 1 $P_W = 10ms$ DC Operation 0.1 T_a=25°C Single Pulse Mounted on a ceramic board $(30mm \times 30mm \times 0.8mm)$ 0.01 0.1 10 100

Drain - Source Voltage : V_{DS} [V]

Fig.3 Normalized Transient Thermal Resistance vs. Pulse Width



Pulse Width: Pw[s]

1000
T_a=25°C
Single Pulse

1000
10
0.1
0.0001
0.01
1 100

Pulse Width: Pw [s]

ROHM

Fig.5 Avalanche Current vs Inductive Load

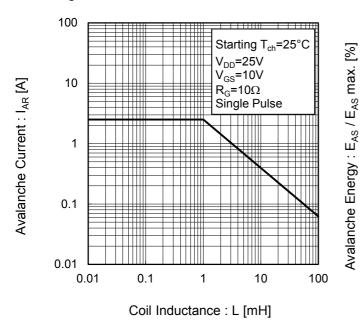
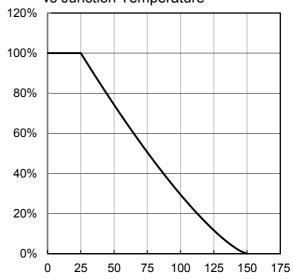
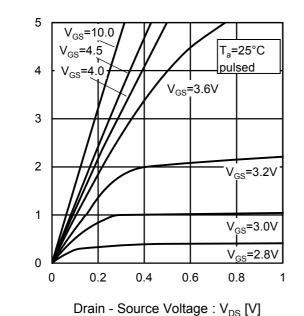


Fig.6 Avalanche Energy Derating Curve vs Junction Temperature



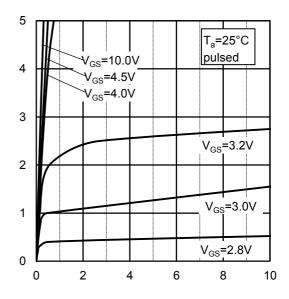
Junction Temperature : T_i [°C]

Fig.7 Typical Output Characteristics(I)



Drain Current : I_D [A]

Fig.8 Typical Output Characteristics(II)



Drain - Source Voltage : V_{DS} [V]

Drain Current : I_D [A]

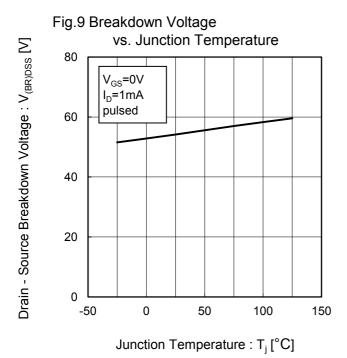
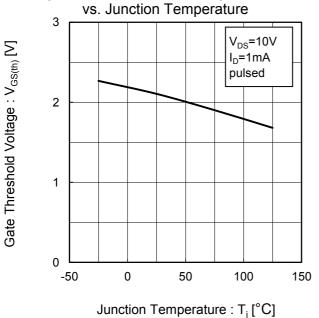


Fig. 10 Typical Transfer Characteristics 10 pulsed Drain Current: I_D [A] =125°C =75°C =25°C 0.1 0.01 0.001 2.0 2.5 1.5 3.0 0.0 1.0

Gate - Source Voltage : V_{GS} [V]

Fig.12 Transconductance vs. Drain Current

Fig.11 Gate Threshold Voltage



V_{DS}=10V pulsed 1

T_a=125°C T_a=75°C Ta=25°C T₂= -25°C 0.1 0.01 0.01 0.1 1 10

Drain Current : I_D [A]

Fransconductance: g_{fs} [S]

Fig.13 Drain CurrentDerating Curve

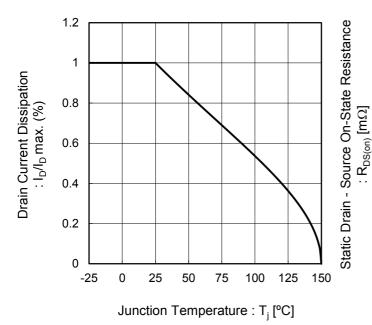
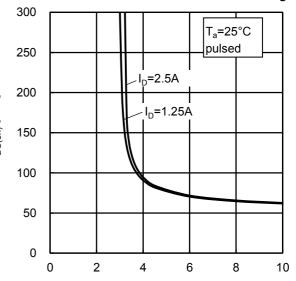


Fig.14 Static Drain - Source On - State Resistance vs. Gate Source Voltage



Gate - Source Voltage : V_{GS} [V]

Fig.15 Static Drain - Source On - State Resistance vs. Drain Current(I)

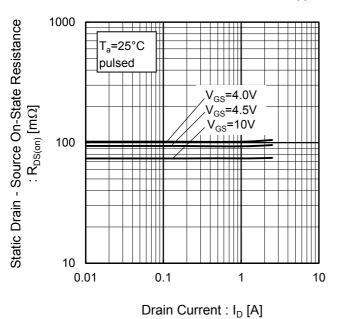
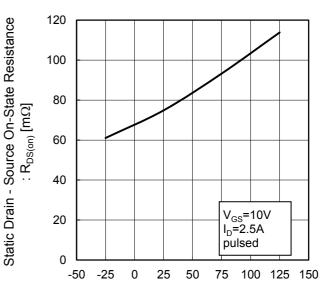
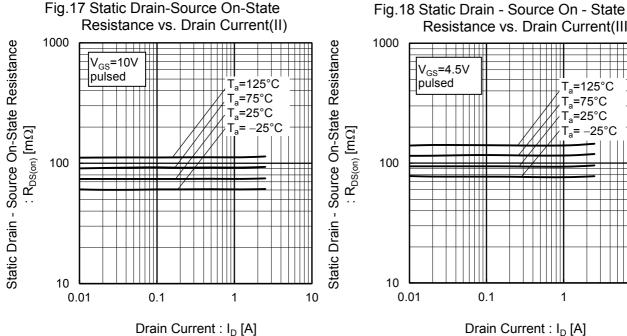


Fig.16 Static Drain - Source On - State Resistance vs. Junction Temperature



Junction Temperature : T_i [°C]



Resistance vs. Drain Current(III) V_{GS}=4.5V pulsed T_a=125°C T_a=75°C T_a=25°C $T_a = -25^{\circ}C$ 0.01 0.1 10 Drain Current : I_D [A]

Fig.19 Static Drain - Source On - State Resistance vs. Drain Current(IV)

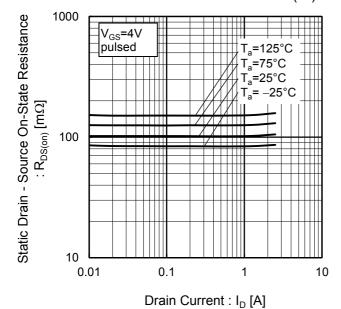


Fig.20 Typical Capacitance
vs. Drain - Source Voltage

10000

T_a=25°C

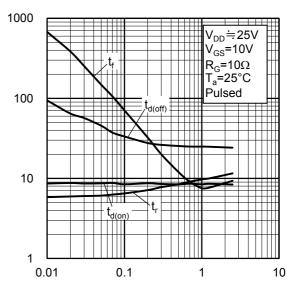
f=1MHz
V_{GS}=0V

100

100

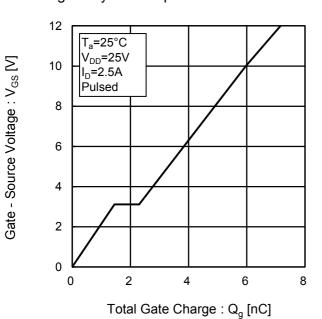
Drain - Source Voltage : V_{DS} [V]

Fig.21 Switching Characteristics



Drain Current : I_D [A]

Fig.22 Dynamic Input Characteristics



Source Current : I_s [A]

Switching Time : t [ns]

Source-Drain Voltage : V_{SD} [V]

●Measurement circuits

Fig.1-1 Switching Time Measurement Circuit

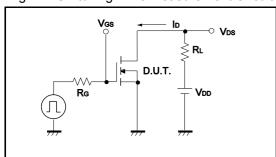


Fig.2-1 Gate Charge Measurement Circuit

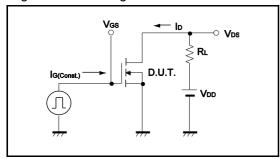


Fig.3-1 Avalanche Measurement Circuit

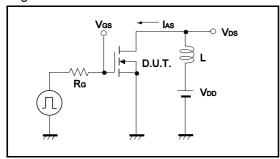


Fig.1-2 Switching Waveforms

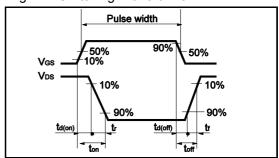


Fig.2-2 Gate Charge Waveform

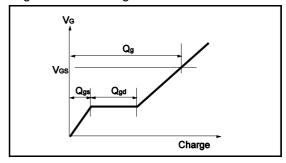
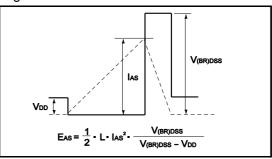
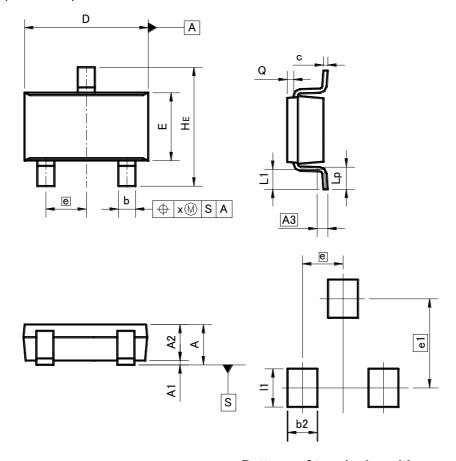


Fig.3-2 Avalanche Waveform



●Dimensions (Unit: mm)

TSMT3



Patterm of terminal position areas

DIM	MILIM	ETERS	INCHES		
DIM	MIN	MAX	MIN	MAX	
Α	_	1.00	ı	0.039	
A1	0.00	0.10	0	0.004	
A2	0.75	0.95	0.03	0.037	
A3	0.3	25	0.0	01	
b	0.35	0.50	0.014	0.02	
С	0.10	0.26	0.004	0.01	
D	2.80	3.00	0.11	0.118	
E	1.50	1.80	0.059	0.071	
е	0.9	95	0.0	04	
HE	2.60	3.00	0.102	0.118	
L1	0.30	0.60	0.012	0.024	
Lp	0.40	0.70	0.016	0.028	
Q	0.05	0.25	0.002	0.01	
х	_	0.20		0.008	

DIM	MILIMETERS		INCHES		
DIIVI	MIN	MAX	MIN	MAX	
e1	2.10		0.08		
b2		0.70	ı	0.028	
l1	_	0.90	_	0.035	

Dimension in mm/inches

Notice

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(Note1) Medical Equipment Classification of the Specific Applications

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JAPAN	USA	EU	CHINA
CLASSⅢ	CLASSⅢ	CLASSIIb	CLASSⅢ
CLASSIV	CLASSIII	CLASSⅢ	CLASSIII

- 2. ROHM designs and manufactures its Products subject to strict quality control system. However, semiconductor products can fail or malfunction at a certain rate. Please be sure to implement, at your own responsibilities, adequate safety measures including but not limited to fail-safe design against the physical injury, damage to any property, which a failure or malfunction of our Products may cause. The following are examples of safety measures:
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 - [f] Sealing or coating our Products with resin or other coating materials
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 - [h] Use of the Products in places subject to dew condensation
- 4. The Products are not subject to radiation-proof design.
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- 7. De-rate Power Dissipation (Pd) depending on Ambient temperature (Ta). When used in sealed area, confirm the actual ambient temperature.
- 8. Confirm that operation temperature is within the specified range described in the product specification.
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- 2. In principle, the reflow soldering method must be used on a surface-mount products, the flow soldering method must be used on a through hole mount products. If the flow soldering method is preferred on a surface-mount products, please consult with the ROHM representative in advance.

For details, please refer to ROHM Mounting specification

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 - [a] the Products are exposed to sea winds or corrosive gases, including Cl2, H2S, NH3, SO2, and NO2
 - [b] the temperature or humidity exceeds those recommended by ROHM
 - [c] the Products are exposed to direct sunshine or condensation
 - [d] the Products are exposed to high Electrostatic
- 2. Even under ROHM recommended storage condition, solderability of products out of recommended storage time period may be degraded. It is strongly recommended to confirm solderability before using Products of which storage time is exceeding the recommended storage time period.
- 3. Store / transport cartons in the correct direction, which is indicated on a carton with a symbol. Otherwise bent leads may occur due to excessive stress applied when dropping of a carton.
- 4. Use Products within the specified time after opening a humidity barrier bag. Baking is required before using Products of which storage time is exceeding the recommended storage time period.

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