AEC-Q101 Qualified

4V Drive Pch MOS FET RSQ035P03FRA

Structure

Silicon P-channel MOSFET

● Features

- 1) Low On-resistance.($65m\Omega$ at 4.5V)
- 2) High Power Package.
- 3) High speed switching.
- 4) Low voltage drive. (4V)

Applications

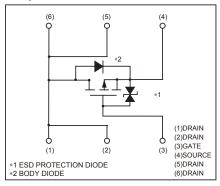
DC-DC converter

TSMT6 TSMT6 2.9 1.00MAX 0.85 0.7 0.7 0.16 Each lead has same dimensions Abbreviated symbol : TM

●Packaging specifications

Type	Package	Taping
	Code	TR
	Basic ordering unit (pieces)	3000
RSQ035P03F	0	

Equivalent circuit



● Absolute maximum ratings (Ta=25°C)

Parameter		Symbol	Limits	Unit			
Drain-source voltage		VDSS	-30	V			
Gate-source voltage		Vgss	±20	V			
Drain current	Continuous	ΙD	±3.5	A			
Drain current	Pulsed	IDP *1	±14	A			
Source current	Continuous	ls	-1	A			
(Body diode)	Pulsed	Isp *1	-4	A			
Total power dissipation		P _D *2	1.25	W			
Channel temperature	Tch	150	°C				
Range of Strage temperature		Tstg	-55 to +150	°C			

^{*1} Pw≤10μs, Duty cycle≤1%

*2 Mounted on a ceramic board Thermal resistance

Parameter	Symbol	Limits	Unit
Channel to ambient	Rth(ch-a) *	100	°C / W

^{*} Mounted on a ceramic board.



●Electrical characteristics (Ta=25°C)

Parameter	Symbol	Min.	Тур.	Max.	Unit	Conditions	
Gate-source leakage	Igss	-	_	±10	μΑ	V _{GS} =±20V, V _{DS} =0V	
Drain-source breakdown voltage	V(BR)DSS	-30	_	-	V	I _D =-1mA, V _{GS} =0V	
Zero gate voltage drain current	IDSS	_	_	-1	μΑ	V _{DS} =-30V, V _{GS} =0V	
Gate threshold voltage	V _{GS(th)}	-1.0	_	-2.5	V	V _{DS} =-10V, I _D =-1mA	
		_	45	65	mΩ	I _D =-3.5A, V _G S=-10V	
Static drain-source on-state	RDS(on)*	-	65	90	mΩ	I _D =-3.5A, V _G s=-4.5V	
resistance		-	70	95	mΩ	I _D =-1.75A, V _G S=-4.0V	
Foward transfer admittance	Yfs *	2.0	-	-	S	V _{DS} =-10V, I _D =-1.75A	
Input capacitance	Ciss	-	780	_	pF		
Output capacitance	Coss	_	180	-	pF	V _{DS} =-10V,V _{GS} =0V f=1MHz	
Reverse transfer capacitance	Crss	_	130	_	pF		
Turn-on delay time	td(on) *	_	15	-	ns	1 - 4.754	
Rise time	tr *	_	35	-	ns	Ib=-1.75A VDD≒-15V VGS=-10V RL=8.6Ω RG=10Ω	
Turn-off delay time	td(off) *	_	45	_	ns		
Fall time	t _f *	_	25	_	ns		
Total gate charge	Qg	_	9.2	-	nC		
Gate-source charge	Qgs	_	2.2	_	nC	V _{DD} =-15V V _{GS} =-5V	
Gate-drain charge	Qgd	_	3.4	-	nC	ID=-3.5A	

^{*}PULSED

●Body diode characteristics (Source-drain) (Ta=25°C)

Parameter	Symbol	Min.	Тур.	Max.	Unit	Conditions
Forward voltage	VsD	_	-	-1.2	V	Is=-1A, Vgs=0V

•Electrical characteristic curves

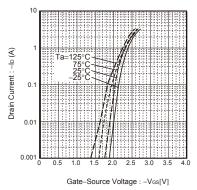


Fig.1 Typical Transfer Characteristics

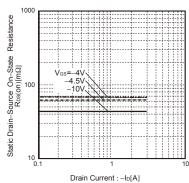


Fig.2 Static Drain–Source On–State Resistance vs.Drain Current

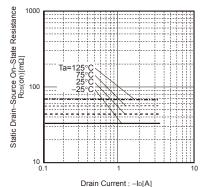


Fig.3 Static Drain–Source On–State Resistance vs.Drain Current

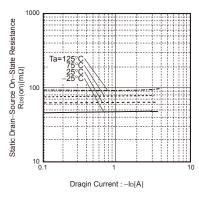


Fig.4 Static Drain-Source On-State vs.Drain-Current

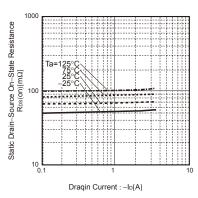


Fig.5 Static Drain–Source On–State vs.Drain–Current

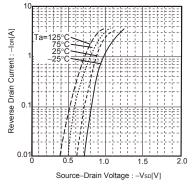


Fig.6 Reverse Drain Current Source-Drain Current

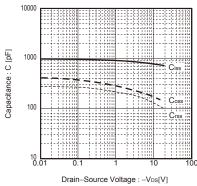


Fig.7 Typical Capactitance vs.Drain-Source Voltage

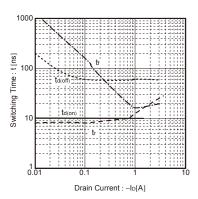
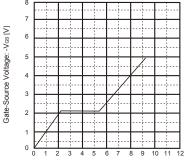


Fig.8 Switching Characteristics



Total Gate Charge : Qg[nC]
Fig.9 Dynamic Input Characteristics

•Switching characteristics measurement circuits

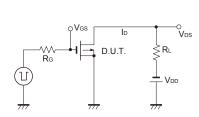


Fig.10 Switching Time Test Circuit

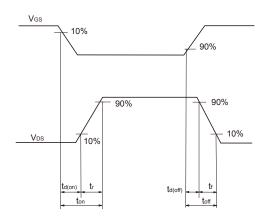


Fig.11 Switching Time Waveforms

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(: ::::a:::a:::a:::a:::a:::a:::a:::a:::a				
JAPAN	USA	EU	CHINA	
CLASSⅢ	CL ACCIII	CLASSIIb	CL ACCIII	
CLASSIV	CLASSⅢ	CLASSⅢ	CLASSⅢ	

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 - [h] Use of the Products in places subject to dew condensation
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 may be degraded. It is strongly recommended to confirm solderability before using Products of which storage time is
 exceeding the recommended storage time period.
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