| SPI | $\mathbf{F}C\mathbf{I}$ | FI | $C\Delta$ | TI | \mathbf{O} | NC |
|-----|-------------------------|--------------|-----------|----|--------------|---------|
| 171 | ו איו | , , , | | | \ / | 1 4 7 7 |

| CUSTOMER | : |
|-------------|---|
| SAMPLE CODE | : |
| | (This Code will be changed while mass production) |

MASS PRODUCTION CODE : PC0802ARS-AWA-A (VER.0)

Customer Approved

Date:

| Sales Sign | QC Confirmed | Checked By | Designer |
|------------|--------------|---|-------------------|
| | | Variary S 2003/07/07 Tom 2003/07/08 | 宋亚孫 2003/07/05 |

Approval For Specifications Only.

Please contact Powertip or it's representative before designing your product based on this specification.

Approval For Specifications and Sample.

Powertip Corporation

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^{*} This specification is subject to change without notice.



RECORDS OF REVISION

| Date | Rev. | Description | Note | Page |
|----------|------|------------------|------|------|
| 2003/7/5 | 0 | Revised Contents | | |
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Note: For detailed information please refer to IC data sheet: <u>ST7066U</u>



1. SPECIFICATIONS

1.1 Features

| Item | Standard Value |
|-------------------|---|
| Display Type | 8*2 Characters |
| LCD Type | STN Gray Positive ReflectiveNormalTemp. |
| Driver Condition | LCD Module: 1/16 Duty, 1/4 Bias |
| Viewing Direction | 6 O' clock |
| Backlight | - |
| Weight | 17 g |
| Interface | - - |
| Other | - |

1.2 Mechanical Specifications

| Item | Standard Value | Unit |
|-------------------|-----------------------------------|------|
| Outline Dimension | 58.0(L) * 32.0(w) * 10.3m(H)(Max) | mm |
| Viewing Area | 38.0(L) * 16.0(w) | mm |
| Active Area | 27.81(L) * 11.5 (w) | mm |
| Dot Size | 0.56(L) * 0.66(w) | mm |
| Dot Pitch | 0.60(L) * 0.70(w) | mm |

Note: For detailed information please refer to LCM drawing

1.3 Absolute Maximum Ratings

| Item | Symbol | Condition | Min. | Max. | Unit |
|---------------------------|-------------------|--------------|----------|----------------------|------|
| Power Supply Voltage | V_{DD} | - | -0.3 | 7.0 | V |
| LCD Driver Supply Voltage | V_{LCD} | - | VDD-10.0 | V _{DD} +0.3 | V |
| Input Voltage | V _{IN} | - | -0.3 | V _{DD} +0.3 | V |
| Operating Temperature | T_{OP} | Excluded B/L | 0 | 50 | |
| Storage Temperature | T_{ST} | Excluded B/L | -20 | 70 | |
| Storage Humidity | H_D | Ta < 40 | - | 90 | %RH |



1.4 DC Electrical Characteristics

 $V_{DD} = 5.0~V \pm 10\%$, $V_{SS} = 0V$, Ta = 25

| Item | Symbol | Condition | Min. | Тур. | Max. | Unit |
|----------------------|-------------------|---|---------|------|------|------|
| Logic Supply Voltage | V_{DD} | - | 4.5 | 5.0 | 5.5 | V |
| "H" Input Voltage | V_{IH} | - | 0.7 VDD | 1 | Vdd | V |
| "L" Input Voltage | $V_{\rm IL}$ | - | -0.3 | ı | 0.6 | V |
| "H" Output Voltage | V_{OH} | Iон=-0.1mА | 3.9 | 1 | Vdd | V |
| "L" Output Voltage | V_{OL} | IOL=0.1mA | - | 1 | 0.4 | V |
| Supply Current | I_{DD} | $V_{DD} = 5.0 \text{ V}$ | - | 1.3 | • | mA |
| | | V _{DD} - V _O (-20) | - | ı | • | |
| LCM Driver Voltage | V_{OP} | V _{DD} - V _O (25) | - | 4.0 | - | V |
| | | V _{DD} - V _O (70) | _ | - | - | |

1.5 Optical Characteristics

LCD Panel : 1/16 Duty , 1/4 Bias , $V_{LCD} = \!\! 4.4~V$, Ta = 25

| Item | Symbol | Conditions | Min. | Тур. | Max. | Reference |
|---------------------|--------|--|------|--------|------|-------------|
| View Angle | è | $C \ge 2.0, \varnothing = 0^{\circ}$ | 40° | - | 1 | Notes 1 & 2 |
| Contrast Ratio | С | $\grave{e} = 5^{\circ}, \varnothing = 0^{\circ}$ | 5 | 7 | 1 | Note 3 |
| Response Time(rise) | tr | $\grave{e} = 5^{\circ}, \varnothing = 0^{\circ}$ | - | 150 ms | - | Note 4 |
| Response Time(fall) | tf | $\grave{e} = 5^{\circ}, \varnothing = 0^{\circ}$ | - | 330 ms | - | Note 4 |



Note 1: Definition of angles θ and \varnothing

Light (when reflected) $z (\theta=0^{\circ})$

Sensor θ X' Z'LCD panel $X(\emptyset=90^\circ)$

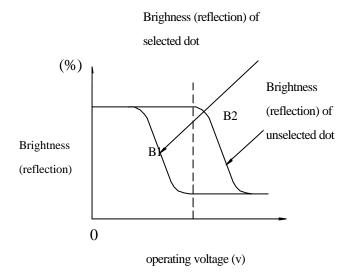
Light (when transmitted) $Y(\varnothing = 0^{\circ})$ $(\theta = 90^{\circ})$

Note 3: Definition of contrast C

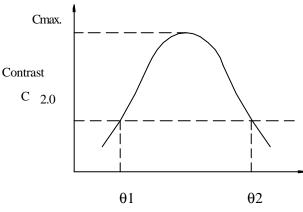
C = -

Brightness (reflection) of unselected dot (B2)

Brightness (reflection) of selected dot (B1)



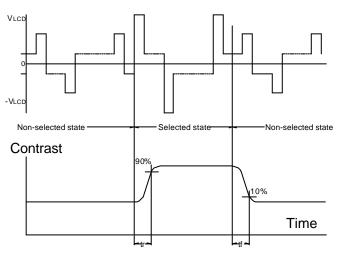
Note 2: Definition of viewing angles $\theta 1$ and $\theta 2$



viewing angle θ (\emptyset fixed)

Note : Optimum viewing angle with the naked eye and viewing angle θ at Cmax. Above are not always the same

Note 4: Definition of response time



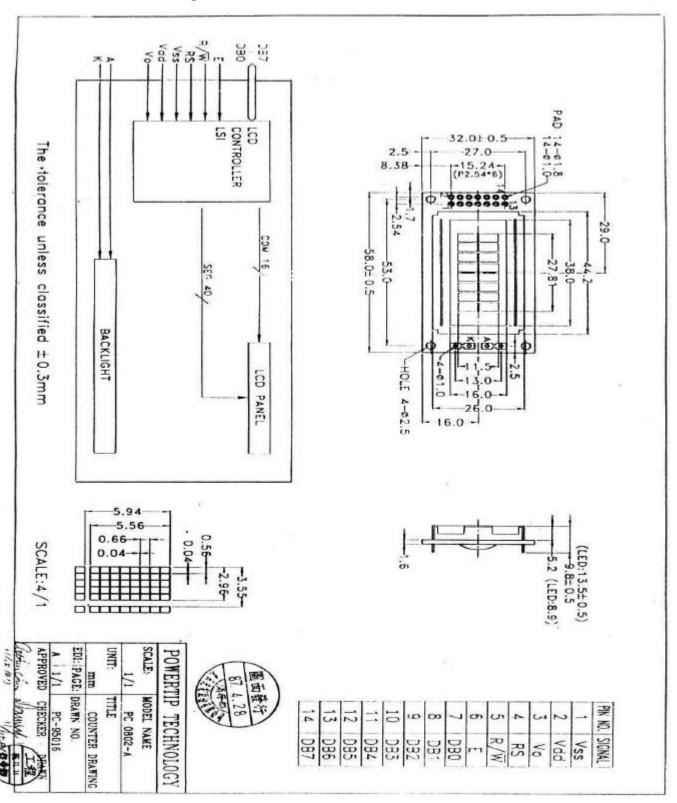
Note: Measured with a transmissive LCD panel which is displayed 1 cm²

 V_{LCD} : Operating voltagef $_{FRM}$: Frame frequency t_r : Response time (rise) 1: Response time (fall)



2. MODULE STRUCTURE

2.1 Counter Drawing

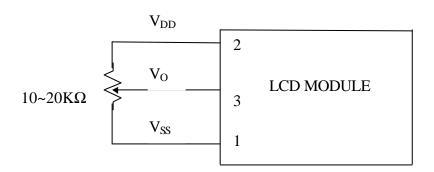




2.2 Interface Pin Description

| Pin No. | Symbol | Signal Description |
|---------|-----------|---|
| 1 | Vss | Power Supply (Vss=0) |
| 2 | Vdd | Power Supply (V _{DD} >V _{SS}) |
| 3 | Vo | Operating voltage (LCD Driver) |
| | | Register Selection input |
| 4 | RS | High = Data register |
| 4 | K2 | Low = Instruction register (for write) |
| | | Busy flag address counter (for read) |
| 5 | | Read/Write signal input is used to select the read/write mode |
| 3 | R/W | High = Read mode, Low = Write mode |
| 6 | Е | Start enable signal to read or write the data |
| | | Four low order bi-directional three-state data bus lines. Use |
| 7~10 | DB0 ~ DB3 | for data transfer between the MPU and the LCD module. |
| | | These four are not used during 4-bit operation. |
| | | Four high order bi-directional three-state data bus lines. Used |
| 11~14 | DB4 ~ DB7 | for data transfer between the MPU and the LCD module. |
| | | DB7 can be used as a busy flag. |

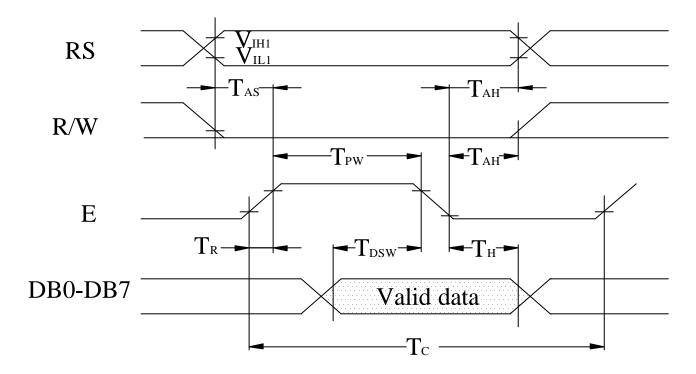
Contrast Adjust



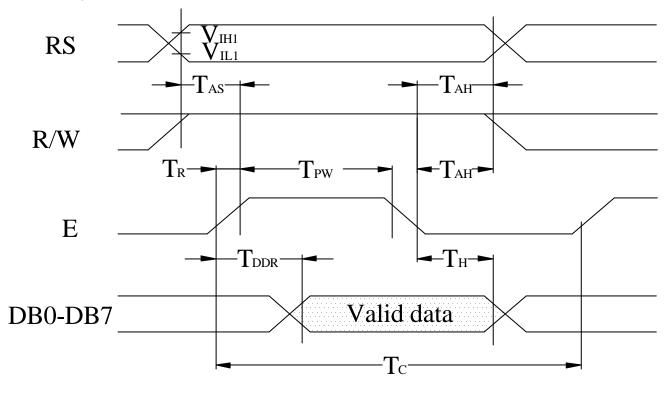


2.3 Timing Characteristics

• Writing data from MPU to ST7066U



Reading data from ST7066U to MPU





• Write Mode (Writing data from MPU to ST7066U)

 $(Vcc = +5V,Ta=25^{\circ}C)$

| Symbol | Characteristics | Test Condition | Min. | Тур. | Max. | Unit |
|------------------|-------------------------|-----------------|------|------|------|------|
| $T_{\rm C}$ | Enable Cycle Time | Pin E | 1200 | - | 1 | ns |
| T_{PW} | Enable Pulse Width | Pin E | 140 | - | - | ns |
| T_R, T_F | Enable Rise / Fall Time | Pin E | - | - | 25 | ns |
| T _{AS} | Address Setup Time | Pins: RS , RW,E | 0 | 1 | 1 | ns |
| T_{AH} | Address Hold Time | Pins :RS,RW,E | 10 | 1 | 1 | ns |
| T_{DSW} | Data Setup Time | Pins:DB0~DB7 | 40 | - | | ns |
| T_{H} | Data Hold Time | Pins:DB0~DB7 | 10 | - | - | ns |

• Read Mode (Reading data from ST7066U to MPU)

 $(Vcc = +5V,Ta=25^{\circ}C)$

| Symbol | Characteristics | Test Condition | Min. | Тур. | Max. | Unit |
|--------------------|-------------------------|-----------------|------|------|------|------|
| $T_{\rm C}$ | Enable Cycle Time | Pin E | 1200 | 1 | 1 | ns |
| T_{PW} | Enable Pulse Width | Pin E | 140 | - | 1 | ns |
| T_R , T_F | Enable Rise / Fall Time | Pin E | - | - | 25 | ns |
| T _{AS} | Address Setup Time | Pins: RS , RW,E | 0 | - | 1 | ns |
| T_{AH} | Address Hold Time | Pins :RS,RW,E | 10 | - | - | ns |
| T_{DDR} | Data Setup Time | Pins:DB0~DB7 | - | - | 100 | ns |
| T_{H} | Data Hold Time | Pins:DB0~DB7 | 10 | - | - | ns |



2.4 Display Command

| | | | | | Instru | ıction | Code | | | Description | | |
|--------------|--------|---|----|----|-----------|-----------|------|----------------|-----|-------------|--|------------------|
| Instructions | RS R/W | | DB | DB | DB | DB | DB | DB | DB | DB | Description | Time (270KHz) |
| | | | 7 | 6 | 5 | 4 | 3 | 2 | 1 | 0 | | (270121) |
| Clear | | | | | | | | | | | Write "20H" to DDRAM. and set | |
| Display | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 1 | DDRAM address to "00H" from | 1.52ms |
| 2 ispiny | | | | | | | | | | | AC. | |
| | | | | | | | | | | | Set DDRAM address to "00H" | |
| Return | | | | | | | | | | | from AC and return cursor to it's | |
| Home | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 1 | × | original position if shifted. | 1.52ms |
| 1101110 | | | | | | | | | | | The contents of DDRAM are not | |
| | | | | | | | | | | | changed. | |
| | | | | | | | | | | | Sets cursor move direction and | |
| Entry Mode | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 1 | I/D | S | specifies display shift. These | 37us |
| Set | | | | | | | | | | | operations are performed during | |
| | | | | | | | | | | | data write and read . | |
| Display | | | | | | | | | | | D=1 : entire display on | |
| ON/OFF | 0 | 0 | 0 | 0 | 0 | 0 | 1 | D | С | В | C=1 : cursor on | 37µs |
| | | | | | | | | | | | B=1 : cursor position on | |
| Cursor or | | | | | | | | | | | Set cursor moving and display | |
| Display | 0 | 0 | 0 | 0 | 0 | 1 | S/C | R/L | × | × | shift control bit, and the direction, | 37µs |
| Shift | | U | U | U | U | 1 | 5/0 | IVL | | | without changing of DDRAM | 57μ5 |
| Simt | | | | | | | | | | | data. | |
| Function | | | | | | | | | | | DL: interface data is 8/4 bits | |
| | 0 | 0 | 0 | 0 | 1 | DL | N | F | × | × | NL: number of line is 2/1 | 37µs |
| Set | | | | | | | | | | | F: font size is $5 \times 11/5 \times 8$ | |
| Set | | | | | AC | AC | AC | AC | AC | ۸С | Set CGRAM address in address | |
| CGRAM | 0 | 0 | 0 | 1 | 5 AC | AC 4 | 3 | $\frac{AC}{2}$ | | | | 37µs |
| Address | | | | | <u> </u> | 4 | 3 | | 1 | 0 | counter. | |
| Set | | | | ۸С | AC | AC | ۸С | 100 | 10 | ۸С | Set DDRAM address in address | |
| DDRAM | 0 | 0 | 1 | AC | AC 5 | AC 4 | AC 3 | AC 2 | AC | | | 37µs |
| Address | | | | 6 | J | 4 | 3 | | 1 | 0 | counter. | |



| Read Busy Flag and Address | 0 | 1 | BF | AC 6 | AC 5 | AC 4 | AC 3 | AC 2 | AC | | Whether during internal operation or not can be known by reading BF. The contents of address counter can also be read. | 0µs |
|----------------------------------|---|---|----|---------|---------|---------|------|------|----|----|--|------|
| Write Data to RAM | 1 | 0 | D7 | D6 | D5 | D4 | D3 | D2 | D1 | D0 | Write data into internal RAM (DDRAM/CGRAM). | 37µs |
| Read Data from RAM | 1 | 1 | D7 | D6 | D5 | D4 | D3 | D2 | D1 | D0 | Read data from internal RAM (DDRAM/CGRAM). | 37µs |

Note:

Be sure the ST7066U is not in the busy state (BF=0) before sending an instruction from the MPU to the ST7066.

If an instruction is sent without checking the busy flag , the time between the first instruction and next instruction will take much longer than the instruction time itself.

Refer to Instruction Table for the list of each instruction execution time .



2.5 Character Pattern

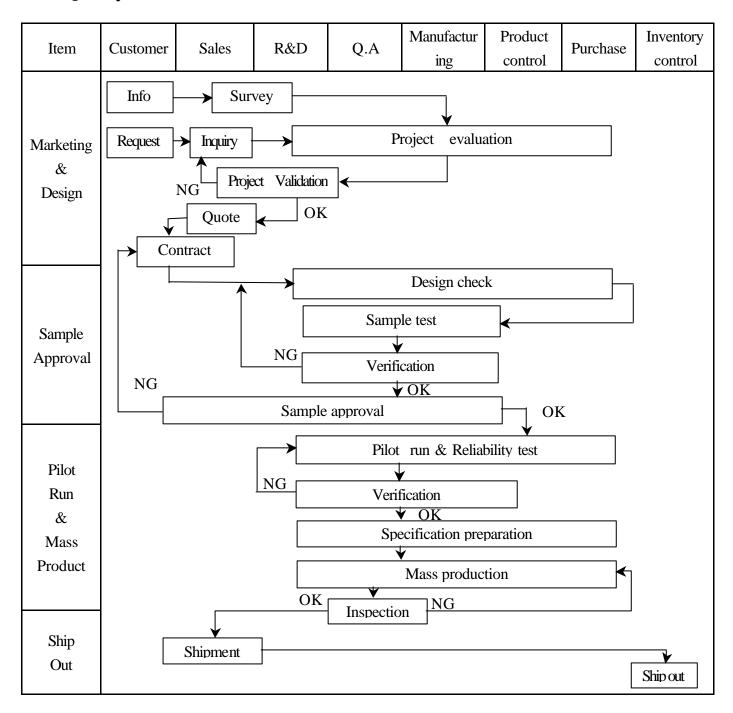
■ CHARACTER PATTERN(SO/HO/EA,WA)

| Upper 4 Bits 4 Bits | 0000 | 0001 | 0010 | 0011 | 0100 | 0101 | 0110 | 0111 | 1000 | 1001 | 1010 | 1011 | 1100 | 1101 | 1110 | 1111 |
|---------------------------|------------------|------|-----------------|----------------|----------|--------------|----------|---------------|------|------|----------------|---------|-------|-------------------|-----------------|--------------|
| xxxx0000 | CG RAM (1) | | | | | | •• | : - | | | | | -53 | ₩. | | ! |
| xxxx0001 | (2) | | 1 | 1. | | | -=== | -== | | | === | | === | £; | - | : ::: |
| xxxx0010 | (3) | | :: | | | | | ! | | | Ē | ٠ | : : : | _:-: [:] | | : |
| xxxx0011 | (4) | | # | | | ==== | : | ::::- | | | | | ·# | | ε. | ::-: |
| xxxx0100 | (5) | | | :: : [. | | ···· | | ŧ. | | | ٠. | <u></u> | ŀ- | - | ļi | <u></u> |
| xxxx0101 | (6) | | ::-: <u>:</u> : | | | <u></u> | ==== | 11 | | | :: | := | :: | | :::: | <u></u> |
| xxxx0110 | (7) | | | | | IJ | -ŧ | II | | | | | | | | E |
| xxxx0111 | (8) | | := | | | | - | 1,1,1 | | | _::· | = | [3:3] | | | 31 |
| xxxx1000 | (1) | | | | | <u> </u> ::: | |]::: <u>[</u> | | | i ⁻ | -:::] | | Ļ | .,i'' | :-:; |
| xxxx1001 | (2) | | <u> </u> | | I | | 1 | -::: | | | | -"]" | | 11. | : | i |
| xxxx1010 | (3) | | :-[-: | : | | | i | :::: | | | | | · `: | <u>.</u> | .] | |
| xxxx1011 | (4) | | [| :: | H: | | l-: | 4 | | | ::: <u> </u> | | | | :-: | |
| xxxx1100 | (5) | | := | -:[| i | | I. | i | | | -[-:- | ::: | -::: | :- <u>-</u> _: | | |
| xxxx1101 | (6) | | | | | | [**] | 3 | | - | .::. | : | ٠٠. | : | ŧ | ·÷· |
| xxxx1110 | (7) | | | > | | ···. |]-"; | | | | | | | ٠,٠- | F 1 | |
| xxxx1111 | (8) | | | : | | | :: | : | | 1 | ::: | ·! | -: | === | | |

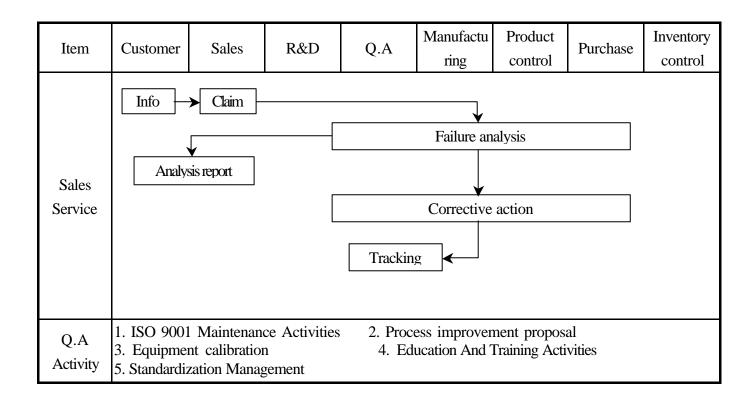


3. QUALITY ASSURANCE SYSTEM

3.1 Quality Assurance Flow Chart









3.2 Inspection Specification

Inspection Standard: MIL-STD-105E Table Normal Inspection Single Sampling Level

Equipment: Gauge, MIL-STD, Powertip Tester, Sample,

IQC Defect Level: Major Defect AQL 0.4; Minor Defect AQL 1.5.

FQC Defect Level: 100% Inspection. OUT Going Defect Level: Sampling.

Specification:

| NO | Item | Specification | Judge | Level |
|----|--|---|-------|------------|
| 1 | Part Number | The part number is inconsistent with work order of production | N.G. | Major |
| 2 | Quantity | The quantity is inconsistent with work order of production | N.G. | Major |
| | Electronic | The display lacks of some patterns. | N.G. | Major |
| | characteristics of | Missing line. | N.G. | Major |
| 3 | | LCM The size of missing dot, A is $> 1/2$ Dot size | N.G. | Major |
| | $A=(L+W) \div 2$ | There is no function. | N.G. | Major |
| | | Output data is error | N.G. | Major |
| | | Material is different with work order of production | N.G. | Major |
| | | LCD is assembled in inverse direction | N.G. | Major |
| | | Bezel is assembled in inverse direction | N.G. | Major |
| | | Shadow is within LCD viewing area + 0.5 mm | N.G. | Major |
| | Appearance of | The diameter of dirty particle, A is $> 0.4 \text{ mm}$ | N.G. | Minor |
| | $\begin{array}{c} LCD \\ A=(L+W) \div 2 \end{array}$ | Dirty particle length is > 3.0mm, and 0.01mm < width 0.05mm | N.G. | Minor |
| 4 | Distry moutials | Display is without protective film | N.G. | Minor |
| | Dirty particle (Including | Conductive rubber is over bezel 1mm | N.G. | Minor |
| | scratch, bubble) | Polarizer exceeds over viewing area of LCD | N.G. | Minor |
| | scratcik bubble j | Area of bubble in polarizer, A > 1.0mm, the number of bubble is > 1 piece. | N.G. | Minor |
| | | 0.4mm < Area of bubble in polarizer, A < 1.0mm, the number of bubble is > 4 pieces. | N.G. | Minor |
| | | Burned area or wrong part number is on PCB | N.G. | Major |
| | | The symbol, character, and mark of PCB are unidentifiable. | N.G | Minor |
| | | The stripped solder mask, A is > 1.0mm | N.G. | Minor |
| | | 0.3mm < stripped solder mask or visible circuit, A < | NC | M : |
| _ | Appearance of | 1.0mm, and the number is 4 pieces | N.G. | Minor |
| 5 | PCB | There is particle between the circuits in solder mask | N.G | Minor |
| | $A=(L+W) \div 2$ | The circuit is peeled off or cracked | N.G | Minor |
| | | There is any circuits risen or exposed. | N.G | Minor |
| | | 0.2mm < Area of solder ball, A is 0.4mm The number of solder ball is 3 pieces | N.G | Minor |
| | | The magnitude of solder ball, A is > 0.4mm. | N.G | Minor |



| NO | Item | Specification | Judge | Level |
|----|---------------------------------|--|-------|-------|
| | | The shape of modeling is deformed by touching. | N.G. | Major |
| | Appearance of | Insufficient epoxy: Circuit or pad of IC is visible | N.G. | Minor |
| 6 | molding $A=(L+W) \div 2$ | Excessive epoxy: Diameter of modeling is > 20mm or height is > 2.5mm | N.G. | Minor |
| | | The diameter of pinhole in modeling, A is > 0.2mm. | N.G. | Minor |
| | | The folding angle of frame must be $> 45 + 10$ | N.G. | Minor |
| 7 | Appearance of frame | The area of stripped electroplate in top-view of frame, A is > 1.0mm. | N.G. | Minor |
| 7 | $A=(L+W) \div 2$ | Rust or crack is (Top view only) | N.G. | Minor |
| | | The scratched width of frame is > 0.06mm. (Top view only) | N.G. | Minor |
| | F14-21 | The color of backlight is nonconforming | N.G. | Major |
| | Electrical | Backlight can't work normally. | N.G. | Major |
| 8 | characteristic of backlight | The LED lamp can't work normally | N.G. | Major |
| 8 | | The unsoldering area of pin for backlight, A is > 1/2 solder joint area. | N.G. | Minor |
| | $A=(L+W) \div 2$ | The height of solder pin for backlight is > 2.0mm | N.G. | Minor |
| | | The mark or polarity of component is unidentifiable. | N.G. | Minor |
| | | The height between bottom of component and surface of the PCB is floating > 0.7mm | N.G. | Minor |
| 10 | Assembly parts $A=(L+W) \div 2$ | D > 1/4W W D D Pad | N.G. | Minor |
| | 11 (2 1 11) 1 2 | End solder joint width, D' is > 50% width of component termination or width of pad | N.G. | Minor |
| | | Side overhang, D is > 25% width of component termination. | N.G. | Minor |
| | | Component is cracked, deformed, and burned, etc. | N.G. | Minor |
| | | The polarity of component is placed in inverse direction. | N.G. | Minor |
| | | Maximum fillet height of solder extends onto the component body or minimum fillet height is < 0.5mm. | N.G. | Minor |



4. RELIABILITY TEST

4.1 Reliability Test Condition

| NO | Item | Test Co | ondition | | | | | |
|----|---------------------------------------|--|---|--|--|--|--|--|
| 1 | High Temperature Storage | Storage at $80 \pm 2 96 \sim 100 \text{ hrs}$ Surrounding temperature, then storage at normal condition 4hrs | | | | | | |
| 2 | Low Temperature Storage | Storage at -30 ± 2 96~100 hrs Surrounding temperature, then storage at normal condition 4hrs | | | | | | |
| 3 | High Temperature /Humidity Storage | 1.Storage 96~100 hrs 60 ± 2 , 90~95%RH surrounding temperature, then storage at normal condition 4hrs. (Excluding the polarizer). or 2.Storage 96~100 hrs 40 ± 2 , 90~95%RH surrounding temperature, then storage at normal condition 4 hrs. | | | | | | |
| 4 | Temperature Cycling | -20 25 70 25 (30mins) (5mins) (30mins) (5mins) 10 Cycle | | | | | | |
| 5 | Vibration | 10~55Hz (1 minute) 1.5mm X,Y and Z direction * (each 2hrs) | | | | | | |
| 6 | ESD Test | Air Discharge: Apply 6 KV with 5 times discharge for each polarity +/- Testing location: Around the face of LCD | Contact Discharge: Apply 250V with 5 times discharge for each polarity +/- Testing location: 1.Apply to bezel. 2.Apply to Vdd, Vss. | | | | | |
| 7 | Drop Test | Packing Weight (Kg) 0 ~ 45.4 45.4 ~ 90.8 90.8 ~ 454 Over 454 | Drop Height (cm) 122 76 61 46 | | | | | |



5. PRECAUTION RELATING PRODUCT HANDLING

5.1 SAFETY

- 5.1.1 If the LCD panel breaks, be careful not to get the liquid crystal to touch your skin.
- 5.1.2 If the liquid crystal touches your skin or clothes, please wash it off immediately by using soap and water.

5.2 HANDLING

- 5.2.1 Avoid any strong mechanical shock which can break the glass.
- 5.2.2 Avoid static electricity which can damage the CMOS LSI—When working with the module, be sure to ground your body and any electrical equipment you may be using.
- 5.2.3 Do not remove the panel or frame from the module.
- 5.2.4 The polarizing plate of the display is very fragile. So , please handle it very carefully ,do not touch , push or rub the exposed polarizing with anything harder than an HB pencil lead (glass , tweezers , etc.)
 - 5.2.5 Do not wipe the polarizing plate with a dry cloth, as it may easily scratch the surface of plate.
- 5.2.6 Do not touch the display area with bare hands, this will stain the display area.
- 5.2.7 Do not use ketonics solvent & aromatic solvent. Use with a soft cloth soaked with a cleaning naphtha solvent.
- 5.2.8 To control temperature and time of soldering is 280 ± 10 and 3-5 sec.
- 5.2.9 To avoid liquid (include organic solvent) stained on LCM.

5.3 STORAGE

- 5.3.1 Store the panel or module in a dark place where the temperature is 25 ± 5 and the humidity is below 65% RH.
- 5.3.2 Do not place the module near organics solvents or corrosive gases.
- 5.3.3 Do not crush, shake, or jolt the module.

5.4 TERMS OF WARRANTY

- 5.4.1 Applicable warrant period
 - The period is within thirteen months since the date of shipping out under normal using and storage conditions.
- 5.4.2 Unaccepted responsibility
 - This product has been manufactured to your company's specification as a part for use in your company's general electronic products. It is guaranteed to perform according to delivery specifications. For any other use apart from general electronic equipment, we cannot take responsibility if the product is used in nuclear power control equipment, aerospace equipment, fire and security systems or any other applications in which there is a direct risk to human life and where extremely high levels of reliability are required.

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