



DDTA (R1 = R2 SERIES) EE

PNP PRE-BIASED SMALL SIGNAL SURFACE MOUNT TRANSISTOR

Features

- Epitaxial Planar Die Construction
- Complementary NPN Types Available (DDTC)
- Built-In Biasing Resistors, R1 = R2
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- Qualified to AEC-Q101 Standards for High Reliability

Mechanical Data

- Case: SOT523
- Case Material: Molded Plastic, "Green" Molding Compound
- UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Finish Matte Tin Plated Leads, Solderable per MIL-STD-202, Method 208 (@)
- Weight: 0.002 grams (Approximate)

	Part Number	R1, R2 (NOM)	1
	DDTA123EE	2.2kΩ	1
	DDTA143EE	4.7kΩ	
	DDTA114EE	10kΩ	
	DDTA124EE	22kΩ	
	DDTA144EE	47kΩ	
	DDTA115EE	100kΩ	
SOT523		ОUТ 3	\sim
		R2 G ND (+)	IN <u>B</u> 1 2 <u>E</u> GND (0)







Ordering Information (Note 4)

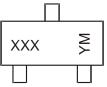
-					
Product	Compliance	Marking	Reel Size (inches)	Tape Width (mm)	Quantity per Reel
DDTA123EE-7-F	AEC-Q101	P04	7	8	3,000
DDTA143EE-7-F	AEC-Q101	P08	7	8	3,000
DDTA114EE-7-F	AEC-Q101	P13	7	8	3,000
DDTA124EE-7-F	AEC-Q101	P17	7	8	3,000
DDTA144EE-7-F	AEC-Q101	P20	7	8	3,000
DDTA115EE-7-F	AEC-Q101	P24	7	8	3,000

No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant.
See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.

3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.

4. For packaging details, go to our website at https://www.diodes.com/design/support/packaging/diodes-packaging/.

Marking Information



XXX = Product Type Marking Code, See Table Above YM =_Date Code Marking Y or Y = Year (ex: F = 2018) M = Month (ex: 9 = September)

Date Code Key

Notes:

Year	2018	2019	2020	2021	202	2 20	23	2024	2025	2026	2027	2028
Code	F	G	Н	I	J	I	<	L	М	Ν	0	Р
Month	Jan	Feb	Mar	Apr	Мау	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Code	1	2	3	4	5	6	7	8	9	0	N	D



Maximum Ratings (@T_A = +25°C, unless otherwise specified.)

	Characteristic	Symbol	Value	Unit
Supply Voltage <pin: (3)<="" th=""><th>) to (2)></th><th>Vcc</th><th>50</th><th>V</th></pin:>) to (2)>	Vcc	50	V
Input Voltage <pin: (1)="" (2)="" to=""></pin:>	DDTA123EE DDTA143EE DDTA114EE DDTA124EE DDTA124EE DDTA144EE DDTA115EE	V _{IN}	+10 to -12 +10 to -30 +10 to -40 +10 to -40 +10 to -40 +10 to -40	V
Output Current	DDTA123EE DDTA143EE DDTA114EE DDTA124EE DDTA124EE DDTA144EE DDTA115EE	lo	-100 -100 -50 -30 -30 -20	mA
Output Current	·	I _C (Max)	-100	mA

Thermal Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit
Power Dissipation (Note 5 & 6)	PD	150	mW
Thermal Resistance, Junction to Ambient Air (Note 5)	R _{0JA}	833	°C/W
Operating and Storage Temperature Range	T _J , T _{STG}	-55 to +150	°C

Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

Cha	aracteristic	Symbol	Min	Тур	Max	Unit	Test Condition
		V _{I(OFF)}	-0.5	-1.1	—		$V_{CC} = -5V, I_{O} = -100\mu A$
Input Voltage		V _{I(ON)}	_	-1.9	-3	V	$ \begin{array}{l} V_O = -0.3V, \ I_O = -20mA, \ DDTA123EE \\ V_O = -0.3V, \ I_O = -20mA, \ DDTA143EE \\ V_O = -0.3V, \ I_O = -10mA, \ DDTA114EE \\ V_O = -0.3V, \ I_O = -5mA, \ DDTA124EE \\ V_O = -0.3V, \ I_O = -2mA, \ DDTA144EE \\ V_O = -0.3V, \ I_O = -1mA, \ DDTA115EE \\ \end{array} $
Output Voltage		Vo(on)		-0.1	-0.3	V	$\begin{array}{l} I_{O}/I_{I} = -10 \text{mA}/-0.5 \text{mA} & \text{DDTA123EE} \\ I_{O}/I_{I} = -10 \text{mA}/-0.5 \text{mA} & \text{DDTA143EE} \\ I_{O}/I_{I} = -10 \text{mA}/-0.5 \text{mA} & \text{DDTA114EE} \\ I_{O}/I_{I} = -10 \text{mA}/-0.5 \text{mA} & \text{DDTA124EE} \\ I_{O}/I_{I} = -10 \text{mA}/-0.5 \text{mA} & \text{DDTA124EE} \\ I_{O}/I_{I} = -5 \text{mA}/-0.25 \text{mA} & \text{DDTA115EE} \end{array}$
Input Current	DDTA123EE DDTA143EE DDTA114EE DDTA124EE DDTA124EE DDTA115EE	h	_	_	-3.8 -1.8 -0.88 -0.36 -0.18 -0.15	mA	V ₁ = -5V
Output Current		I _{O(OFF)}			-0.5	μA	$V_{CC} = -50V, V_1 = 0V$
DC Current Gain	DDTA123EE DDTA143EE DDTA114EE DDTA124EE DDTA124EE DDTA144EE DDTA115EE	Gı	-20 -20 -30 -56 -68 -82		_	_	$ \begin{array}{l} V_{O} = -5V, \ I_{O} = -20mA \\ V_{O} = -5V, \ I_{O} = -10mA \\ V_{O} = -5V, \ I_{O} = -5mA \end{array} $
Input Resistor Tolerance		ΔR_1	-30		+30	%	_
Resistance Ratio Tolera	nce	$\Delta R_2/R_1$	0.8	1	1.2	%	_
Gain-Bandwidth Produc	t (Note 7)	f⊤		250		MHz	$V_{CE} = -10V, I_E = 5mA,$ f = 100MHz

 Mounted on FR-4 PC Board with minimum recommended pad layout.
150mW per element must not be exceeded.
Transistor only. Notes:



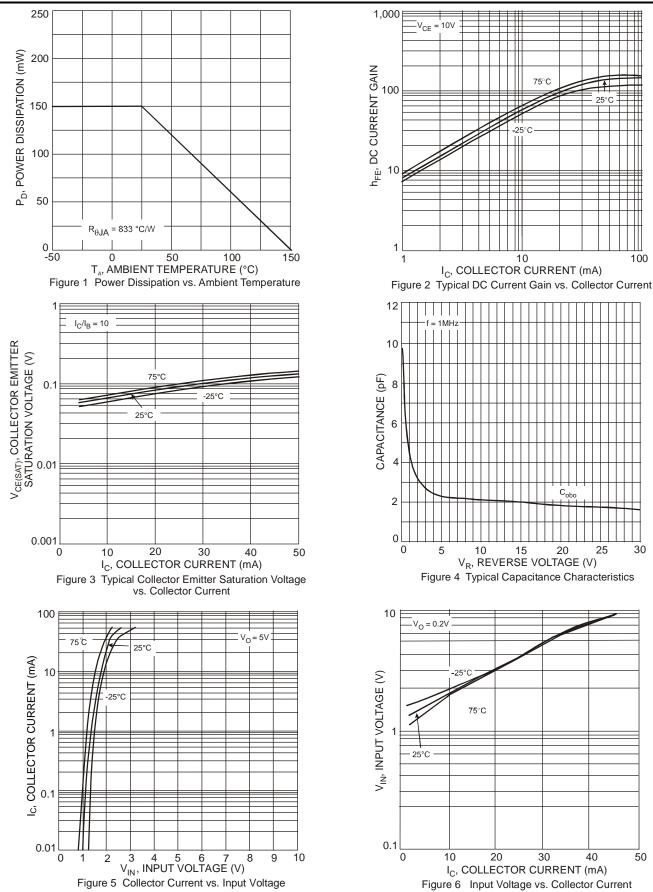
25°C

25

30

100

Typical Electrical Characteristics – DDTA143EE



DDTA(R1 = R2 SERIES) EE Document number: DS30317 Rev. 9 - 2

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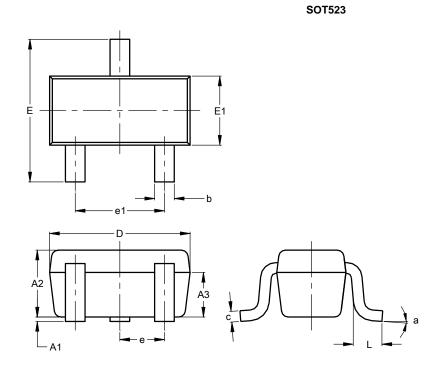
50

40



Package Outline Dimensions

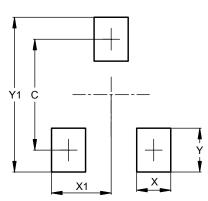
Please see http://www.diodes.com/package-outlines.html for the latest version.



SOT523						
Dim	Min	Max	Тур			
A1	0.00	0.10	0.05			
A2	0.60	0.80	0.75			
A3	0.45	0.65	0.50			
b	0.15	0.30	0.22			
С	0.10	0.20	0.12			
D	1.50	1.70	1.60			
Е	1.45	1.75	1.60			
E1	0.75	0.85	0.80			
e		0.50 BS	С			
e1	0.90	1.10	1.00			
L	0.20	0.40	0.33			
а	0°		8°			
Α	II Dimen	isions ir	n mm			

Suggested Pad Layout

Please see http://www.diodes.com/package-outlines.html for the latest version.



Dimensions	Value (in mm)
С	1.29
Х	0.40
X1	0.70
Y	0.51
Y1	1.80

SOT523



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