OLED DISPLAY MODULE

Product Specification

CUSTOMER	**Standard**	
PRODUCT NUMBER	**DD-160128FC-1A**	
CUSTOMER APPROVAL		**Date**

INTERNAL APPROVALS		
Product Mgr	Doc. Control	Electr. Eng
Bruno Recaldini	**Anthony Perkins**	**Bazile Peter**
Date: 10Jul06	Date: 10Jul06	Date: 10Jul06

☐ **Approval for Specification only**

☐ **Approval for Specification and Sample**

TABLE OF CONTENTS

REVISION RECORD

Rev.	Date	Page	Chapt.	Comment	ECR no.
A	10-Jul-06			First Issue	

1 MAIN FEATURES

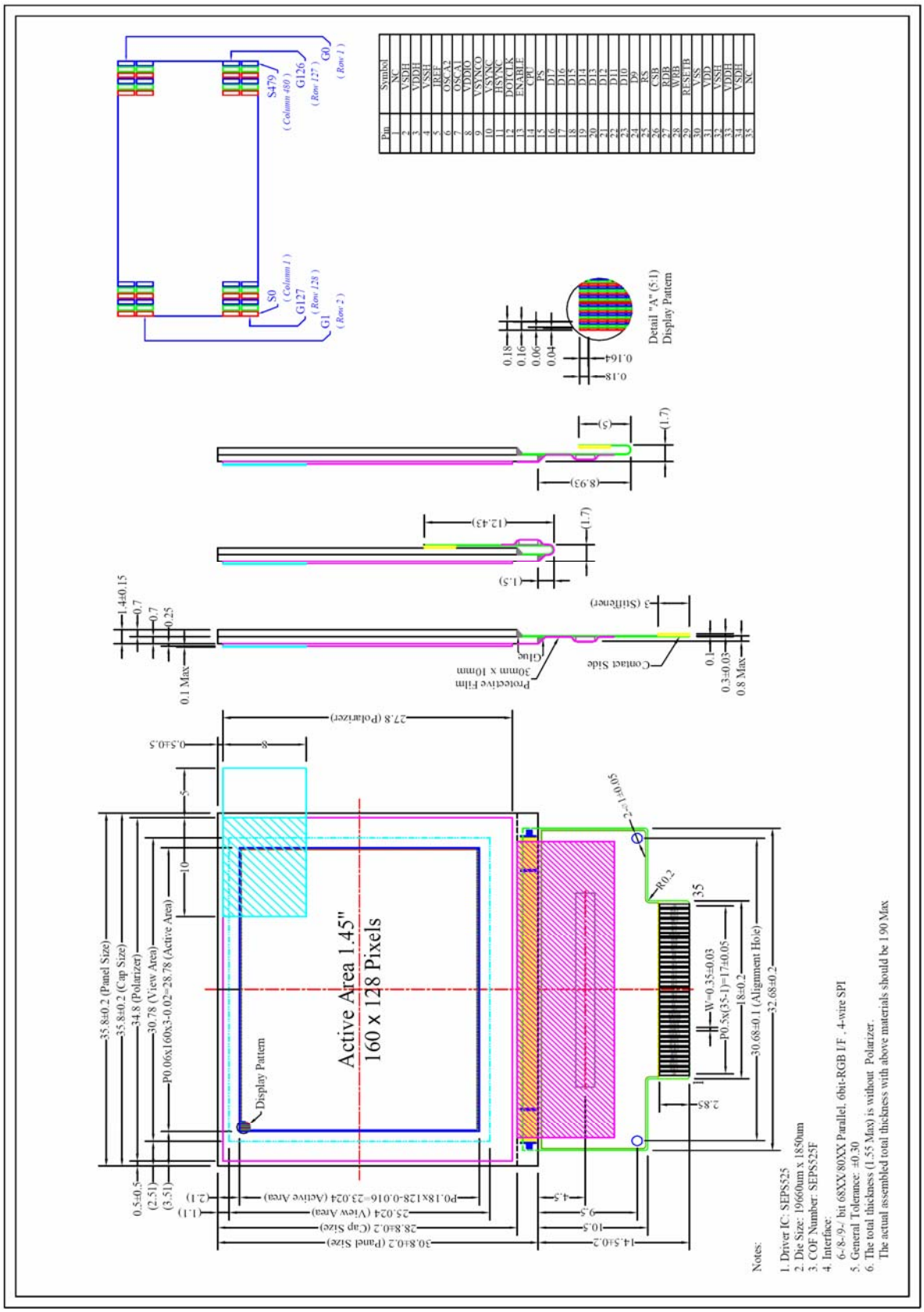
ITEM	CONTENTS
Display Format	160 (RGB) x 128 Dots
Overall Dimensions	Glass 35.8 x 30.8 x 1.7 mm
Colour	262,144 Colour
Active Area	28.78 x 23.024 mm
Viewing Area	30.78 x 25.02 mm
Display Mode	Passive Matrix (1.45”)
Driving Method	1/128 duty
Driver IC	SEPS525F
Operating temperature	-20 ~ +70
Storage temperature	-30 ~ +80

2 MECHANICAL SPECIFICATION

2.1 MECHANICAL CHARACTERISTICS

ITEM	CHARACTERISTIC	UNIT
Display Format	160 (RGB) x 128	Dots
Overall Dimensions	35.80 x 30.80 x 1.7	mm
Viewing Area	30.78 x 25.02	mm
Active Area	28.78 x 23.024	mm
Dot Size	0.04 x 0.164	mm
Dot Pitch	0.06 x 0.18	mm
Weight	3.6	g
IC Controller/Driver	SEPS525F (COF)	

2.2 MECHANICAL DRAWING



Pin	Symbol
1	NC
2	VSDH
3	VDDH
4	VSSH
5	IREF
6	OSCA2
7	OSCA1
8	VDDIO
9	VSYNCO
10	VSYNC
11	HSYNC
12	DOTCLK
13	ENABLE
14	CPH
15	PS
16	D17
17	D16
18	D15
19	D14
20	D13
21	D12
22	D11
23	D10
24	D9
25	RS
26	CSB
27	RDB
28	WRB
29	RESETB
30	VSS
31	VDD
32	VSSH
33	VDDH
34	NC
35	

3 ELECTRICAL SPECIFICATION

3.1 ABSOLUTE MAXIMUM RATINGS

VSS = 0 V, Ta = 25 °C

Item	Symbol	Min	Max	Unit	Note
Power Supply Voltage	V_{DD}-V_{SS}	-0.3	4	V	Note 1, 2
Supply Voltage for I/O Pins	V_{DDIO}	-0.3	4	V	
OLED Power Supply	V_{DDH}	-0.3	19.5	V	
Operating Temperature	Top	-30	70	°C	
Storage Temperature	Tst	-40	80	°C	
Static Electricity	Be sure that you are grounded when handling displays.				

Note 1: All the above voltages are on the basis of "GND=0V".

Note 2: When this module is used beyond the above absolute maximum ratings, permanent damage to the module may occur. Also for normal operations it's desirable to use this module under the conditions according to Section 3.2 "Electrical Characteristics". If this module is used beyond these conditions the module may malfunction and the reliability could deteriorate.

3.2 ELECTRICAL CHARACTERISTICS

Characteristics	Symbol	Conditions	Min	Typ	Max	Unit
Supply Voltage	V_{DD}		2.6	2.8	3.3	V
Supply Voltage x I/O pins	V_{DDIO}		1.6	2.8	3.3	V
Driver Supply Voltage	V_{CC}	Note 3	-	13	-	V
High Level Input	V_{IH}		0.8xV_{DD}	-	V_{DDIO}	V
Low Level Input	V_{IL}		0	-	0.4	V
High Level Output	V_{OH}		V_{DD}-0.4	-	-	V
Low Level Output	V_{OL}		-	-	0.4	V
V_{DD} Current	I_{DD}	Note 1,2	-	2.5	3.5	mA
V_{CC} Current	I_{CC}	Note 1	-	16	19	mA
		Note 2	-	27	32	mA

Note 1 V_{DD} = 2.8V, V_{CC} = 13V, Software initial setting follow chapter 5.4, 50% Display area turned on.

Note 2 V_{DD} = 2.8V, V_{CC} = 13V, Software initial setting follow chapter 5.4, 100% Display area turned on

Note 3 Brightness (Lbr) and driver supply voltage (VCC) could be changed to customer request.

3.3 INTERFACE PIN ASSIGNMENT

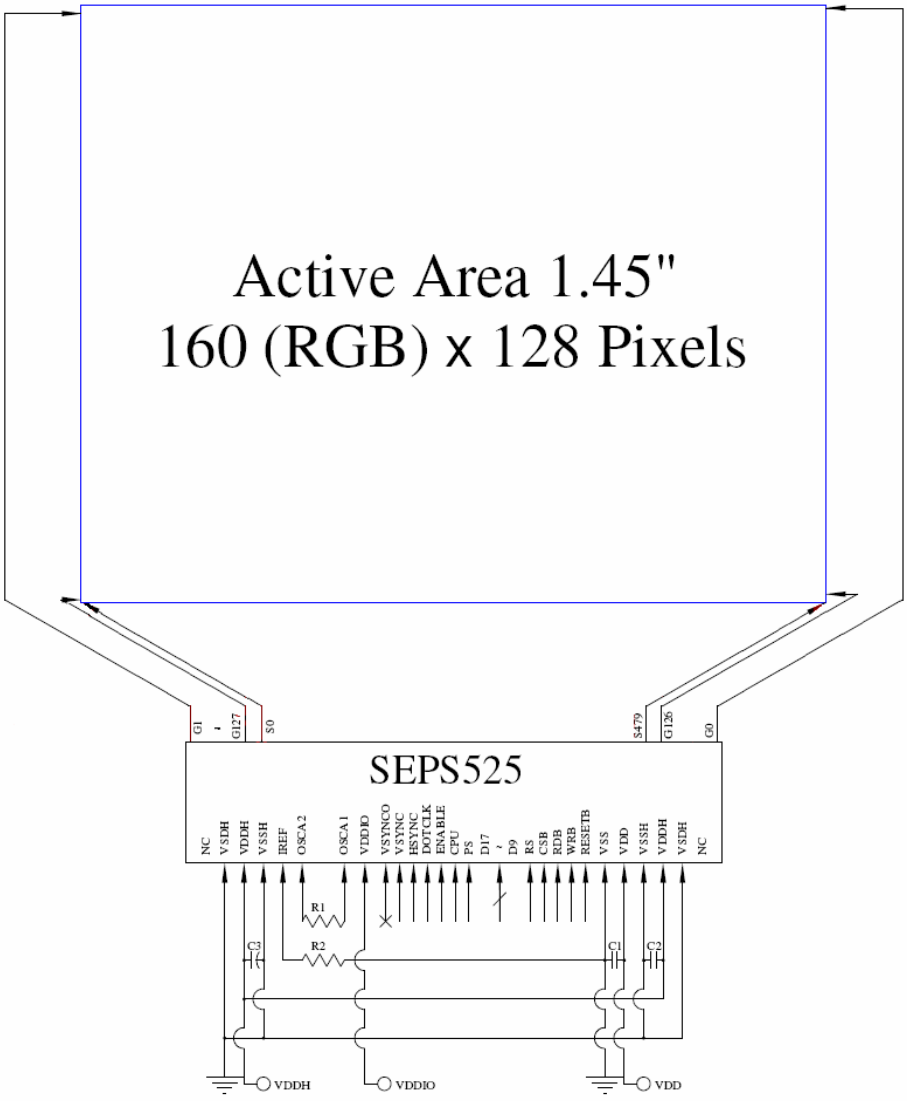
<table>
<tr><th>No.</th><th>Symbol</th><th>I/O</th><th>Function</th></tr>
<tr><td>1</td><td>N.C.</td><td>-</td><td>No Connection</td></tr>
<tr><td>2</td><td>VSDH</td><td>I</td><td>Data Driver Ground</td></tr>
<tr><td>3</td><td>VDDH</td><td>I</td><td>Data, Scan Driver Power supply</td></tr>
<tr><td>4.</td><td>VSSH</td><td>I</td><td>Scan Driver Ground</td></tr>
<tr><td>5</td><td>IREF</td><td>I/O</td><td>Current reference for brightness Adjustment
Connect 70KΩ Resistor to VSS</td></tr>
<tr><td>6</td><td>OSCA2</td><td>O</td><td rowspan="2">Fine Adjustment for Oscillation
Connect a 10KΩ Resistor between OSCA1 and OSCA2. OSCA1 is selected for External clock mode.</td></tr>
<tr><td>7</td><td>OSCA1</td><td>I</td></tr>
<tr><td>8</td><td>VDDIO</td><td>I</td><td>MPU I/F PAD Power Supply</td></tr>
<tr><td>9</td><td>VSYNCO</td><td>O</td><td rowspan="5">RGB Mode functional Pins

VSYNCO : Vertical Sync Output
VSYNC: Vertical Sync Input
HSYNC : Horizontal Sync Input
COTCLK : Dot Clock Input
ENNABLE : Video Enable Input</td></tr>
<tr><td>10</td><td>VSYNC</td><td>I</td></tr>
<tr><td>11</td><td>HSYNC</td><td>I</td></tr>
<tr><td>12</td><td>COTCLK</td><td>I</td></tr>
<tr><td>13</td><td>ENABLE</td><td>I</td></tr>
<tr><td>14</td><td>CPU</td><td>I</td><td>Select CPU type
Low: 80-Series High: 68-Series</td></tr>
<tr><td>15</td><td>PS</td><td>I</td><td>Select Parallel/Serial Interface
Low: Serial High: Parallel</td></tr>
<tr><td>16</td><td>D17</td><td rowspan="9">I/O</td><td rowspan="9">Host Data Input/Output Bus.
These pins are 9-bit bi-directional data bus to be connected with MCU data bus.

<table><tr><th>PS</th><th>Description</th></tr><tr><td>1</td><td>8-bit Bus: D17 to D10
9-bit Bus: D17 to D9</td></tr><tr><td>0</td><td>D[17] SCL: Synchronous Clock Input
D[16] SDI: Serial Data Input
D[15] SDO: Serial Data Output</td></tr></table></td></tr>
<tr><td>17</td><td>D16</td></tr>
<tr><td>18</td><td>D15</td></tr>
<tr><td>19</td><td>D14</td></tr>
<tr><td>20</td><td>D13</td></tr>
<tr><td>21</td><td>D12</td></tr>
<tr><td>22</td><td>D11</td></tr>
<tr><td>23</td><td>D10</td></tr>
<tr><td>24</td><td>D9</td></tr>
</table>

25	RS	I	***Selects Data/Command*** **Low**: Command **High**: Parameter/data
26	CSB	I	***Chip Select*** **Low**: SEPS525 is selected and can be accessed **High**: SEPS525 is not selected and cannot be accessed
27	RDB	I	***Read/Write Select or Write.*** 80-System bus interface: Read strobe signal(Active Low) 68-System bus interface: Bus enable strobe (Active High) When serial mode, fix this to VDD or VSS Level
28	WRB	I	***Write or Read/Write Select*** 80-System bus interface: Write strobe signal(active Low) 68-System bus interface: Read/write select. **Low**: Write **High**: Read When serial mode, fix this to VDD or VSS Level
29	RESETB	I	***Chip Reset*** Reset SEPS225 (Active Low)
30	VSS	I	***Power Supply Ground***
31	VDD	I	***Logic Power Supply***
32	VSSH	I	***Scan Driver Ground***
33	VDDH	I	***Data, Scan Driver Power supply***
34	VSDH	I	***Data Driver Ground***
35	N.C.	-	***No Connection***

3.4 BLOCK DIAGRAM



MCU Interface Selection: PS and CPU
Pins connected to MCU interface: D17~D9, RS, CSB, RDB, WRB, RESETB, ENABLE, DOTCLK, HSYNC and VSYNC

When RGB mode is used, D[17:12], ENABLE, DOTCLK, HSYNC and VSYNC Should follow the 6-bit RGB interface instruction. Otherwise these four inputs ENABLE, DOTCLK, HSYNC and VSYNC should be tied to VDDIO Level.

C1: 1 µF
C2, C3: 4.7µF
R1: 10 kΩ
R2: 68 kΩ

3.5 TIMING CHARACTERISTICS

3.5.1 AC CHARACTERISTICS

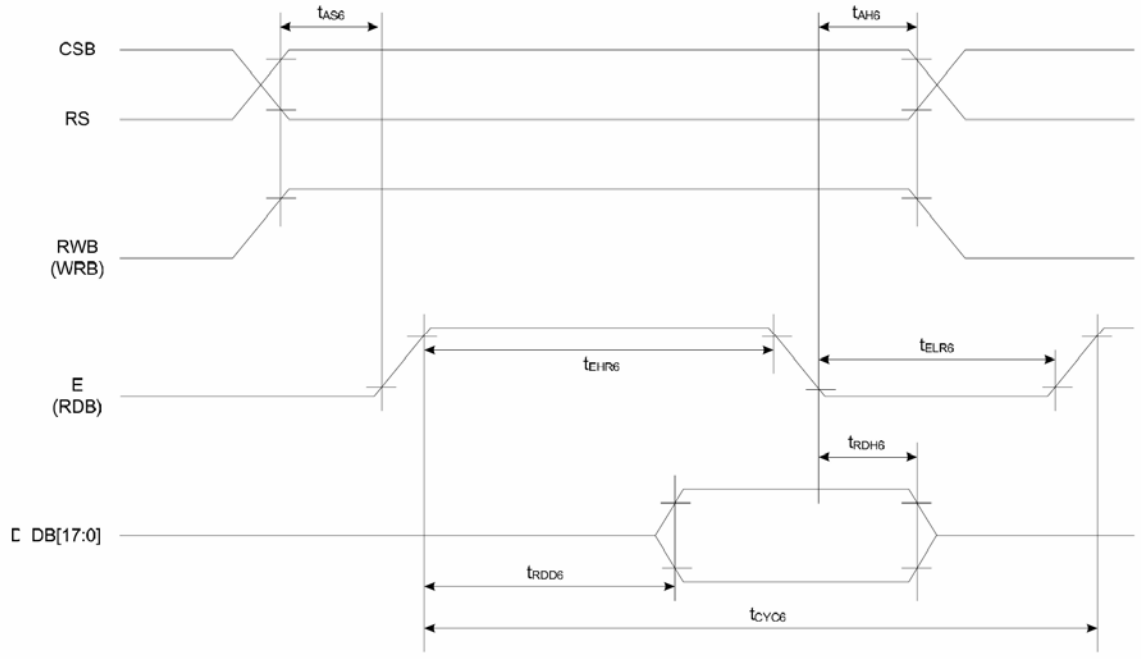
3.5.1.1 6800-Series MPU Parallel Interface Timing Characteristics

VDD = 2.8V, Ta = 25˚C

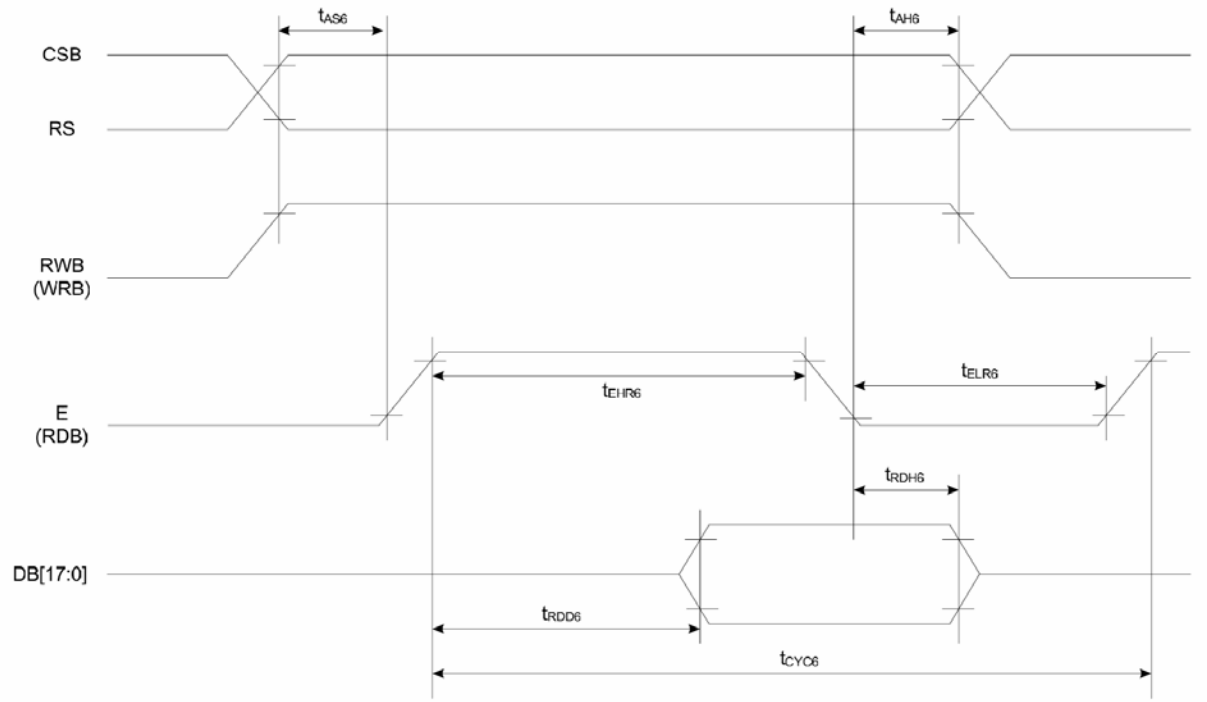
Characteristics	Symbol	Min	Max	Unit	Unit
Write Timing					
Address hold timing	tAH6	5	-	nS	CSB
Address setup timing	tAS6	5			RS
System cycle timing Write	tCYC6	100	-	nS	E
"L" pulse width Write	tELW6	45			
"H" pulse width	tEHW6	45			
Data setup timing	tDS6	40	-	nS	DB[17:0]
Data hold timing	tDH6	10			
Read Timing					
Address hold timing	tAH6	10	-	nS	CSB
Address setup timing	tAS6	10			RS
System cycle timing Write	tCYC6	200	-	nS	E
"L" pulse width Write	tELW6	90			
"H" pulse width	tEHW6	90			
Data setup timing (CL= 15pF)	tDS6	0	70	nS	DB[17:0]
Data hold timing (CL= 15pF)	tDH6				

- ***All the timing should be based on 10% and 90% of V_{DD}.***

Write Timing



Read Timing

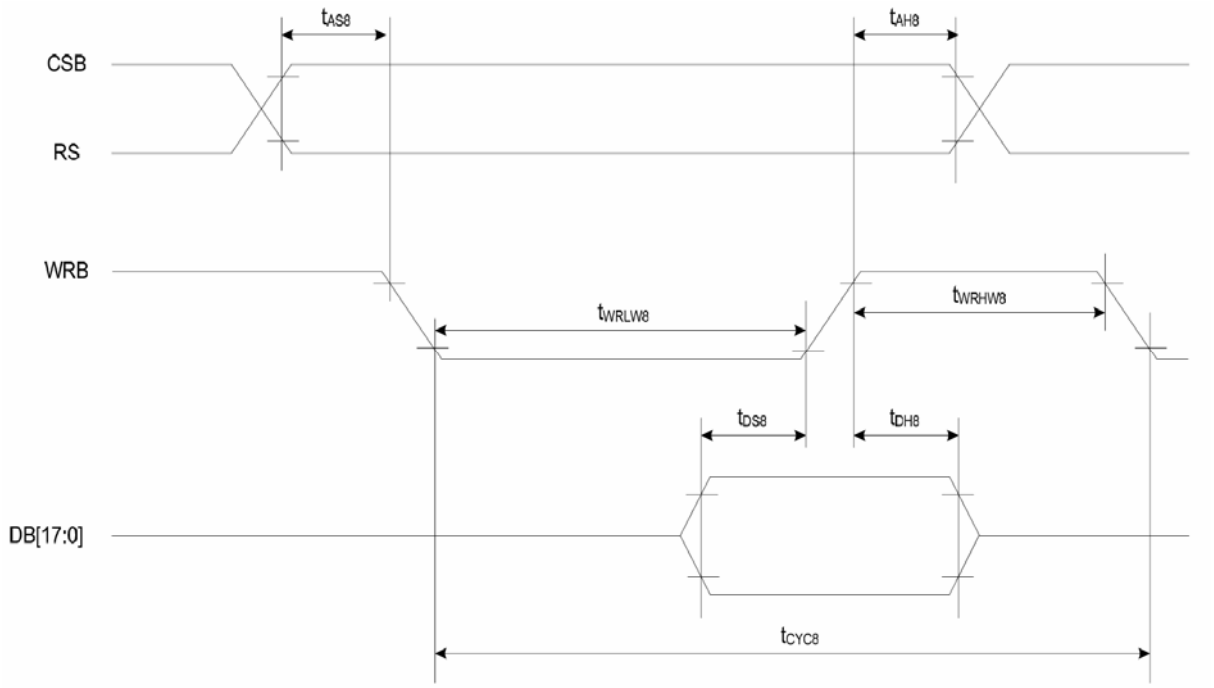


3.5.1.2 8080-Series MPU Parallel Interface Timing Characteristics

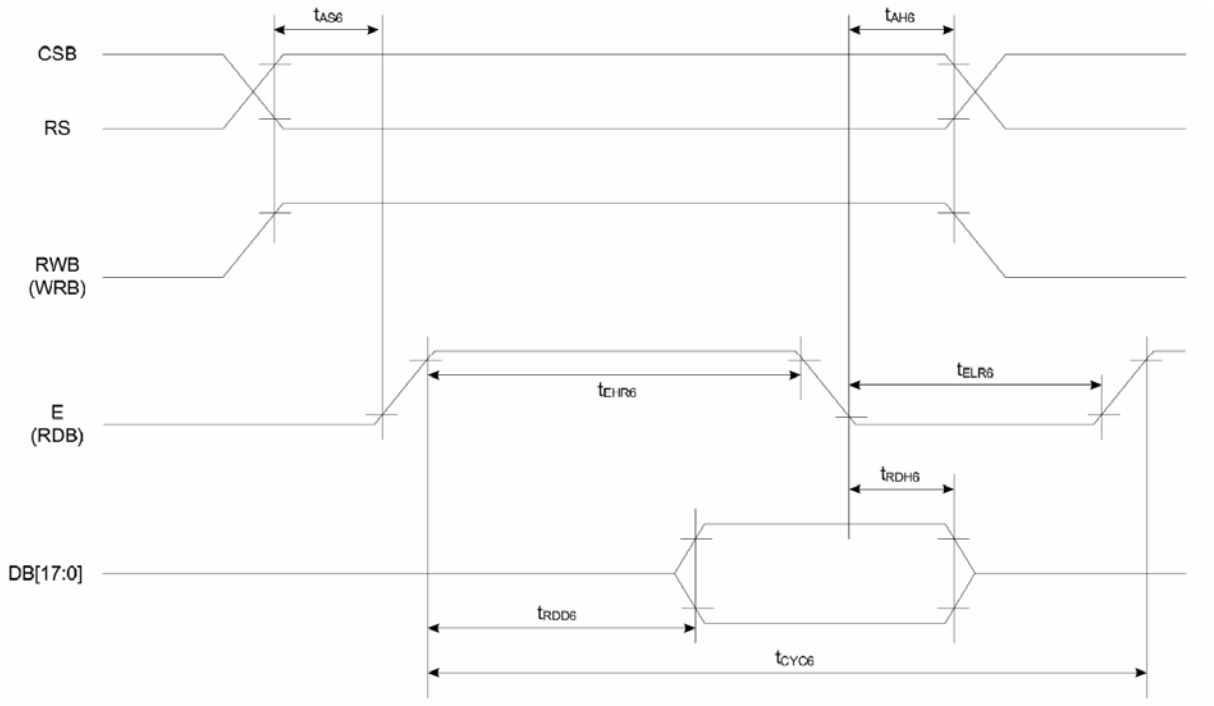
Characteristics	Symbol	Min	Max	Unit	Unit
Write Timing					
Address hold timing	tAH6	5	-	nS	CSB
Address setup timing	tAS6	5			RS
System cycle timing Write	tCYC6	100	-	nS	WRB
"L" pulse width Write	tELW6	45			
"H" pulse width	tEHW6	45			
Data setup timing	tDS6	30	-	nS	DB[17:0]
Data hold timing	tDH6	10			
Read Timing					
Address hold timing	tAH6	10	-	nS	CSB
Address setup timing	tAS6	10			RS
System cycle timing Write	tCYC6	200	-	nS	RDB
"L" pulse width Write	tELW6	90			
"H" pulse width	tEHW6	90			
Data setup timing (CL= 15pF)	tDS6	0	60	nS	DB[17:0]
Data hold timing (CL= 15pF)	tDH6				

**** All the timing should be based on 10% and 90% of V_{DD}***

Write Timing



Read timing

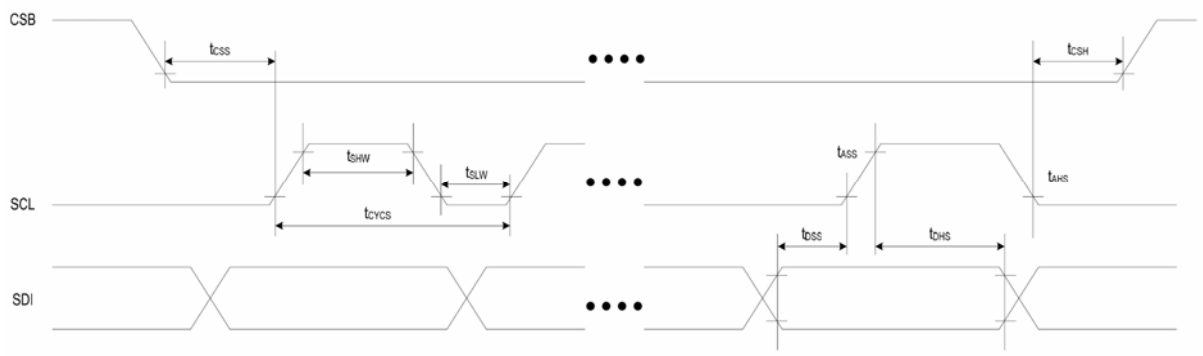


3.5.1.3 Serial Interface Timing Characteristics

ITEM	SYMBOL	MIN	MAX	UNIT	PORT
Serial clock cycle SCL	tCYCS	60	-	nS	SCL
"H" pulse width SCL	tSHW	25			
"L" pulse width	tSLW	25			
Data setup timing Data	tDSS	25	-	nS	SDI
Hold timing	tDHS	25			
CSB-SCL timing	tCSS	25	-	nS	CSB
CSB-hold timing	tCSH	25			

**** All the timing should be based on 10% and 90% of V_{DD}***

Serial Interface Timing



4 OPTICAL SPECIFICATION

4.1 OPTICAL CHARACTERISTICS

Characteristics	Symbol	Condition	Min	Typ	Max	Unit
Brightness(White)	L_{br}	Display Average Note 1	75	100	-	cd/m^2
C.I.E.(White)	(X)		0.25	0.29	0.33	-
	(Y)		0.29	0.33	0.37	
C.I.E.(Red)	(X)		0.57	0.61	0.65	-
	(Y)		0.32	0.36	0.40	
C.I.E.(Green)	(X)		0.26	0.30	0.34	-
	(Y)		0.60	0.64	0.68	
C.I.E.(Blue)	(X)		0.10	0.14	0.18	-
	(Y)		0.15	0.19	0.23	
Dark Room Contrast	CR		-	>1000:1	-	-
Viewing Angle			>160	-	-	degree

Optical measurement, follow the software initial setting on chapter 5.4

Note 1: Brightness (Lbr) and driver supply voltage (VCC) could be changed to customer request.

5 FUNCTIONAL SPECIFICATION

5.1 COMMANDS

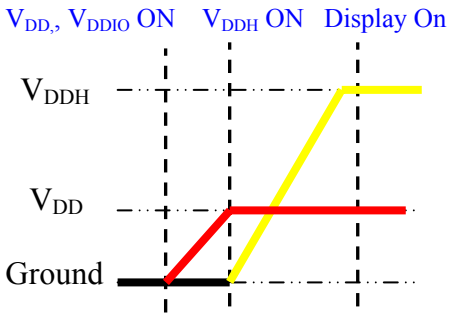
Please refer to the Technical Manual for the SEPS525F

5.2 POWER UP/DOWN SEQUENCE

To protect panel and extend the panel lifetime, the driver IC power up/down routine should include a delay period between high voltage and low voltage power sources during turn on/off. It gives the panel enough time to complete the action of charge and discharge before/after the operation.

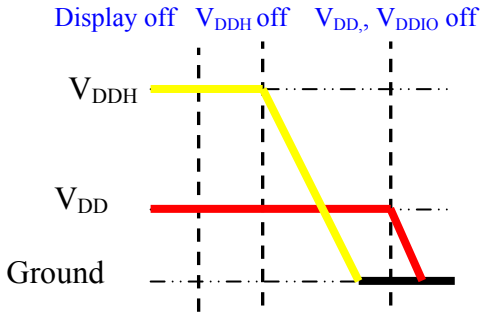
5.2.1 POWER UP SEQUENCE

1. Power up V_{DD} & $V_{DD\ IO}$
2. Send Display off command
3. Clear Screen
4. Power up V_{DDH}
5. Delay 100ms (When V_{DD} & V_{DDIO} is stable)
6. Send Display on command



5.2.2 POWER DOWN SEQUENCE

1. Send Display off command
2. Power down V_{DDH}
3. Delay 100ms
   (When V_{DDH} reach 0 and panel is completely discharges)
4. Power down V_{DD} & V_{DDIO}



5.3 RESET CIRCUIT

When RESETB input is low, the chip is initialized with the following status:

1. Frame frequency: 90Hz
2. OSC: internal OSC
3. Internal OSC: ON
4. DDRAM write horizontal address: MX1 = 00h, MX2 = 9Fh
5. DDRAM write vertical address: MY1 = 00h, MY2 = 7Fh
6. Display data RAM write: HC = 1, VC = 1, HV = 0
7. RGB data swap: OFF
8. Row scan shift direction: G0, G1, … , G126, G127
9. Column data shift direction: S0, S1, … , S478, S479
10. Display ON/OFF: OFF
11. Panel display size: FX1 = 00h, FX2 = 9Fh, FY1 = 00h, FY2 = 7Fh
12. Display data RAM read column/row address: FAC = 00h, FAR = 00h
13. Pre-charge time(R/G/B): 0 clock
14. Pre-charge current(R/G/B): 0 uA
15. Driving current(R/G/B): 0 uA

5.4 ACTUAL APPLICATION EXAMPLE

Initial Code:

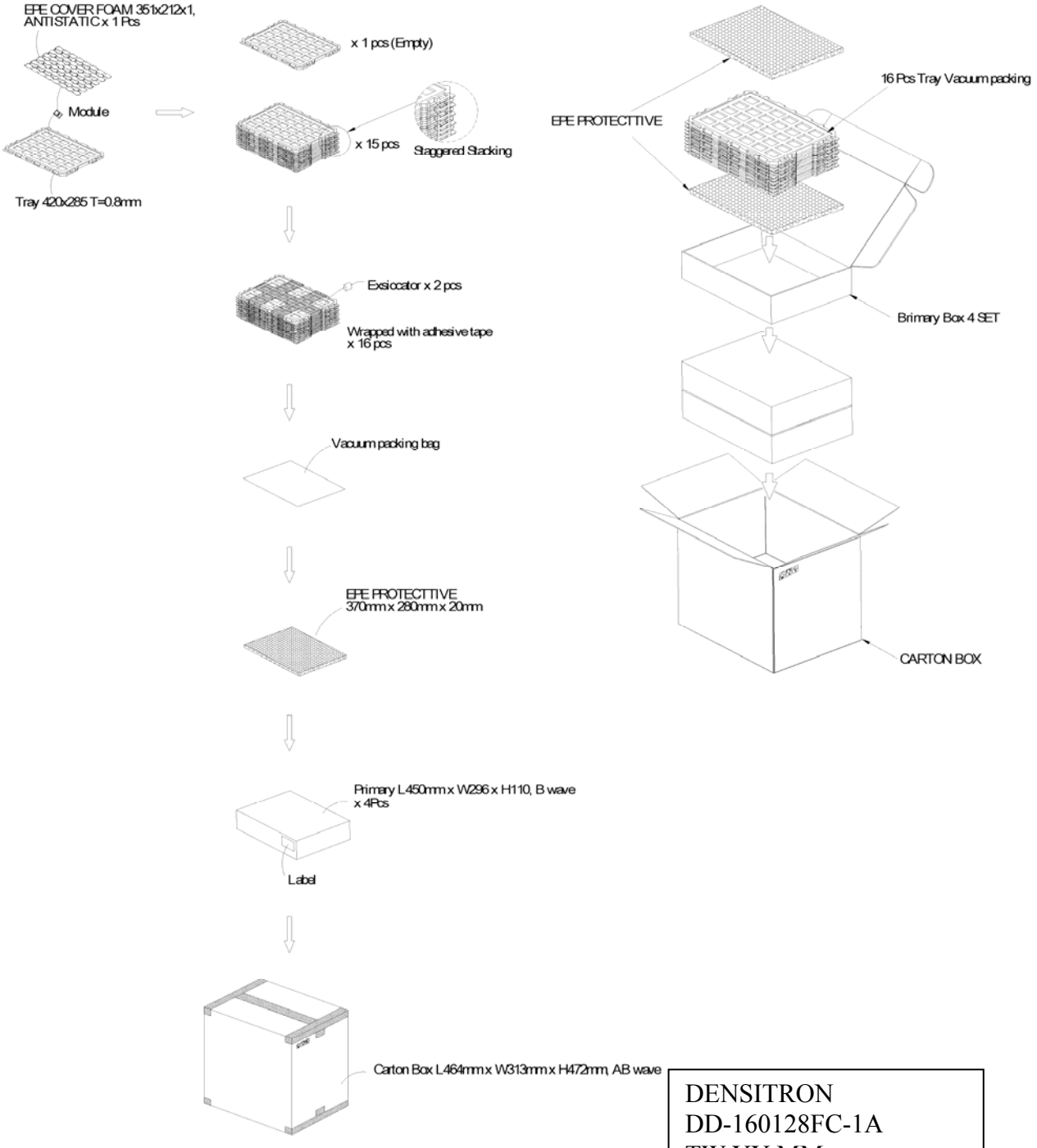
```
//OSC control
//EXPORT1 internal clock and OSC operates with external resiste
   Write_Register(0x02);
   Write_Parameter(0x01);

//REDUCE_CURRENT
//Reduced driving current : normal
//Power save mode:normal
   Write_Register(0x04);
   Write_Parameter(0x00);

//CLOCK_DIV
//OSC frequency setting : 90Hz
//Display frequency divide ration:1
   Write_Register(0x03);
   Write_Parameter(0x30);

//IREF→Reference volt. controlled by External resister
//→RGB current and precharge time,current separate control
   Write_Register(0x80);
   Write_Parameter(0x00);

//PRECHARGE_TIME_R
//1 Precharge Time
   Write_Register(0x08);
   Write_Parameter(0x01);

//PRECHARGE_TIME_G
//1 Precharge Time
   Write_Register(0x09);
   Write_Parameter(0x01);

//PRECHARGE_TIME_B
//1 Precharge Time
   Write_Register(0x0A);
   Write_Parameter(0x01);

//PRECHARGE_CURRENT_R
   Write_Register(0x0B);
   Write_Parameter(0x0A);

//PRECHARGE_CURRENT_G
   Write_Register(0x0C);
   Write_Parameter(0x0A);

//PRECHARGE_CURRENT_B
   Write_Register(0x0D);
   Write_Parameter(0x0A);

//DRIVING_CURRENT_R
//82uA
   Write_Register(0x10);
   Write_Parameter(0x52);
```

```
//DRIVING_CURRENT_G
//56uA
    Write_Register(0x11);
    Write_Parameter(0x38);

//DRIVING_CURRENT_B
//58uA
    Write_Register(0x12);
    Write_Parameter(0x3A);

//Display mode set
//RGB,column=0→159,column data display control=Normal Dispaly
    Write_Register(0x13);
    Write_Parameter(0x00);

//External interface mode =MPU
    Write_Register(0x14);
    Write_Parameter(0x01);

//MEMORY_WRITE_MODE
//6btis Triple transfer,262K support ,Horizontal address counter is increased,Vertical
  address
//counter is increased,The data is continuously written horizontally
    Write_Register(0x16);
    Write_Parameter(0x76);

//Memory Address setting range 0x17~0x19→160x128
    Write_Register(0x17);   //column start
    Write_Parameter(0x00);
    Write_Register(0x18);   //column end
    Write_Parameter(0x9F);
    Write_Register(0x19);   //row start
    Write_Parameter(0x00);
    Write_Register(0x1A);   //row end
    Write_Parameter(0x7F);

//Memory Start Address set 0x20~0x21
    Write_Register(0x20);   // X
    Write_Parameter(0x00);
    Write_Register(0x21);   // Y
    Write_Parameter(0x00);

//DUTY
    Write_Register(0x28);
    Write_Parameter(0x7F);//128

//Display Start LIne
    Write_Register(0x29);
    Write_Parameter(0x00);

//DDRAM Read Address Start point 0x2E~0x2F
    Write_Register(0x2E);   // X
    Write_Parameter(0x00);
    Write_Register(0x2F);   // Y
    Write_Parameter(0x00);
```

```
//Display Screen Saver Size 0x33~0x36
    Write_Register(0x33);   //Display Screen Saver Columns Start
    Write_Parameter(0x00);
    Write_Register(0x34);   //Display Screen Saver Columns End
    Write_Parameter(0x9F);
    Write_Register(0x35);   //Display Screen Saver Row Start
    Write_Parameter(0x00);
    Write_Register(0x36);   //Display Screen Saver Row End
    Write_Parameter(0x7F);
    Write_Register(0x06);   //Display ON
    Write_Parameter(0x01);
```

6 PACKAGING AND LABELLING SPECIFICATION

6.1 LABELLING & MARKING



7 QUALITY ASSURANCE SPECIFICATION

7.1 CONFORMITY

The performance, function and reliability of the shipped products conform to the Product Specification.

7.2 DELIVERY ASSURANCE

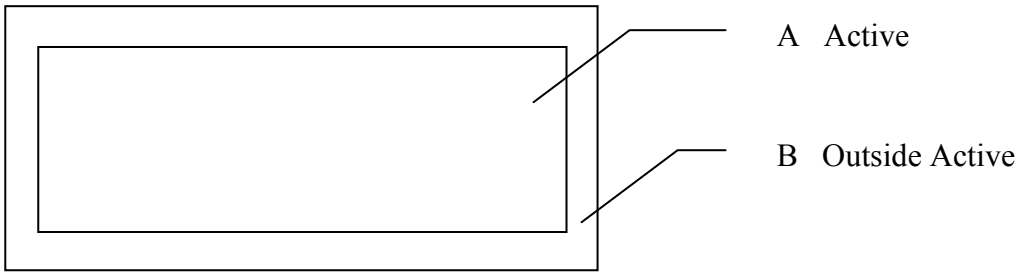
7.2.1 DELIVERY INSPECTION STANDARDS

MIL-STD-105E, general inspection level II, single sampling level;
IPC-AA610 rev. C, class 2 electronic assemblies standard

The quality assurance levels are shown below:

Class	AQL (%)
Critical defect	0.5%
Major defect	1.0%
Minor defect	1.5%
TOTAL	2.0%

7.2.2 Zone definition



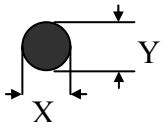
7.2.3 Visual inspection

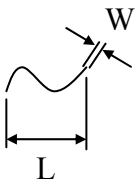
Test and measurement to be conducted under following conditions□

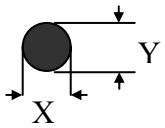
Temperature:	23±5℃
Humidity:	55±15%RH
Fluorescent lamp:	30 W
Distance between the Panel & Eyes of the Inspector:	≧30cm
Distance between the Panel & the lamp:	≧50cm

7.2.4 Standard of appearance inspection

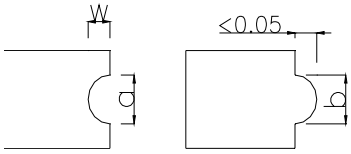
Units: mm

<table>
<tr><th>Class</th><th>Item</th><th colspan="2">Criteria</th></tr>
<tr><td>Minor</td><td rowspan="2">Packing & Label</td><td>Outside & inside package</td><td>Presence of product no., lot no., quantity</td></tr>
<tr><td>Critical</td><td colspan="2">Product must not be mixed with others and quantity must not be different from that indicated on the label</td></tr>
<tr><td>Major</td><td>Dimension</td><td colspan="2">Product dimensions must be according to specification and drawing</td></tr>
<tr><td>Major</td><td>Electrical</td><td colspan="2">Product electrical characteristics must be according to specification</td></tr>
<tr><td>Critical</td><td>OLED Display</td><td colspan="2">Missing lines, short circuits or wrong patterns on OLED display are not allowed</td></tr>
<tr><td>Minor</td><td>Black spot, white spot, dust</td><td colspan="2">Round type: as per following drawing
∅ = (X+Y)/2
 

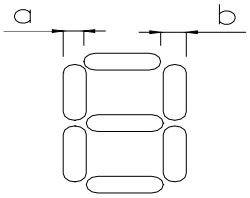
<table><tr><th colspan="3">Acceptable quantity</th></tr><tr><th>Size</th><th>Zone A</th><th>Zone B</th></tr><tr><td>∅<0.1</td><td>Any number</td><td rowspan="4">Any number</td></tr><tr><td>0.1<∅<0.2</td><td>3</td></tr><tr><td>0.2<∅<0.25</td><td>1</td></tr><tr><td>0.25<∅</td><td>0</td></tr></table>
Line type: as per following drawing
 

<table><tr><th colspan="4">Acceptable quantity</th></tr><tr><th>Length</th><th>Width</th><th>Zone A</th><th>Zone B</th></tr><tr><td>- -</td><td>W≤0.05</td><td>Any number</td><td rowspan="3">Any number</td></tr><tr><td>L≤2.0</td><td>W≤0.1</td><td>3</td></tr><tr><td>L>2.0</td><td></td><td>0</td></tr></table>
Total acceptable quantity: 3</td></tr>
<tr><td>Minor</td><td>Polariser scratch</td><td colspan="2">Scratch on protective film is permitted
Scratch on polariser: same as No. 1</td></tr>
<tr><td>Minor</td><td>Polariser bubble</td><td colspan="2">∅ = (X+Y)/2
 

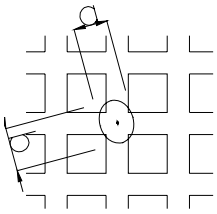
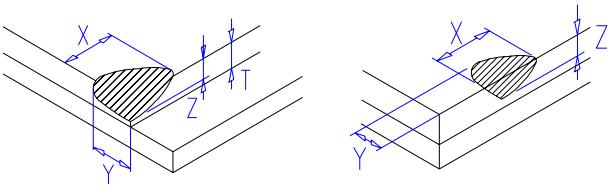
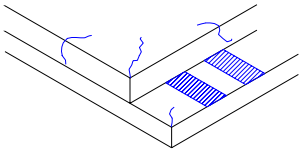
<table><tr><th colspan="3">Acceptable quantity</th></tr><tr><th>Size</th><th>Zone A</th><th>Zone B</th></tr><tr><td>∅<0.5</td><td>Any number</td><td rowspan="2">Any number</td></tr><tr><td>∅>0.5</td><td>0</td></tr></table></td></tr>
</table>

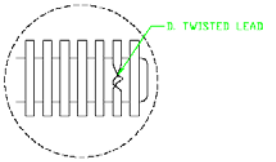
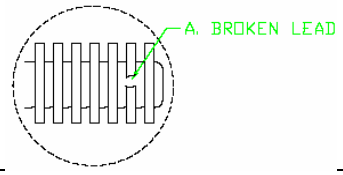
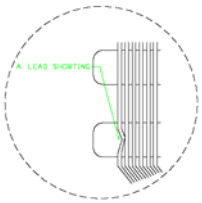
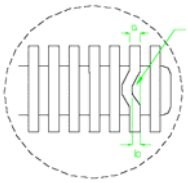
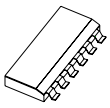
<table>
<tr><th>Class</th><th>Item</th><th>Criteria</th></tr>
<tr><td>Minor</td><td>Segment deformation</td><td>1b. Pin hole on dot matrix display
 

<table>
<tr><td colspan="2">Acceptable quantity</td></tr>
<tr><td>Size</td><td></td></tr>
<tr><td>a,b<0.1</td><td>Any number</td></tr>
<tr><td>(a+b)/2≤0.1</td><td>Any number</td></tr>
<tr><td>0.5<∅<1.0</td><td>3</td></tr>
</table>
Total acceptable quantity: 7

2. Segments / dots with different width
 

<table>
<tr><td colspan="2">Acceptable</td></tr>
<tr><td>a≥b</td><td>a/b≤4/3</td></tr>
<tr><td>a<b</td><td>a/b>4/3</td></tr>
</table>

3. Alignment layer defect
∅ = (a+b)/2
 
<table>
<tr><td colspan="2">Acceptable quantity</td></tr>
<tr><td>Size</td><td></td></tr>
<tr><td>∅≤0.4</td><td>Any number</td></tr>
<tr><td>0.4<∅≤1.0</td><td>5</td></tr>
<tr><td>1.0<∅≤1.5</td><td>3</td></tr>
<tr><td>1.5<∅≤2.0</td><td>2</td></tr>
</table>
Total acceptable quantity: 7</td></tr>
<tr><td>Minor</td><td>Panel Chipping</td><td>X ≤ 1/6 Panel length
Y ≤ 1
Z ≤ T
 
</td></tr>
<tr><td>Minor</td><td>Panel Cracking</td><td>Cracks not allowed
  </td></tr>
<tr><td>Minor</td><td>Cupper exposed (pin or film)</td><td>Not allowed if visible by eye inspection</td></tr>
<tr><td>Minor</td><td>Film or Trace Damage</td><td>Not allowed if affect electrical function</td></tr>
</table>

<table>
<tr><th>Class</th><th>Item</th><th>Criteria</th></tr>
<tr><td>Minor</td><td>Contact
Lead Twist</td><td>Not allowed
  </td></tr>
<tr><td>Minor</td><td>Contact
Lead
Broken</td><td>Not allowed
  </td></tr>
<tr><td rowspan="2">Minor</td><td rowspan="2">Contact
Lead Bent</td><td>Not allowed if
bent lead causes
short circuit
  </td></tr>
<tr><td>Not allowed if bent lead
extends horizontally
more than 50%
of its width
  </td></tr>
<tr><td>Minor</td><td>Colour
uniformity</td><td>Level of sample for approval set as limit sample</td></tr>
<tr><td>Major</td><td rowspan="4">PCB
  </td><td>No unmelted solder paste should be present on PCB</td></tr>
<tr><td>Critical</td><td>Cold solder joints, missing solder connections, or oxidation are not allowed</td></tr>
<tr><td>Minor</td><td>No residue or solder balls on PCB are allowed</td></tr>
<tr><td>Critical</td><td>Short circuits on components are not allowed</td></tr>
<tr><td>Minor</td><td>Tray
particles</td><td>
<table>
<tr><th></th><th>Size</th><th>Quantity</th></tr>
<tr><td rowspan="2">On tray</td><td>∅<0.2</td><td>Any number</td></tr>
<tr><td>∅>0.25</td><td>4</td></tr>
<tr><td rowspan="2">On display</td><td>∅≥0.25</td><td>2</td></tr>
<tr><td>L = 3</td><td>1</td></tr>
</table>
</td></tr>
</table>

7.3 DEALING WITH CUSTOMER COMPLAINTS

7.3.1 Non-conforming analysis

Purchaser should supply Densitron with detailed data of non-conforming sample.
After accepting it, Densitron should complete the analysis in two weeks from receiving the sample.
If the analysis cannot be completed on time, Densitron must inform the purchaser.

7.3.2 Handling of non-conforming displays

If any non-conforming displays are found during customer acceptance inspection which Densitron is clearly responsible for, return them to Densitron.
Both Densitron and customer should analyse the reason and discuss the handling of non-conforming displays when the reason is not clear.
Equally, both sides should discuss and come to agreement for issues pertaining to modification of Densitron quality assurance standard.

8 RELIABILITY SPECIFICATION

8.1 RELIABILITY TESTS

Test Item	Test Condition	Evaluation and assessment
High Temperature Operation	70°C±2, 240 hours	No abnormalities in function and appearance
Low Temperature Operation	-30°C±2, 240 hours	No abnormalities in function and appearance
High Temperature Storage	80°C±2, 240 hours	No abnormalities in function and appearance
Low Temperature Storage	-40°C±2, 240 hours	No abnormalities in function and appearance
High Temperature & High Humidity Storage(Operation)	60°C±2, 90%RH, 120 hours	No abnormalities in function and appearance
Thermal Shock	24 cycle of -40°C 1 Hour, 85°C 1 Hour	No abnormalities in function and appearance

- The brightness should be greater than 50% of the initial brightness.
- The samples used for above tests do not include polarizer.
- No moisture condensation is observed during tests.

8.1.1 FAILURE CHECK STANDARD

After the completion of the described reliability test, the samples were left at room temperature for 2 hrs prior to conducting the failure teat at 23±5 °C; 55±15% RH

8.2 LIFE TIME

Item	Description
1	Function, performance, appearance, etc. shall be free from remarkable deterioration more than 10,000 hours under 100 CD/m² brightness and storage conditions of room temperature (25±10 °C), normal humidity (45±20% RH), and in area not exposed to direct sunlight.
2	End of lifetime is specified as 50% of initial brightness.

9 HANDLING PRECAUTIONS

Safety
If the panel breaks, be careful not to get the organic substance in your mouth or in your eyes.
If the organic substance touches your skin or clothes, wash it off immediately using soap and plenty of water.

Mounting and Design
Place a transparent plate (e.g. acrylic, polycarbonate or glass) on the display surface to protect the display from external pressure. Leave a small gap between the transparent plate and the display surface.
Design the system so that no input signal is given unless the power supply voltage is applied.

Caution during OLED cleaning
Lightly wipe the display surface with a soft cloth soaked with Isopropyl alcohol, Ethyl alcohol or Trichlorotriflorothane.
Do not wipe the display surface with dry or hard materials that will damage the polariser surface.
Do not use aromatic solvents (toluene and xylene), or ketonic solvents (ketone and acetone).

Caution against static charge
As the display uses C-MOS LSI drivers, connect any unused input terminal to V_{DD} or V_{SS}. Do not input any signals before power is turned on.
Also, ground your body, work/assembly table and assembly equipment to protect against static electricity.

Packaging
Displays use OLED elements, and must be treated as such. Avoid strong shock and drop from a height.
To prevent displays from degradation, do not operate or store them exposed directly to sunshine or high temperature/humidity.

Caution during operation
It is indispensable to drive the display within the specified voltage limit since excessive voltage shortens its life.

Other Precautions
When a display module is operated for a long of time with fixed pattern may remain as an after image or slight contrast deviation may occur.
Nonetheless, if the operation is interrupted and left unused for a while, normal state can be restored.
Also, there will be no problem in the reliability of the module.

Storage
Store the display in a dark place where the temperature is 25°C ± 10°C and the humidity below 50%RH.
Store the display in a clean environment, free from dust, organic solvents and corrosive gases.
Do not crash, shake or jolt the display (including accessories).

X-ON Electronics

Largest Supplier of Electrical and Electronic Components

Click to view similar products for densitron ***manufacturer:***

Other Similar products are found below :

DMT024QVNUNT0-2C DMT028QVNTNT0S-1A DMT028QVHXCMI-1A DMT024QVNUCMI-2A LMR4048BG2C16HNG/5V