

OLED DISPLAY MODULE

Product Specification

CUSTOMER	Standard	
PRODUCT NUMBER	DD-12864YO-5A	
CUSTOMER APPROVAL		Date

INTERNAL APPROVALS			
Product Mgr Doc. Control Electr. Eng			
Elijah Ebo	Anthony Perkins	Bazile Peter	
Date: 03 Dec 07	Date: 03 Dec 07	Date: 03 Dec 07	



TABLE OF CONTENTS

1	\mathbf{M}_{λ}	AIN FEATURES	4
2	M	ECHANICAL SPECIFICATION	5
	2.1	MECHANICAL CHARACTERISTICS	5
	2.2	MECHANICAL DRAWING	6
3	EL	ECTRICAL SPECIFICATION	7
	3.1	ABSOLUTE MAXIMUM RATINGS	
	3.2	ELECTRICAL CHARACTERISTICS	
	3.3	INTERFACE PIN ASSIGNMENT	
	3.4	TIMING CHARACTERISTICS	
4	OI	PTICAL SPECIFICATION	15
	4.1	OPTICAL CHARACTERISTICS	15
5	FU	NCTIONAL SPECIFICATION	16
	5.1	COMMANDS	16
	5.2	POWER UP/DOWN SEQUENCE	
	5.3	RESET CIRCUIT	16
	5.4	ACTUAL APPLICATION EXAMPLE	
6	PA	CKAGING AND LABELLING SPECIFICATION	18
7	QU	JALITY ASSURANCE SPECIFICATION	19
	7.1	CONFORMITY	19
	7.2	DELIVERY ASSURANCE	
	7.3	DEALING WITH CUSTOMER COMPLAINTS	23
8	RE	CLIABILITY SPECIFICATION	24
	8.1	RELIABILITY TESTS	24
	8.2	LIFE TIME	
9	H	ANDLING PRECAUTIONS	25

Product No.	DD-12864YO-5A	REV. A
r toduct No.		

Page	2/25
------	------



REVISION RECORD

Rev.	Date	Page	Chapt.	Comment	ECR no.
A	03 Dec 07			First Issue	

Product No.	DD-12864YO-5A	REV. A

Page	3 / 25



1 MAIN FEATURES

ITEM	CONTENTS
Display Format	128 x 64 Dots
Overall Dimensions	73.00 x 41.86 x 2.00 mm
Colour	Monochrome Yellow
Active Area	61.41 x 30.69 mm
Viewing Area	63.41 x 32.69mm
Display Mode	Passive Matrix (2.70")
Driving Method	1/64 duty
Driver IC	SSD1325
Operating temperature	-30 ~ +70
Storage temperature	-40 ~ +80

Product No.	DD-12864YO-5A	REV. A
Froduct No.		



2 MECHANICAL SPECIFICATION

2.1 MECHANICAL CHARACTERISTICS

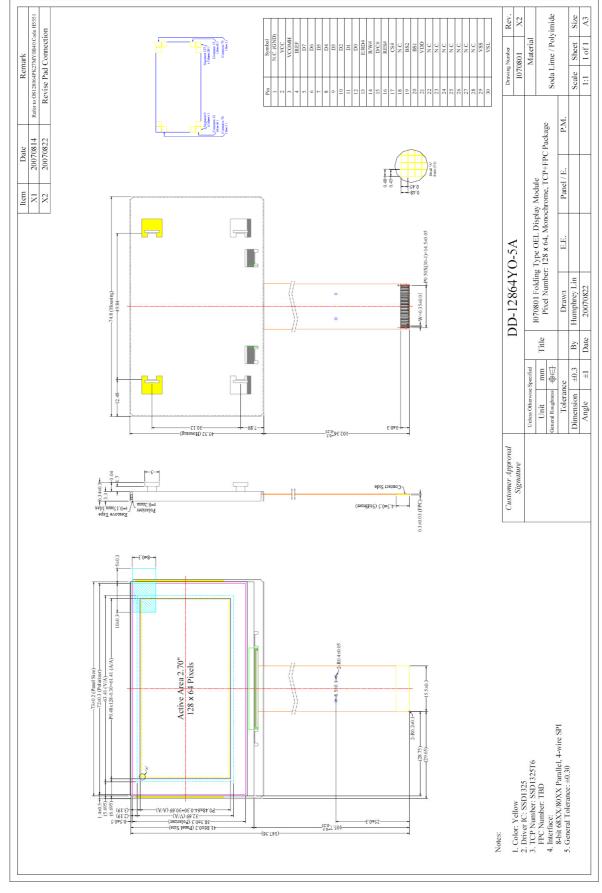
ITEM	CHARACTERISTIC	UNIT
Display Format	128 x 64 Dots	Dots
Overall Dimensions	73.00 x 41.86 x 2.00	mm
Viewing Area	63.41 x 32.69	mm
Active Area	61.41 x 30.69	mm
Dot Size	0.45 x 0.45	mm
Dot Pitch	0.48 x 0.48	mm
Weight	21	g
IC Controller/Driver	SSD1325	

Product No.	DD-12864YO-5A	REV. A
r roduct ivo.		

Page	5 / 25
------	--------



2.2 MECHANICAL DRAWING



Product No.	DD-12864YO-5A	REV. A] [Dogo	6 / 25
Floduct No.				Page	0723



3 ELECTRICAL SPECIFICATION

3.1 ABSOLUTE MAXIMUM RATINGS

VSS = 0 V, Ta = 25 °C

Item	Symbol	Min	Max	Unit	Note
Supply Voltage for logic	V_{DD}	-0.3	4.0	V	Note 1 2
Supply voltage for Display	Vcc	0	16	V	Note 1, 2
Operating Temperature	Тор	-30	70	°C	
Storage Temperature	Tst	-40	80	°C	
Static Electricity	Be sure that you are grounded when handling displays.				

Note 1: All the above voltages are on the basis of "GND=0V".

Note 2: When this module is used beyond the above absolute maximum ratings, permanent damage to the module may occur. Also for normal operations it's desirable to use this module under the conditions according to Section 3.2 "Electrical Characteristics". If this module is used beyond these conditions the module may malfunction and the reliability could deteriorate.



3.2 ELECTRICAL CHARACTERISTICS

Characteristics	Symbol	Conditions	Min	Тур	Max	Unit
Supply Voltage	V_{DD}		2.4	2.8	3.5	V
Supply Voltage for Display	Vcc	Note 3	14.25	15	15.75	V
High Level Input	V _{IH}		$0.8 \mathrm{xV}_\mathrm{DD}$	-	V_{DD}	V
Low Level Input	V _{IL}	IOUT=0.1mA.	0	-	0.2 x VDD	V
High Level Output	V _{OH}	3.3MHz	0.9 x V DD	-	Vdd	V
Low Level Output	V _{OL}		0	-	0.1 x V _{DD}	V
On anoting assument for Van	Inn	Note 4	-	250	400	
Operating current for VDD	Idd	Note 5	-	250	400	μA
On anoting a summent for Vac	Inc	Note 4	-	31	39	A
Operating current for Vcc	Icc	Note 5	-	53	66	mA
Sleep mode current for VDD	Idd Sleep		-	1	2	μΑ
Sleep mode current for Vcc	ICC SLEEP		-	1	2	μA

Note 3 Brigthness (Lbr) and Supply Voltage for Display (Vcc) are subject to the change of the panel characteristics and the customers request

Note 4 $V_{DD} = 2.8V$, $V_{CC} = 15V$, 50% display area turned on.

Note 5 $V_{DD} = 2.8V$, $V_{CC} = 15V$, 100% display area turned on.

Product No.	DD-12864YO-5A	REV. A
Floduct No.		

Page	8 / 25
------	--------



3.3 INTERFACE PIN ASSIGNMENT

No.	Symbol	Function			
1	N.C.(GND)	Reserved Pin (Supporting Pin) The supporting pins can reduce the influences from stresses on the function pins. These pins must be connected to external ground.			
2	VCC	Power Supply for OEL Panel This is the most positive voltage supply pin of the chip. It can be supplied externally or generated internally by using internal DC/DC voltage converter.			
3	VCOMH	Voltage Output High Level for COM Signal This pin is the input pin for the voltage output high level for COM signals. It can be supplied externally or internally. When VCOMH is generated internally, a capacitor should be connected between this pin and VSS.			
4	IREF	Current Reference for Brightness Adjustment This pin is segment current reference pin. A resistor should be connected between this pin and VSS. Set the current at 10µA.			
5~12	D7~D0	Host Data Input/Output Bus These pins are 8-bit bi-directional data bus to be connected to the microprocessor's data bus. When serial mode is selected, D1 will be the serial data input SDIN and D0 will be the serial clock input SCLK.			
13	E/RD#	Read/Write Enable or Read This pin is MCU interface input. When interfacing to a 68XX-series microprocessor, this pin will be used as the Enable (E) signal. Read/write operation is initiated when the pin is pulled high and the CS# is pulled low. When connecting to an 80XX-microprocessor, this pin receives the Read (RD#) signal. Data read operation is initiated when this pin is pulled low and CS# is pulled low.			
14	R/W#	Read/Write Select or Write This pin is MCU interface input. When interfacing to a 68XX-series microprocessor, this pin will be used as Read/Write (R/W#) selection input. Pull this pin to "High" for read mode and pull it "Low" for write mode. When 80XX interface mode is selected, this pin will be the Write (WR#) input. Data write operation is initiated when this pin is pulled low and the CS# is pulled low.			
15	D/C#	Data/Command Control This pin is Data/Command control pin. When the pin is pulled high, the input at D7~D0 is treated as display data. When the pin is pulled low, the input at D7~D0 will be transferred to the command register. For detail relationship to MCU interface signals, please refer to the Timing Characteristics Diagrams. When the pin is pulled high and serial interface mode is selected, the data at SDIN is treated as data. When it is pulled low, the data at SDIN			
16	RES#	will be transferred to the command register. Power Reset for Controller and Driver This pin is reset signal input. When the pin is low, initialization of the chip is executed.			
17	CS#	Chip Select This pin is the chip select input. The chip is enabled for MCU communication only when CS# is pulled low.			
18	N.C.	No Connection			
Produc	et No.	DD-12864YO-5A REV. A Page 9 / 25			



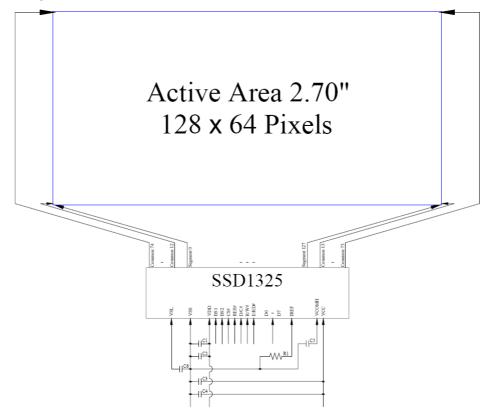
19	BS1	Communicating Protocol Select These pins are MCU interface selection input. See the following table:				
		1	68XX-parallel	80XX-parallel	Serial	
20	DCO	BS1	0	1	0	
20	BS2	BS2	1	1	0	
21	VDD		pply for Logic Cire			
	***************************************	This is a voltage supply pin. It must be connected to external source.				
		Reserved	Pin			
22~28	N.C.	N.C. The supporting pins can reduce the influences from stresses on the				
			•	e connected to exter	nal ground.	
		Ground of OEL System This is a ground pin. It also acts as a reference for the logic pins, the OEL				
29	VSS					
		driving voltages, and the analog circuits. It must be connected to external ground.				
	Voltage Output Low Level for SEG Signal					
30 VSL This pin is the			ne output for the volta	age output low level		s. A

Product No.	DD-12864YO-5A	REV. A
Floduct No.		

Page	10 / 25
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BLOCK DIAGRAM



MCU Interface Selection: BS1 and BS2

Pins connected to MCU interface: D7~D0, E/RD#, R/W#, D/C#, RES# and CS#.

 $\begin{array}{ccc} C1, C3: & 0.1 \mu F \\ C2, C6: & 4.7 \mu F \\ C4: & 10 \mu F \end{array}$

C5: 4.7µF/25V Tantalum Capacitor

R1: $820 \text{ k}\Omega$, R1 = (Voltage at IREF – BGGND) / IREF

Product No	DD-12864YO-5A	REV. A
Product No.		

Page	11/25
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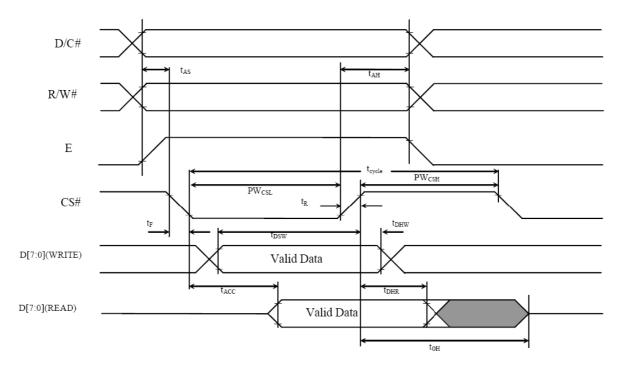
3.4 TIMING CHARACTERISTICS

3.4.1 AC CHARACTERISTICS

3.4.1.1 68XX-Series MPU Parallel Interface Timing Characteristics

VDD = 2.8V, Ta = 25°C

Symbol	Description	Min	Max	Unit
tcycle	System Cycle Time	300	-	ns
tAS	Address Setup Time	0	-	ns
tAH	Address Hold Time	0	-	ns
tDSW	Write Data Setup Time	40	-	ns
tDHW	Write Data Hold Time 15		-	ns
tDHR	Read Data Hold Time	20	-	ns
tOH	H Output Disable Time		70	ns
tACC	Access Time	-	140	ns
DWCCI	Chip Select Low Pulse Width (Read) Chip Select	120		
PWCSL	PWCSL Low Pulse width (Write)		_	ns
DWGGH	Chip Select High Pulse Width (Read) Chip Select			
PWCSH	High Pulse Width (Write)	60	-	ns
tR	Rise Time	-	15	ns
tF	Fall Time	-	15	ns



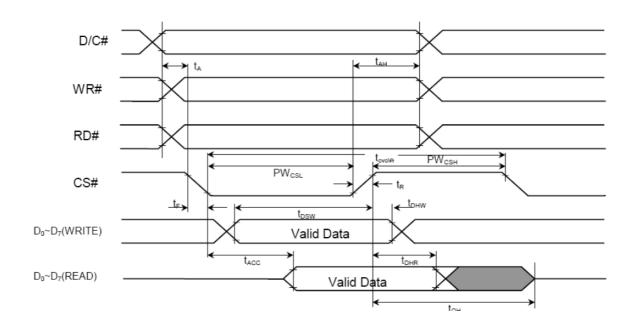
Product No.	DD-12864YO-5A	REV. A
Flouuct No.		

Page	12 / 25
------	---------



3.4.1.2 80XX-Series MPU Parallel Interface Timing Characteristics

Symbol	Description	Min	Max	Unit
tcycle	Clock Cycle Time	300	-	ns
tAS	Address Setup Time	0	-	ns
tAH	Address Hold Time	0	-	ns
tDSW	Write Data Setup Time	40	-	ns
tDHW	Write Data Hold Time	15	-	ns
tDHR	Read Data Hold Time		-	ns
tOH	tOH Output Disable Time		70	ns
tACC	C Access Time		140	ns
PWcsl	Chip Select Low Pulse Width (Read)	120	-	ns
P W CSL	Chip Select Low Pulse Width (Write)	60	-	ns
PWcsh	Chip Select Setup High Pulse Width (Read)	60	-	ns
PWCSH	Chip Select Setup High Pulse Width (Write)	60	-	ns
tR	Rise Time	-	15	ns
tF	Fall Time	-	15	ns



Product No.	DD-12864YO-5A	REV. A
Floduct No.		

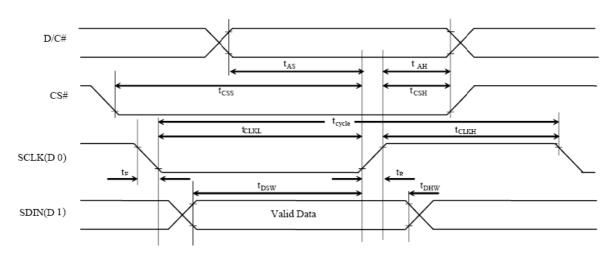
Page	13 / 25
------	---------

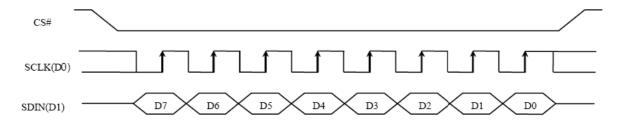


3.4.1.3 Serial Interface Timing Characteristics

VDD = 2.8V, Ta = 25°C

Symbol	Description	Min	Max	Unit
tcycle	Clock Cycle Time	250	-	ns
tAS	Address Setup Time	150	-	ns
tAH	Address Hold Time	150	-	ns
tCSS	Chip Select Setup Time	120	-	ns
tCSH	Chip Select Hold Time	60	-	ns
tDSW	Write Data Setup Time	100	-	ns
tDHW	Write Data Hold Time	100	-	ns
tCLKL	Serial Clock Low Time	100	-	ns
tCLKH	Serial Clock High Time	100	-	ns
tR	Rise Time	-	15	ns
tF	Fall Time	-	15	ns





Product No.	DD-12864YO-5A	REV. A
Flouuct No.		

Page	14/25
------	-------



4 OPTICAL SPECIFICATION

4.1 OPTICAL CHARACTERISTICS

Characteristics	Symbol	Condition	Min	Тур	Max	Unit
Brightness(White)	L _{br}	With Polarizer (Note 3)	70	100	1	cd/m ²
CIE (WILL)	(X)	- Without Polarizer -	0.44	0.48	0.52	
C.I.E.(White)	(Y)		0.47	0.51	0.55	-
Dark Room Contrast	CR		-	>1000:1	1	-
Viewing Angle			>160	-	1	degree

Optical measurement taken at $V_{DD} = 2.8V$, $V_{CC} = 15V$. Software configuration follows Section 4.4 Initialization

Product No.	DD-12864YO-5A	REV. A
Flouuct No.		

Page	15 / 25
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5 FUNCTIONAL SPECIFICATION

5.1 COMMANDS

Please refer to the Technical Manual for the SSD1325

5.2 POWER UP/DOWN SEQUENCE

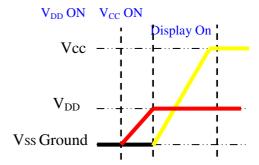
To protect panel and extend the panel lifetime, the driver IC power up/down routine should include a delay period between high voltage and low voltage power sources during turn on/off. It gives the panel enough time to complete the action of charge and discharge before/after the operation.

5.2.1 POWER UP SEQUENCE

- 1. Power up V_{DD}
- 2. Send Display off command
- 3. Initialization
- 4. Clear Screen
- 5.Power up Vcc
- 6. Delay 100ms

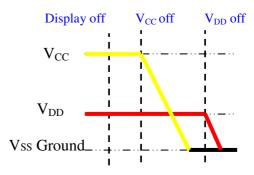
(when VDD is stable)

7. Send Display on command



5.2.2 POWER DOWN SEQUENCE

- 1. Send Display off command
- 2. Power down V_{CC}
- 3. Delay 100ms (When V_{PP} reach 0 and panel is completely discharges)
- 4. Power down V_{DD}



5.3 RESET CIRCUIT

When RES# input is low, the chip is initialized with the following status:

- 1. Display is OFF
- 2. 128x80 Display mode
- 3. Normal segment and display data column and row address mapping (SEG0 mapped to column address 00H and COM0 mapped to row address 00H)
- 4. Shift register data clear in serial interface
- 5. Display start line is set at display RAM address 0
- 6. Column address counter is set at 0
- 7. Normal scan direction of the COM outputs
- 8. Contrast control register is set at 80H
- 9. Normal display mode (Equivalent to A4h command)

Product No.	DD-12864YO-5A	REV. A
Flouuct No.		

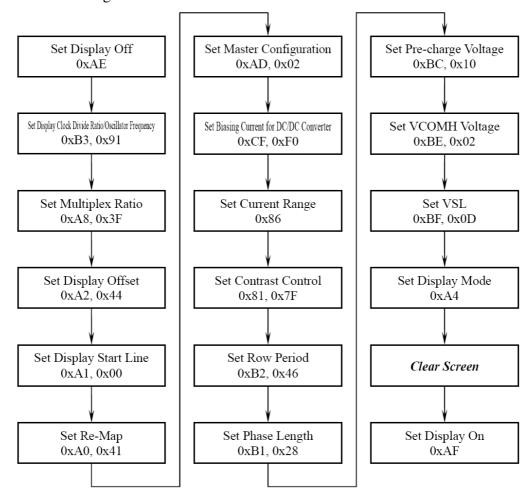
Page	16 / 25
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5.4 ACTUAL APPLICATION EXAMPLE

Command usage and explanation of an actual example

<Initialisation Setting>



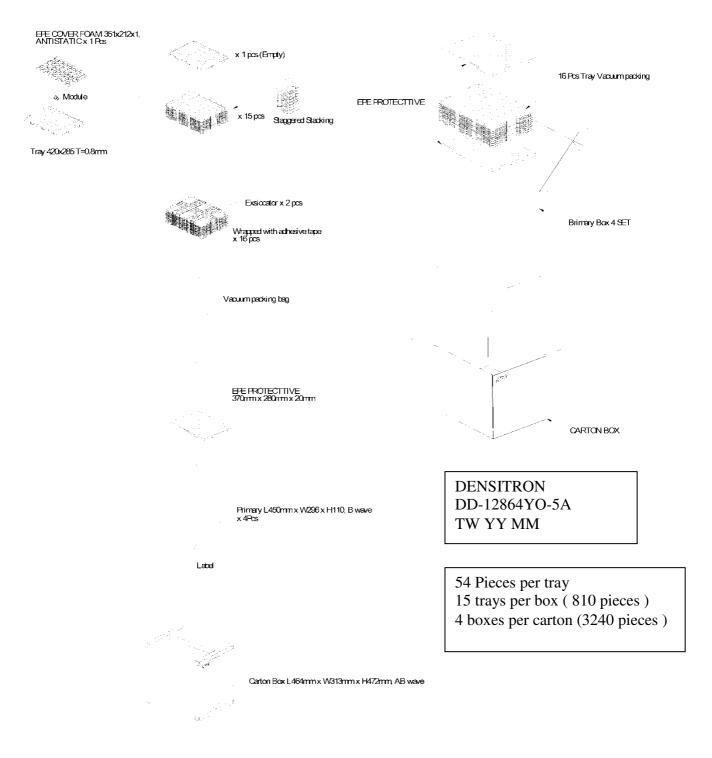
If the noise is accidentally occurred at the displaying window during the operation, please reset the display in order to recover the display function.

Product No.	DD-12864YO-5A	REV. A
Floduct No.		

Page	17 / 25
------	---------



6 PACKAGING AND LABELLING SPECIFICATION



Product No.	DD-12864YO-5A	REV. A
Floduct No.		

Page	18 / 25
------	---------



7 QUALITY ASSURANCE SPECIFICATION

7.1 CONFORMITY

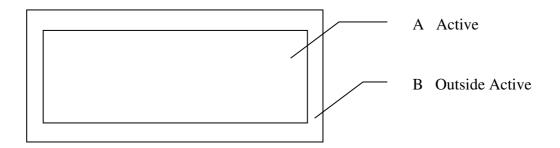
The performance, function and reliability of the shipped products conform to the Product Specification.

7.2 DELIVERY ASSURANCE

7.2.1 DELIVERY INSPECTION STANDARDS

IPC-AA610, class 2 electronic assemblies standard

7.2.2 Zone definition



7.2.3 Visual inspection

Test and measurement to be conducted under following conditions:

Temperature: 23 ± 5 °C

Humidity: $55\pm15\%$ RH

Fluorescent lamp: 30 WDistance between the Panel & Eyes of the Inspector: $\ge 30 \text{cm}$ Distance between the Panel & the lamp: $\ge 50 \text{cm}$

Product No.	DD-12864YO-5A	REV. A
Floduct No.		

Page	19 / 25
------	---------



7.2.4 Standard of appearance inspection

Units: mm

Class	Item			Criteria	1	
Minor	Packing &	Outside & inside package Presence of product no., lot no., quantity				
Critical	Label	Product mus	t not be mixe	d with others and	quantity must not	be different from
		that indicate	d on the label			
Major	Dimension	Product dim	ensions must	be according to sp	pecification and di	rawing
Major	Electrical	Product elec	trical characte	eristics must be ac	cording to specifi	cation
Critical	OLED Display	Missing line allowed	s, short circui	ts or wrong patter	ns on OLED disp	lay are not
Minor	Black spot, white spot,	Round type: $\emptyset = (X+Y)/2$	as per follow	ring drawing		
	dust			A	cceptable quantity	Į.
				Size	Zone A	Zone B
			<u>L</u>	Ø<0.1	Any number	
			Y	0.1<Ø<0.2	3	1, ,
			<u>F</u>	0.2<Ø<0.25	1	Any number
		X		0.25<Ø	0	1
		Line type: as	s per followin	-	ole quantity	
		W	Length	Width	Zone A	Zone B
				W≤0.05	Any number	
		$ \langle \vee $	L≤2.0	W≤0.1	3	Any number
		 	L>2.0		0	
			Total accept	table quantity: 3		
Minor	Polariser	Scratch on p	rotective film	is permitted		
	scratch	_	olariser: same	-		
Minor	Polariser	$\emptyset = (X+Y)/2$				
	bubble	Acceptable quantity				
				Size	Zone A	Zone B
			<u>L</u>	Ø<0.5	Any number	Any number
			Y	Ø>0.5	0	Any number
		X				

Product No.	DD-12864YO-5A	REV. A
Floduct No.		

Page	20 / 25
------	---------



Class	Item	Criteri	a	
Minor	Segment deformation	1b. Pin hole on dot matrix display	Acceptable Size $a,b<0.1$ $(a+b)/2\leq0.1$ $0.5<\emptyset<1.0$ Total acceptable	Any number Any number 3
		2. Segments / dots with different width	Accep a≥b a <b< td=""><td>table a/b≤4/3 a/b>4/3</td></b<>	table a/b≤4/3 a/b>4/3
		3. Alignment layer defect $\emptyset = (a+b)/2$	Acceptable Size $\emptyset \le 0.4$ $0.4 < \emptyset \le 1.0$ $1.0 < \emptyset \le 1.5$ $1.5 < \emptyset \le 2.0$ Total acceptable	Any number 5 3 2
Minor	Panel Chipping	$X \le 1/6$ Panel length $Y \le 1$ $Z \le T$		Z
Minor	Panel Cracking	Cracks not allowed		
Minor	Cupper exposed (pin or film)	Not allowed if visible by eye inspection		
Minor	Film or Trace Damage	Not allowed if affect electrical function		

Product No.	DD-12864YO-5A	REV. A
Product No.		

Page	21 / 25
------	---------



Class	Item	Criteria			
Minor	Contact Lead Twist	Not allowed		D. TVISTED LEAD	
Minor	Contact Lead Broken	Not allowed		A. BROKEN LEAD	
Minor	Contact Lead Bent	Not allowed if bent lead causes short circuit			
		Not allowed if bent extends horizontall more than 50% of its width			
Minor	Colour uniformity	Level of sample for approval set as limit sample			
Major	PCB ~	No unmelted solder paste should be present on PCB			
Critical		Cold solder joints, missing solder connections, or oxidation are not allowed			
Minor		No residue or solder balls on PCB are allowed			
Critical		Short circuits on components are not allowed			
Minor	Tray particles			Size Ø<0.2	Quantity Any number
	Particles		On tray	Ø>0.25	Any number
			On dienley	Ø≥0.25	2
			On display	L = 3	1

Product No.	DD-12864YO-5A	REV. A
Product No.		

Page	22 / 25
------	---------



7.3 DEALING WITH CUSTOMER COMPLAINTS

7.3.1 Non-conforming analysis

Purchaser should supply Densitron with detailed data of non-conforming sample. After accepting it, Densitron should complete the analysis in two weeks from receiving the sample.

If the analysis cannot be completed on time, Densitron must inform the purchaser.

7.3.2 Handling of non-conforming displays

If any non-conforming displays are found during customer acceptance inspection which Densitron is clearly responsible for, return them to Densitron.

Both Densitron and customer should analyse the reason and discuss the handling of non-conforming displays when the reason is not clear.

Equally, both sides should discuss and come to agreement for issues pertaining to modification of Densitron quality assurance standard.

Product No.	DD-12864YO-5A	REV. A
Floduct No.		

Page	23 / 25
------	---------



8 RELIABILITY SPECIFICATION

8.1 RELIABILITY TESTS

Test Item	Test Condition	Evaluation and assessment
High Temperature Operation	70°C, 240 hours	No abnormalities in function and appearance
Low Temperature Operation	-30°C, 240 hours	No abnormalities in function and appearance
High Temperature Storage	80°C, 240 hours	No abnormalities in function and appearance
Low Temperature Storage	-40°C, 240 hours	No abnormalities in function and appearance
High Temperature & High Humidity Storage(Operation)	60°C, 90%RH, 240 hours	No abnormalities in function and appearance
Thermal Shock	24 cycle of -40°C 1 Hour, 85°C 1 Hour. 60 Mins dwell	No abnormalities in function and appearance

- The samples used for above tests do not include polarizer.
- No moisture condensation is observed during tests.

8.1.1 FAILURE CHECK STANDARD

After the completion of the described reliability test, the samples were left at room temperature for 2 hrs prior to conducting the failure teat at 23±5 °C; 55±15% RH

8.2 LIFE TIME

Item	Description
1	Function, performance, appearance, etc. shall be free from remarkable deterioration more than 40,000 hours under 100 cd/m² brightness and 50% Checkerboard, humidity (50% RH), and in area not exposed to direct sunlight.
2	End of lifetime is specified as 50% of initial brightness.

Droduct No.	DD-12864YO-5A	REV. A
Product No.		

Page	24 / 25



9 HANDLING PRECAUTIONS

Safety

If the panel breaks, be careful not to get the organic substance in your mouth or in your eyes. If the organic substance touches your skin or clothes, wash it off immediately using soap and plenty of water.

Mounting and Design

Place a transparent plate (e.g. acrylic, polycarbonate or glass) on the display surface to protect the display from external pressure. Leave a small gap between the transparent plate and the display surface.

Design the system so that no input signal is given unless the power supply voltage is applied.

Caution during OLED cleaning

Lightly wipe the display surface with a soft cloth soaked with Isopropyl alcohol, Ethyl alcohol or Trichlorotriflorothane.

Do not wipe the display surface with dry or hard materials that will damage the polariser surface. Do not use aromatic solvents (toluene and xylene), or ketonic solvents (ketone and acetone).

Caution against static charge

As the display uses C-MOS LSI drivers, connect any unused input terminal to V_{DD} or V_{SS} . Do not input any signals before power is turned on.

Also, ground your body, work/assembly table and assembly equipment to protect against static electricity.

Packaging

Displays use OLED elements, and must be treated as such. Avoid strong shock and drop from a height.

To prevent displays from degradation, do not operate or store them exposed directly to sunshine or high temperature/humidity.

Caution during operation

It is indispensable to drive the display within the specified voltage limit since excessive voltage shortens its life.

Other Precautions

When a display module is operated for a long of time with fixed pattern may remain as an after image or slight contrast deviation may occur.

Nonetheless, if the operation is interrupted and left unused for a while, normal state can be restored. Also, there will be no problem in the reliability of the module.

Storage

Store the display in a dark place where the temperature is $25^{\circ}\text{C} \pm 10^{\circ}\text{C}$ and the humidity below 50%RH

Store the display in a clean environment, free from dust, organic solvents and corrosive gases. Do not crash, shake or jolt the display (including accessories).

Product No.	DD-12864YO-5A	REV. A

Page	25 / 25
------	---------

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