

NTE965 Linear Integrated Circuit Voltage Regulator, Negative, –8V, 1A

Description:

The NTE965 voltage regulator employs current limiting, thermal shutdown, and safe—area compensation which makes it remarkably rugged under most operating conditions. With adequate heat—sinking they can deliver output currents in excess of 1.0 amperes.

Features:

- No External Components Required
- Internal Thermal Overload Protection
- Internal Short–Circuit Current Limiting
- Output Transistor Safe–Area Compensation

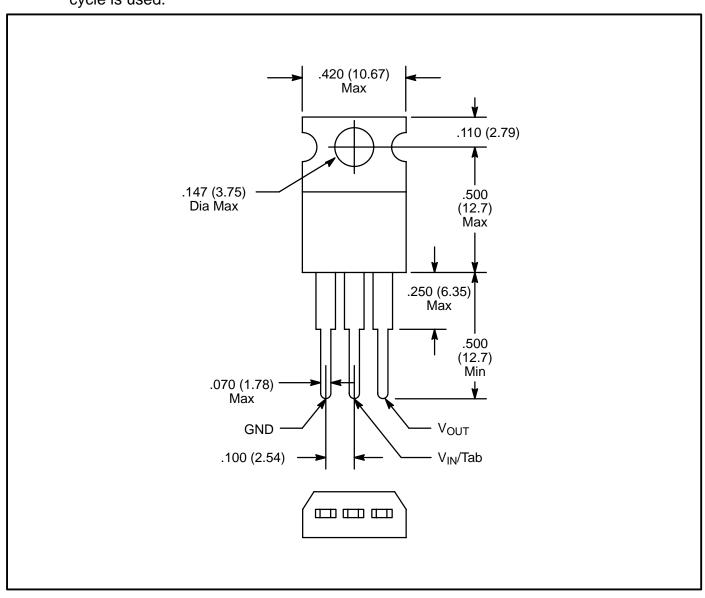
Electrical Characteristics: $(V_{IN} = -14V, I_O = 500 \text{mA}, T_J = 0^{\circ} \text{ to } +125^{\circ}\text{C} \text{ unless otherwise specified})$

Parameter	Symbol	Test Conditions		Min	Тур	Max	Unit
Output Voltage	Vo	$T_J = +25^{\circ}C$		-7.7	-8.0	-8.3	V
		$5mA \le I_O \le 1A, P_O \le 15W, \\ -23V \le V_{IN} \le -10.5V$		-7.6	-8.0	-8.4	V
Line Regulation	Reg _{Line}	$T_J = +25^{\circ}C$, Note 1	$-25V \le V_{1N} \le -10.5V$	_	12	160	mV
			$-17V \le V_{IN} \le -11V$	_	5	80	
Load Regulation	Reg _{Load}	$T_J = +25^{\circ}C$, Note 1	$5\text{mA} \le I_{O} \le 1.5\text{A}$	-	45	160	mV
			$250\text{mA} \le I_O \le 750\text{mA}$	_	16	80	

<u>Electrical Characteristics</u>: $(V_{IN} = -14V, I_O = 500 \text{mA}, T_J = 0^{\circ} \text{ to } +125^{\circ}\text{C} \text{ unless otherwise specified})$

Parameter	Symbol	Test Conditions		Тур	Max	Unit
Quiescent Current	Ι _Β	T _J = +25°C	_	4.3	8.0	mA
Quiescent Current Change	Δl_{B}	$-25V \le V_{IN} \le -10.5V$	_	_	1.0	mA
		$5mA \le I_O \le 1A$	_	_	0.5	
Ripple Rejection	RR	21.5V ≤ V _{IN} ≤ 11.5V, f = 120Hz	_	62	_	dB
Dropout Voltage	$V_{IN} - V_{O}$	$T_J = +25^{\circ}C, I_O = 1A$	_	2.0	_	V
Output Noise Voltage	V _n	$T_A = +25^{\circ}C$, $10Hz \le f \le 100kHz$	_	10	_	μV/V _O
Output Resistance	r _O	f = 1kHz	_	18	-	mΩ
Short–Circuit Current Limit	I _{sc}	$T_A = +25^{\circ}C, V_{IN} = 35V$	_	0.2	_	Α
Peak Output Current	I _{max}	T _J = +25°C	_	2.2	_	Α
Average Temperature Coefficient of Output Voltage	TCV _O		-	-0.6	-	mV/°C

Note 1. Load and line regulation are specified at constant junction temperature. Changes in V_O due to heating effects must be taken into account spearately. Pulse testing with low duty cycle is used.



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