# **N-Channel Power MOSFET** 60 V, 97 A, 7.8 m $\Omega$

#### Features

- Low R<sub>DS(on)</sub>
- High Current Capability
- 100% Avalanche Tested
- These Devices are Pb-Free, Halogen Free and are RoHS Compliant

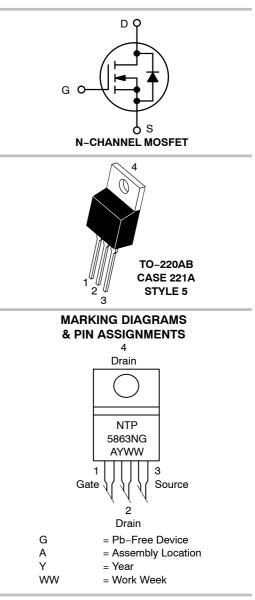
### **MAXIMUM RATINGS** (T<sub>J</sub> = $25^{\circ}$ C Unless otherwise specified)



# **ON Semiconductor®**

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V <sub>(BR)DSS</sub>	R <sub>DS(on)</sub> MAX	I <sub>D</sub> MAX
60 V	7.8 m $\Omega$ @ 10 V	97 A



#### **ORDERING INFORMATION**

See detailed ordering and shipping information in the package dimensions section on page 5 of this data sheet.

Para	Parameter					
Drain-to-Source Volta	Prain-to-Source Voltage			60	V	
Gate-to-Source Voltag	Gate-to-Source Voltage - Continuous			±20	V	
Gate-to-Source Voltag (T <sub>P</sub> < 10 μs)	Gate-to-Source Voltage – Nonrepetitive $(T_P < 10 \ \mu s)$			30	V	
Continuous Drain Current	Steady State	$T_{C} = 25^{\circ}C$	۱ <sub>D</sub>	97	А	
Gurreni	Slale	$T_{C} = 100^{\circ}C$		68		
Power Dissipation	Steady State	$T_C = 25^{\circ}C$	PD	150	W	
Pulsed Drain Current	t <sub>p</sub> = 10 μs		I <sub>DM</sub>	383	А	
Operating and Storage Temperature Range			T <sub>J</sub> , T <sub>stg</sub>	–55 to +175	°C	
Source Current (Body	I <sub>S</sub>	97	А			
Single Pulse Drain-to-Source Avalanche Energy (L = 0.1 mH, $I_{L(pk)}$ = 56 A)			E <sub>AS</sub>	157	mJ	
Peak Diode Recovery (dV/dt)			dV/dt	4.1	V/ns	
Lead Temperature for S Purposes (1/8" from C	ΤL	260	°C			

#### THERMAL RESISTANCE RATINGS

Parameter	Symbol	Мах	Unit
Junction-to-Case (Drain) Steady State	$R_{\theta JC}$	1.0	°C/W
Junction-to-Ambient - Steady State (Note 1)	$R_{\theta JA}$	36	

Stresses exceeding Maximum Ratings may damage the device. Maximum Ratings are stress ratings only. Functional operation above the Recommended Operating Conditions is not implied. Extended exposure to stresses above the Recommended Operating Conditions may affect device reliability.

1. Surface mounted on FR4 board using 1 sq in pad size,

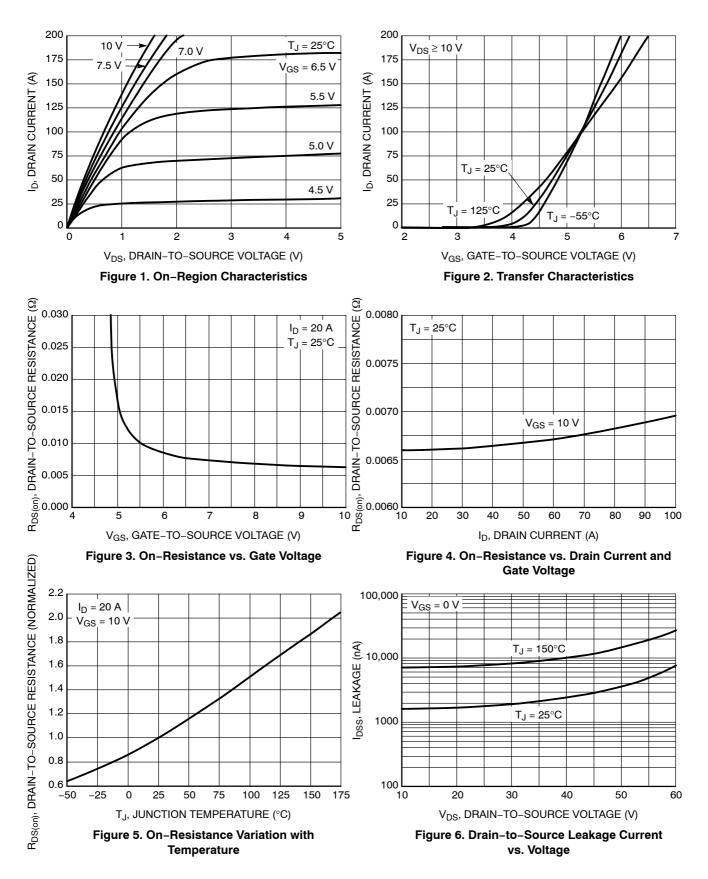
(Cu Area 1.127 sq in [2 oz] including traces).

# **ELECTRICAL CHARACTERISTICS** (T<sub>J</sub> = $25^{\circ}$ C Unless otherwise specified)

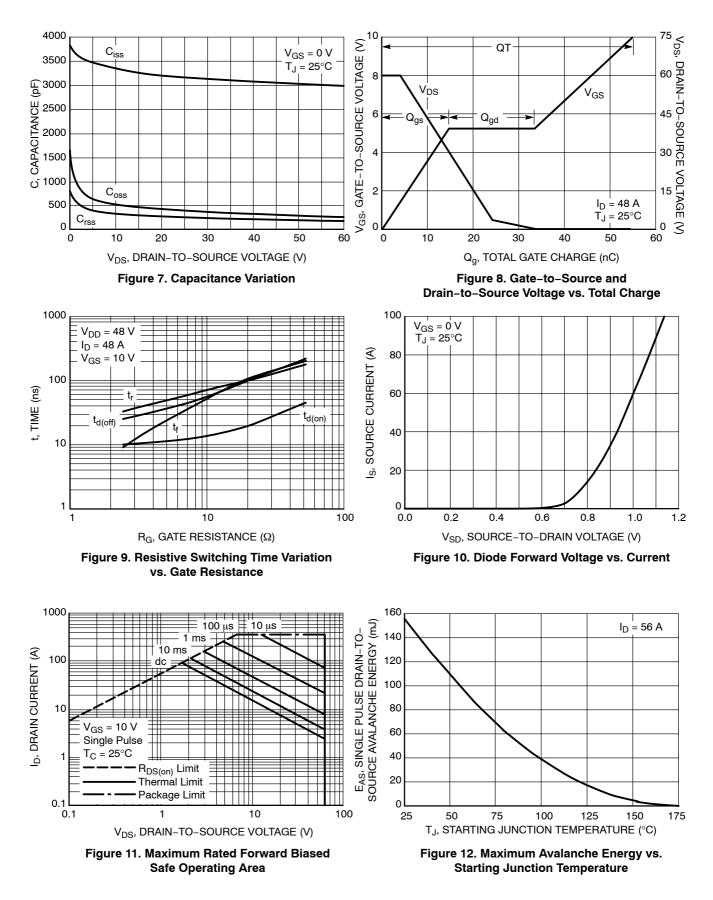
Characteristics	Symbol	Test Condition		Min	Тур	Max	Unit
OFF CHARACTERISTICS							
Drain-to-Source Breakdown Voltage	V <sub>(BR)DSS</sub>	V <sub>DS</sub> = 0 V,	I <sub>D</sub> = 250 μA	60			V
Drain-to-Source Breakdown Voltage Temperature Coefficient	V <sub>(BR)DSS</sub> /T <sub>J</sub>	$I_D = 250 \ \mu A$ , ref to $25^{\circ}C$			47		mV/°C
Zero Gate Voltage Drain Current	$      Gate Voltage Drain Current \qquad I_{DSS} \qquad V_{GS} = 0 V \\ V_{DS} = 60 V $	$V_{GS} = 0 V$	$T_J = 25^{\circ}C$			1.0 μ	μA
		T <sub>J</sub> = 125°C			50		
Gate-Body Leakage Current	I <sub>GSS</sub>	$V_{DS} = 0 V, V_{GS} = \pm 20 V$				±100	nA
ON CHARACTERISTICS (Note 2)							-
Gate Threshold Voltage	V <sub>GS(th)</sub>	$V_{GS} = V_{DS},$	I <sub>D</sub> = 250 μA	2.0		4.0	V
Negative Threshold Temperature Coefficient	V <sub>GS(th)</sub> /T <sub>J</sub>				9.1		mV/°C
Drain-to-Source On-Resistance	R <sub>DS(on)</sub>	V <sub>GS</sub> = 10 V	V, I <sub>D</sub> = 20 A		6.5	7.8	mΩ
Forward Transconductance	9FS	V <sub>DS</sub> = 15 V, I <sub>D</sub> = 30 A			12		S
CHARGES, CAPACITANCES & GATE RESIST	TANCE						-
Input Capacitance	C <sub>iss</sub>	V <sub>DS</sub> = 25 V, V <sub>GS</sub> = 0 V, f = 1 MHz			3200		pF
Output Capacitance	C <sub>oss</sub>				350		1
Transfer Capacitance	C <sub>rss</sub>			230			
Total Gate Charge	Q <sub>G(TOT)</sub>	V <sub>GS</sub> = 10 V, V <sub>DS</sub> = 48 V, I <sub>D</sub> = 48 A			55		nC
Threshold Gate Charge	Q <sub>G(TH)</sub>				3.4		1
Gate-to-Source Charge	Q <sub>GS</sub>				14.5		
Gate-to-Drain Charge	Q <sub>GD</sub>				19		
Gate Resistance	R <sub>G</sub>				0.4		Ω
SWITCHING CHARACTERISTICS, $V_{GS} = 10$ V	(Note 3)						-
Turn-On Delay Time	t <sub>d(on)</sub>				10		ns
Rise Time	tr	V <sub>GS</sub> = 10 V,	V <sub>DD</sub> = 48 V,		34		]
Turn-Off Delay Time	t <sub>d(off)</sub>	$I_D = 48 \text{ A}, R_G = 2.5 \Omega$			25		]
Fall Time	t <sub>f</sub>				9.0		
DRAIN-SOURCE DIODE CHARACTERISTICS	6						-
Forward Diode Voltage	V <sub>SD</sub>	V <sub>GS</sub> = 0 V	$T_J = 25^{\circ}C$		0.96	1.5	V <sub>dc</sub>
	$I_{\rm S} = 48  {\rm A}$ $T_{\rm J} = 150^{\circ} {\rm C}$			0.85		]	
Reverse Recovery Time	t <sub>rr</sub>	$V_{GS} = 0 V_{dc}, I_S = 48 A_{dc}, dI_S/dt = 100 A/\mu s$			32		ns
Charge Time	t <sub>a</sub>				20		
Discharge Time	t <sub>b</sub>				12		
Reverse Recovery Stored Charge	Q <sub>RR</sub>				28		nC

2. Pulse Test: Pulse Width  $\leq$  300 µs, Duty Cycle  $\leq$  2%. 3. Switching characteristics are independent of operating junction temperatures.

## **TYPICAL PERFORMANCE CHARACTERISTICS**



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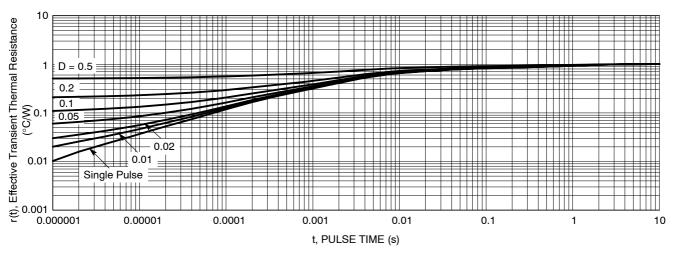


Figure 13. Thermal Response

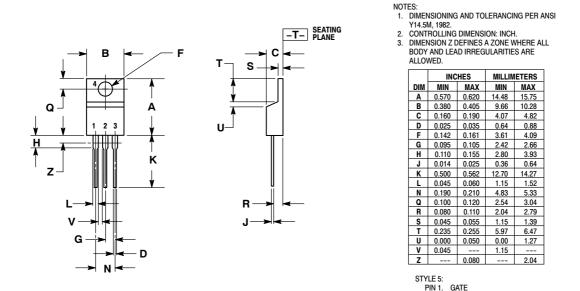
#### **ORDERING INFORMATION**

Device	Package	Shipping <sup>†</sup>		
NTP5863NG	TO-220AB (Pb-Free)	50 Units / Rail		

<sup>†</sup>For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

#### PACKAGE DIMENSIONS

**TO-220** CASE 221A-09 ISSUE AF



2. DRAIN 3. SOURCE 4. DRAIN

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