TOSHIBA Photocoupler GaAs Ired & Photo-Transistor

TLP184

Telephone Use Equipment
Programmable Controllers
AC / DC-Input Module
Telecommunication

The TOSHIBA mini flat coupler TLP184 is a small outline coupler, suitable for surface mount assembly.

TLP184 consist of a photo transistor, optically coupled to a gallium arsenide infrared emitting diode connected inverse parallel, and can operate directly by AC input current.

Collector-emitter voltage: 80V (min)
 Current transfer ratio: 50% (min)
 Rank GB: 100% (min)

Isolation voltage: 3750 Vrms (min)
Operation Temperature:-55 to 110 °C

Safety Standards

UL approved: UL1577, File No. E67349

cUL approved: CSA Component Acceptance Service No. 5A

File No.E67349

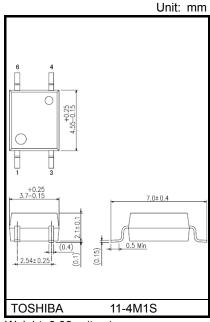
BSI under application: BS EN60065:2002, Certificate No. 9020
 BS EN60950-1:2006, Certificate No. 9021

• Option (V4) type

VDE approved: EN60747-5-2, Certificate No. 40009347 (Note): When a EN60747-5-2 approved type is needed, Please designate "Option(V4)"

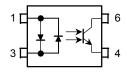
• Construction mechanical rating

Creepage distance : 5.0 mm(min)
Clearance : 5.0 mm(min)
Insulation thickness : 0.4 mm(min)



Weight: 0.08 g (typ.)

Pin Configuration (top view)



1: Anode, Cathode

3: Cathode, Anode

4: Emitter 6: Collector

Current Transfer Ratio

Туре	Classification(*1)	Current Transfer Ratio (%) (I _C /I _F) I _F = 5 mA, V _{CE} = 5 V, Ta = 25°C Min Max		(I _C /I _F)		Marking of classification
	Standard	50	400	Blank, YE, GR, B, GB		
	Rank Y	50	150	YE		
TLP184	Rank GR	100	300	GR		
	Rank BLL	200	400	В		
	Rank GB	100	400	GB		

(Note1): Ex. rank GB: TLP184 (GB,E

(Note) Application type name for certification test, please use standard product type name, i.e. TLP184(GB,E:TLP184

Absolute Maximum Ratings (Ta = 25°C)

	Characteristic	Symbol	Rating	Unit
	R.M.S. forward current	I _{F(RMS)}	±50	mA
Ω	Forward current derating (Ta≥90°C)	ΔI _F / °C	-1.5	mA / °C
LED	Pulse forward current (Note 2)	I _{FP}	±1	А
	Junction temperature	Tj	125	°C
	Collector-emitter voltage	V _{CEO}	80	V
	Emitter-collector voltage	V _{ECO}	7	V
Detector	Collector current	IC	50	mA
Dete	Power dissipation	PC	150	mW
	Power dissipation derating (Ta ≥ 25°C)	ΔP _C / °C	-1.5	mW / °C
	Junction temperature	Tj	125	°C
Оре	erating temperature range	T _{opr}	−55 to 110	°C
Sto	rage temperature range	T _{stg}	−55 to 125	°C
Lea	d soldering temperature (10 s)	T _{sol}	260(10s)	°C
Tota	al package power dissipation	PT	200	mW
Tota	al package power dissipation derating (Ta ≥ 25°C)	ΔP _T / °C	-2.0	mW / °C
Isol	ation voltage (AC,1 min., R.H. ≤ 60%) (Note 3)	BVS	3750	Vrms

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Note 2: Pulse width ≤ 100 µs,f=100 Hz

Note 3: Device considered a two terminal device: Pins 1 and 3 shorted together and 4 and 6 shorted together.

Recommended Operating Conditions (Note)

Characteristic	Symbol	Min	Тур.	Max	Unit
Supply voltage	V _{CC}	_	5	48	V
Forward current	lF	_	16	20	mA
Collector current	IC	_	1	10	mA

Note: Recommended operating conditions are given as a design guideline to obtain expected performance of the device. Additionally, each item is an independent guideline respectively. In developing designs using this product, please confirm specified characteristics shown in this document.

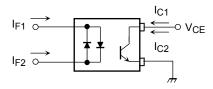
Electrical Characteristics (Ta = 25°C)

	Characteristic	Symbol	Test Condition	Min	Тур.	Max	Unit
LED	Forward voltage	V _F	I _F = ±10 mA	1.1	1.25	1.4	V
	Capacitance	C _T	V = 0, f = 1 MHz	_	60	_	pF
	Collector-emitter breakdown voltage	V _(BR) CEO	I _C = 0.5 mA	80	_	١	٧
ctor	Emitter-collector breakdown voltage	V _(BR) ECO	I _E = 0.1 mA	7	_	_	V
Detector	Collector dark current	lono	V _{CE} = 48 V	_	0.01	0.08	μА
Collector dark current	Collector dark current	ICEO	V _{CE} = 48 V, Ta = 85°C	_	2	50	μΑ
	Capacitance (collector to emitter)	C _{CE}	V = 0, f = 1 MHz	_	10		pF

Coupled Electrical Characteristics (Ta = 25°C)

Characteristic	Symbol	Test Condition	Min	Тур.	Max	Unit
Current transfer ratio	Ic / I _E	$I_F = \pm 5 \text{ mA}, V_{CE} = 5 \text{ V}$	50	_	400	%
Current transfer fatto	10714	Rank GB	100	_	400	70
Saturated CTR	la / la /	I _F = ±1 mA, V _{CE} = 0.4 V	_	60	_	- %
	I _C / I _{F (sat)}	Rank GB	30	_	_	
	VCE (sat)	I _C = 2.4 mA, I _F = ±8 mA	_	_	0.3	V
Collector-emitter saturation voltage		I _C = 0.2 mA, I _F = ±1 mA	-	0.2	_	
		Rank GB	_	_	0.3	
Off-state collector current	I _{C(off)}	V _F = ± 0.7 V, V _{CE} = 48 V	1	1	10	μА
CTR symmetry	I _{C (ratio)}	$I_C (I_F = -5 \text{ mA}) / I_C (I_F = 5 \text{ mA})$ (Note 4)	0.33	1	3	

Note 4:
$$I_{C}(ratio) = \frac{I_{C2}(I_F = I_{F2}, V_{CE} = 5V)}{I_{C1}(I_F = I_{F1}, V_{CE} = 5V)}$$



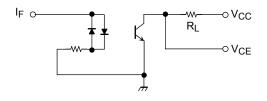
Isolation Characteristics (Ta = 25°C)

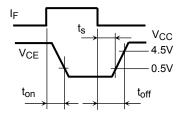
Characteristic	Symbol	Test Condition	Min	Тур.	Max	Unit
Capacitance input to output	Cs	V _S = 0 V, f = 1 MHz	_	8.0	_	pF
Isolation resistance	R _S	V _S = 500 V, R.H. ≤ 60%	1×10 ¹⁰	10 ¹⁴	_	Ω
Isolation voltage	BV _S	AC, 1 minute	3750	_	_	\ <u></u>
		AC, 1 second, in oil	_	10000	_	V _{rms}
		DC, 1 minute, in oil	_	10000	_	V _{dc}

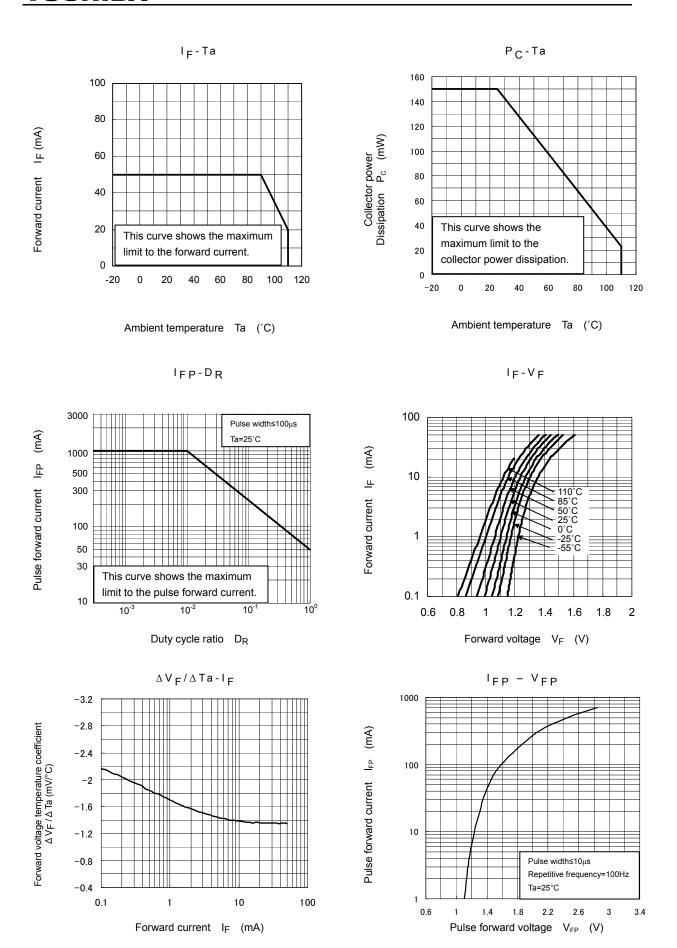
Switching Characteristics (Ta = 25°C)

Characteristic	Symbol	Test Condition	Min	Тур.	Max	Unit
Rise time	t _r		_	5	_	
Fall time	t _f	V _{CC} = 10 V, I _C = 2 mA	_	9	_	
Turn-on time	t _{on}	$R_L = 100 \Omega$	_	9	_	μS
Turn-off time	t _{off}		_	9	_	
Turn-on time	t _{on}		_	2	_	
Storage time	t _S	$R_L = 1.9 \text{ k}\Omega$ (Fig.1) $V_{CC} = 5 \text{ V}, I_F = \pm 16 \text{ mA}$	_	30	_	μS
Turn-off time	t _{off}		_	70	_	

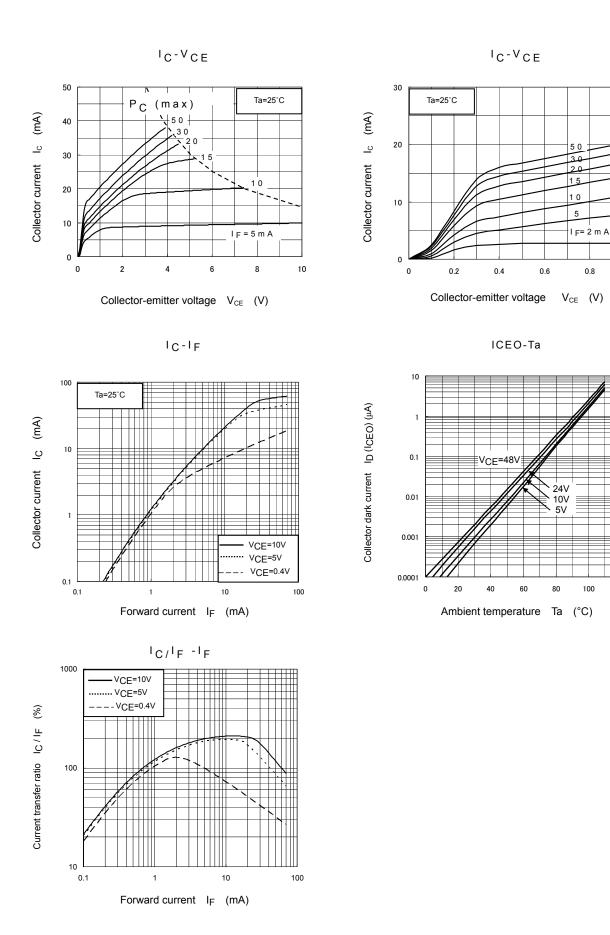
Fig. 1: Switching time test circuit





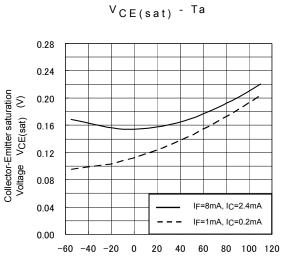


^{*}The above graphs show typical characteristic.

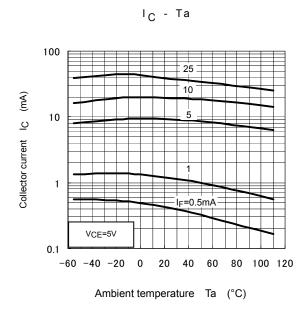


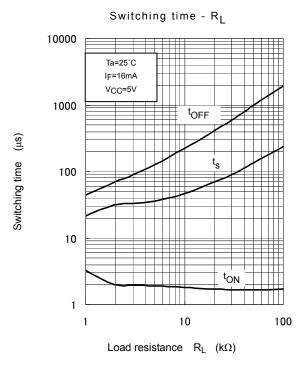
^{*}The above graphs show typical characteristic.

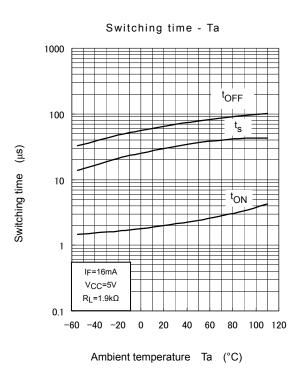
120



Ambient temperature Ta (°C)







^{*}The above graphs show typical characteristic.

Soldering and Storage

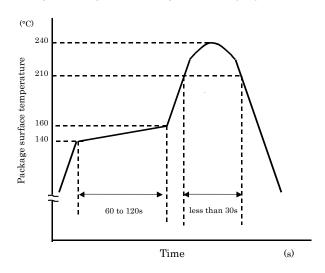
1. Soldering

1.1 Soldering

When using a soldering iron or medium infrared ray/hot air reflow, avoid a rise in device temperature as much as possible by observing the following conditions.

1) Using solder reflow

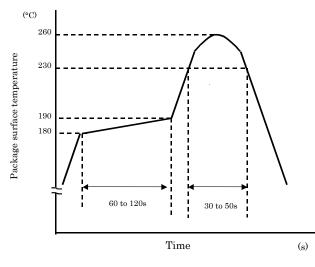
·Temperature profile example of lead (Pb) solder



This profile is based on the device's maximum heat resistance guaranteed value.

Set the preheat temperature/heating temperature to the optimum temperature corresponding to the solder paste type used by the customer within the described profile.

·Temperature profile example of using lead (Pb)-free solder



This profile is based on the device's maximum heat resistance guaranteed value.

Set the preheat temperature/heating temperature to the optimum temperature corresponding to the solder paste type used by the customer within the described profile.

2) Using solder flow (for lead (Pb) solder, or lead (Pb)-free solder)

Please preheat it at 150°C between 60 and 120 seconds.

Complete soldering within 10 seconds below 260°C. Each pin may be heated at most once.

3) Using a soldering iron

Complete soldering within 10 seconds below 260°C, or within 3 seconds at 350°C. Each pin may be heated at most once.

2. Storage

- 1) Avoid storage locations where devices may be exposed to moisture or direct sunlight.
- 2) Follow the precautions printed on the packing label of the device for transportation and storage.
- 3) Keep the storage location temperature and humidity within a range of 5°C to 35°C and 45% to 75%, respectively.
- 4) Do not store the products in locations with poisonous gases (especially corrosive gases) or in dusty conditions.
- 5) Store the products in locations with minimal temperature fluctuations. Rapid temperature changes during storage can cause condensation, resulting in lead oxidation or corrosion, which will deteriorate the solderability of the leads.
- 6) When restoring devices after removal from their packing, use anti-static containers.
- 7) Do not allow loads to be applied directly to devices while they are in storage.
- 8) If devices have been stored for more than two years under normal storage conditions, it is recommended that you check the leads for ease of soldering prior to use.

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