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April 1st, 2010 Renesas Electronics Corporation

Issued by: Renesas Electronics Corporation (http://www.renesas.com)

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H5N3011P

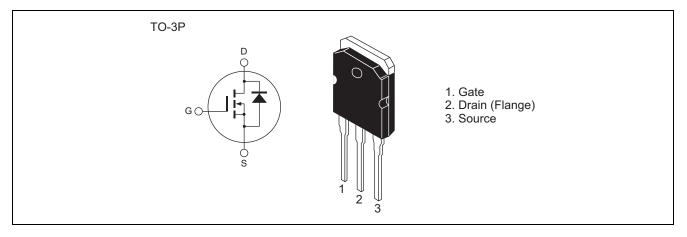
Silicon N Channel MOS FET High Speed Power Switching

> REJ03G0385-0200 Rev.2.00 Aug.05.2004

Features

- Low on-resistance
- Low leakage current
- High speed switching

Outline



Absolute Maximum Ratings

			$(Ta = 25^{\circ}C)$
Item	Symbol	Ratings	Unit
Drain to Source voltage	V _{DSS}	300	V
Gate to Source voltage	V _{GSS}	±30	V
Drain current	I _D	88	А
Drain peak current	Note1 I _{D (pulse)}	176	А
Body-Drain diode reverse Drain current	I _{DR}	88	А
Body-Drain diode reverse Drain peak current	Note1 I _{DR (pulse)}	176	А
Avalanche current	I _{AP} ^{Note3}	30	А
Avalanche energy	E _{AR} ^{Note3}	54	mJ
Channel dissipation	Pch Note2	150	W
Channel to case thermal impedance	θch-c	0.833	°C/W
Channel temperature	Tch	150	°C
Storage temperature	Tstg	-55 to +150	°C

Notes: 1. PW \leq 10 $\mu s,$ duty cycle \leq 1%

2. Value at Tc = $25^{\circ}C$

3. STch = 25° C, Tch $\leq 150^{\circ}$ C



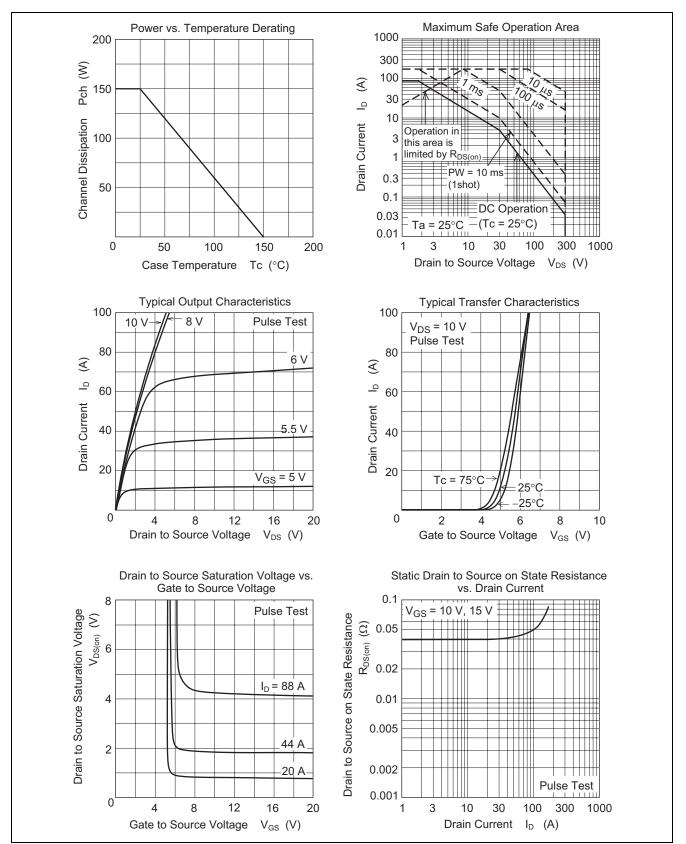
Electrical Characteristics

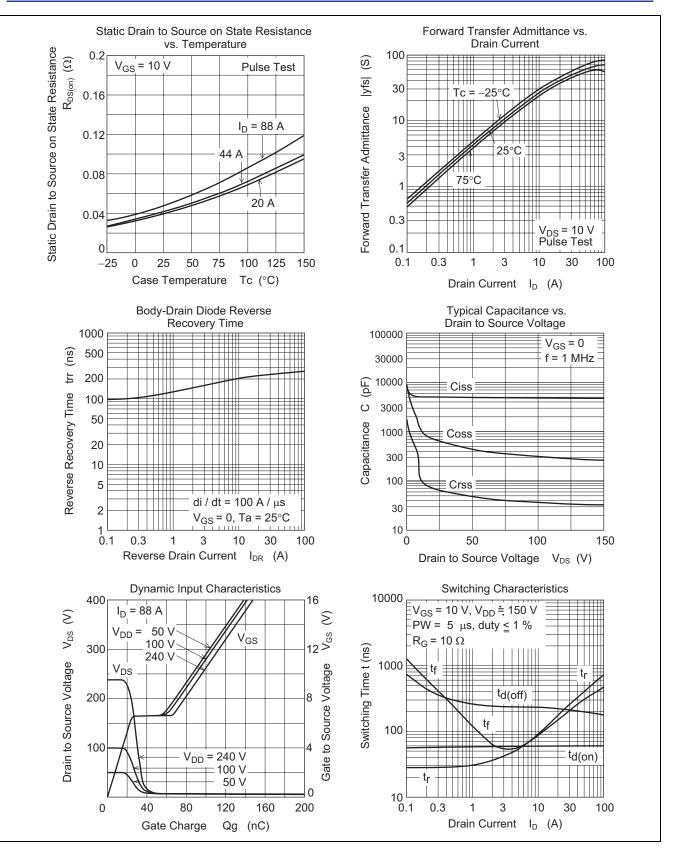
						$(Ta = 25^{\circ}C)$
Item	Symbol	Min	Тур	Max	Unit	Test conditions
Drain to Source breakdown voltage	V _{(BR)DSS}	300	—	_	V	$I_D = 10 \text{ mA}, V_{GS} = 0$
Zero Gate voltage Drain current	I _{DSS}	_	—	1	μΑ	$V_{DS} = 300 \text{ V}, \text{ V}_{GS} = 0$
Gate to Source leak current	I _{GSS}	_	_	±0.1	μΑ	$V_{GS} = \pm 30 \text{ V}, V_{DS} = 0$
Gate to Source cutoff voltage	V _{GS(off)}	3.0	_	4.5	V	$V_{DS} = 10 \text{ V}, I_{D} = 1 \text{ mA}$
Forward transfer admittance	yfs	33	56	_	S	$I_D = 44 \text{ A}, V_{DS} = 10 \text{ V}^{Note4}$
Static Drain to Source on state resistance	R _{DS(on)}	_	0.042	0.048	Ω	$I_D = 44 \text{ A}, V_{GS} = 10 \text{ V}^{Note4}$
Input capacitance	Ciss	_	5000	_	pF	V _{DS} = 25 V
Output capacitance	Coss		640		pF	V _{GS} = 0 f = 1 MHz
Reverse transfer capacitance	Crss	_	65		pF	
Turn-on delay time	t _{d(on)}		60		ns	I _D = 44 A
Rise time	tr	_	370		ns	V _{GS} = 10 V
Turn-off delay time	t _{d(off)}	_	200		ns	$R_L = 3.4 \Omega$
Fall time	t _f	_	280	_	ns	Rg = 10 Ω
Total Gate charge	Qg	_	95	_	nC	V _{DD} = 240 V
Gate to Source charge	Qgs	_	25	_	nC	V _{GS} = 10 V I _D = 88 A
Gate to Drain charge	Qgd	_	40	_	nC	
Body-Drain diode forward voltage	V _{DF}	_	1.0	1.5	V	$I_F = 88 \text{ A}, V_{GS} = 0^{Note4}$
Body-Drain diode reverse recovery time	trr	_	260	—	ns	$I_F = 88 \text{ A}, V_{GS} = 0$
Body-Drain diode reverse recovery charge	Qrr	—	2.5	—	μC	diF/dt = 100 A/µs
Natary A. Dulas test	,					ł

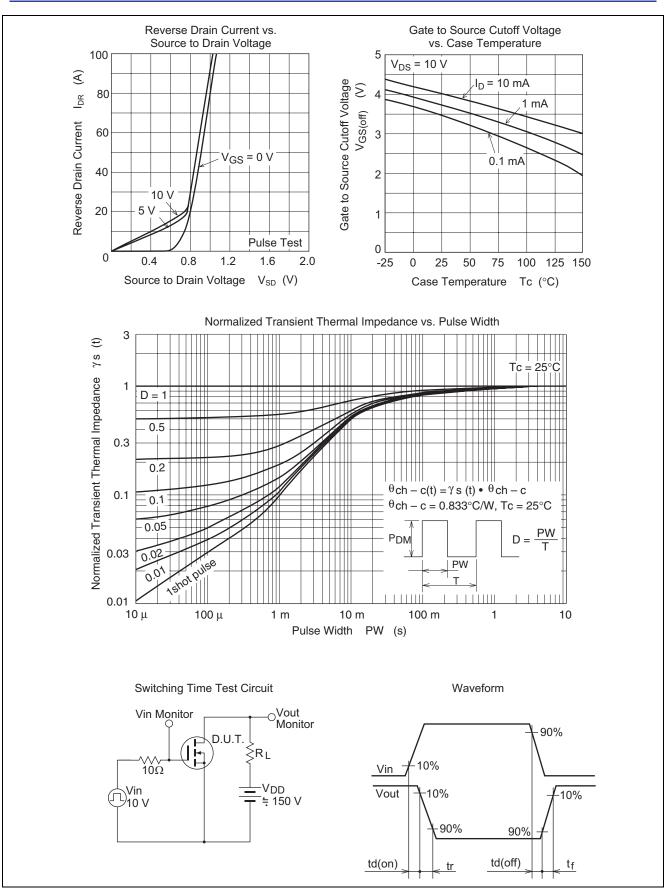
Notes: 4. Pulse test



Main Characteristics

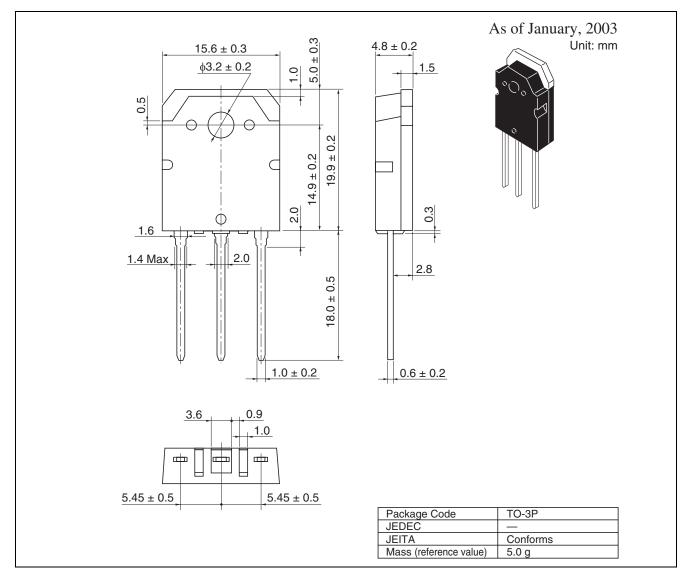








Package Dimensions



Ordering Information

Part Name	Quantity	Shipping Container
H5N3011P-E	30 pcs	Plastic magazine

Note: For some grades, production may be terminated. Please contact the Renesas sales office to check the state of production before ordering the product.



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