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April 1<sup>st</sup>, 2010 Renesas Electronics Corporation

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## **HAT1126R, HAT1126RJ**

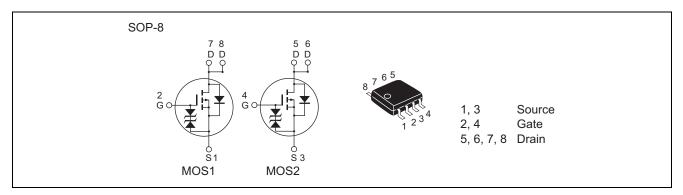
# Silicon P Channel Power MOS FET High Speed Power Switching

REJ03G0406-0100 Rev.1.00 Sep.10.2004

#### **Features**

- Low on-resistance
- Capable of 4.5 V gate drive
- High density mounting
- "J" is for Automotive application High temperature D-S leakage guarantee Avalanche rating

#### **Outline**



#### **Absolute Maximum Ratings**

 $(Ta = 25^{\circ}C)$ 

Name.	Compleal	Rat	11-24		
Item	Symbol	HAT1126R	HAT1126RJ	Unit	
Drain to source voltage	V <sub>DSS</sub>	-60	-60	V	
Gate to source voltage	V <sub>GSS</sub>	±20	±20	V	
Drain current	I <sub>D</sub>	-6.0	-6.0	Α	
Drain peak current	I <sub>D</sub> (pulse) <sup>Note1</sup>	-48	-48	Α	
Avalanche current	I <sub>AP</sub> Note4	_	-6.0	Α	
Avalanche energy	E <sub>AR</sub> Note4	_	3.08	mJ	
Channel dissipation	Pch <sup>Note2</sup>	2	2	W	
Channel dissipation	Pch <sup>Note3</sup>	3	3	W	
Channel temperature	Tch	150	150	°C	
Storage temperature	Tstg	-55 to +150	-55 to +150	°C	

Notes: 1. PW  $\leq$  10 $\mu$ s, duty cycle  $\leq$  1%

- 2. 1 Drive operation: When using the glass epoxy board (FR4 40 x 40 x 1.6 mm), PW  $\leq$  10 s
- 3. 2 Drive operation: When using the glass epoxy board (FR4 40 x 40 x 1.6 mm), PW  $\leq$  10 s
- 4. Value at Tch = 25°C, Rg  $\geq$  50  $\Omega$

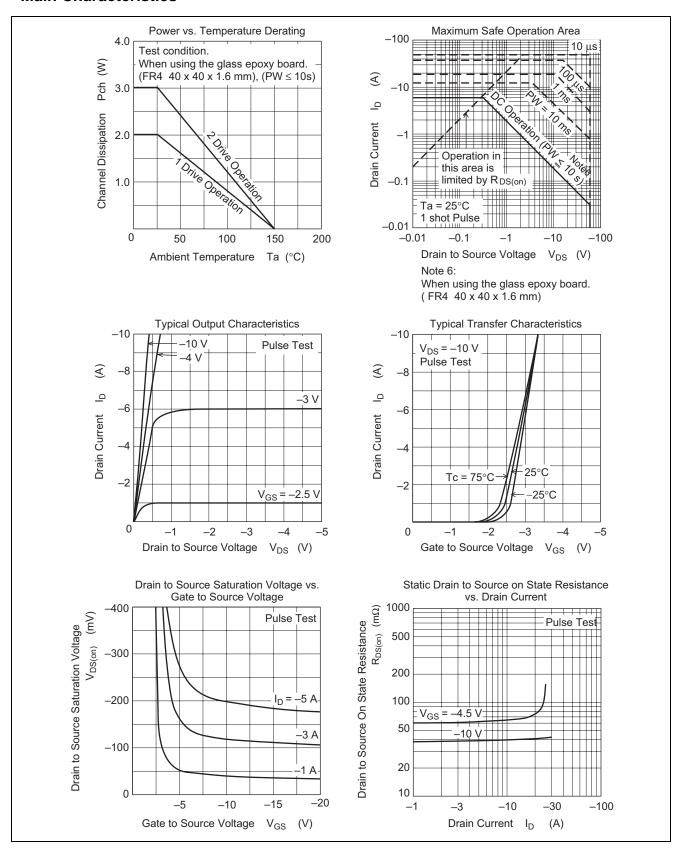
#### **Electrical Characteristics**

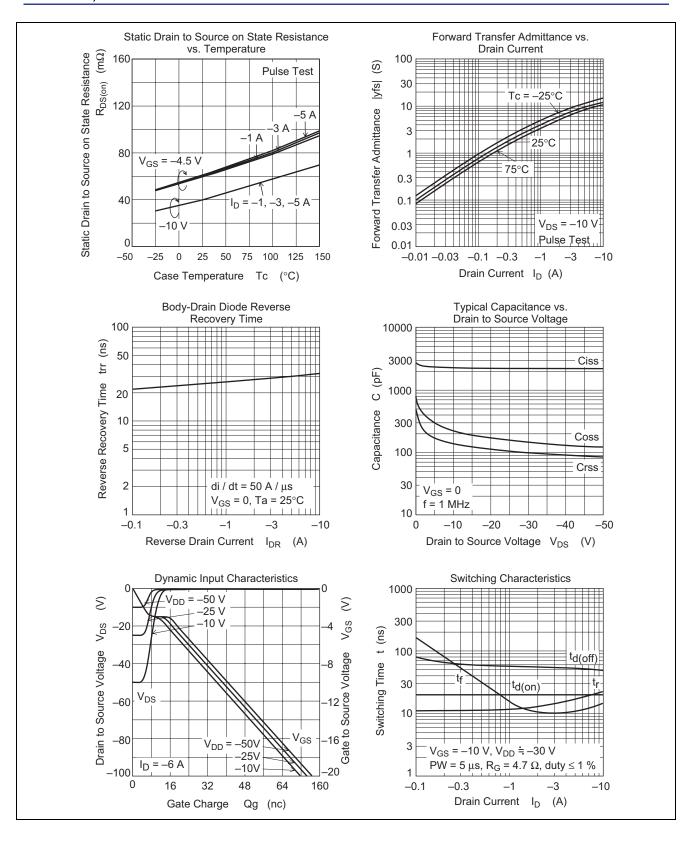
 $(Ta = 25^{\circ}C)$ 

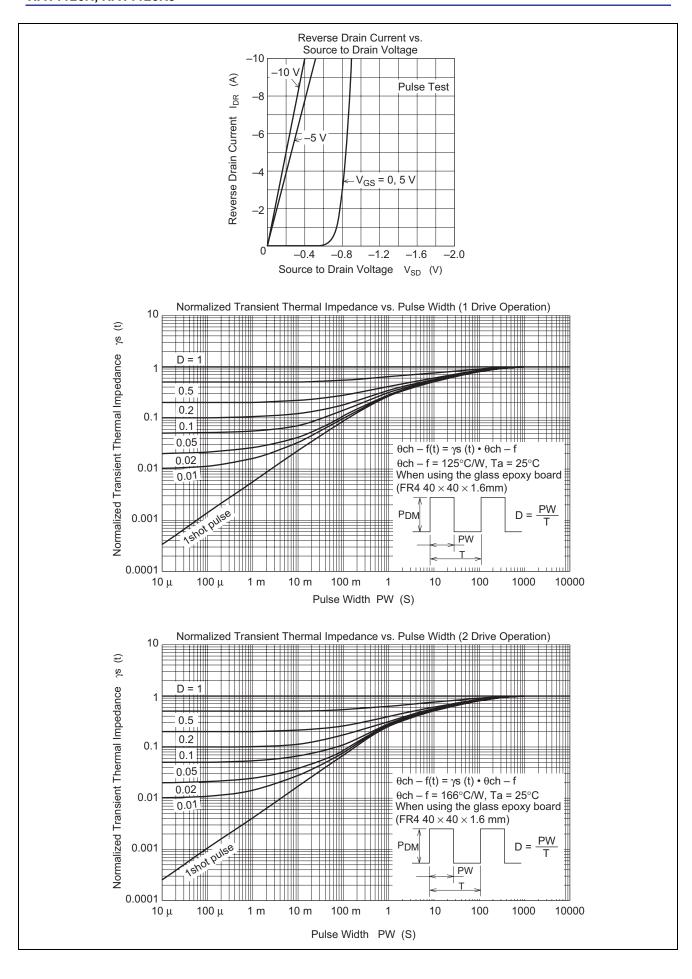
Item		Symbol	Min	Тур	Max	Unit	Unit
Drain to source breakdown		$V_{(BR)DSS}$	-60	_	_	V	$I_D = -10 \text{ mA}, V_{GS} = 0$
voltage							
Gate to Source breakdown voltage		$V_{(BR)GSS}$	±20			V	$I_G = \pm 100 \ \mu A, \ V_{DS} = 0$
Zero gate voltage drain current		$I_{DSS}$	_	_	<b>–</b> 1	μΑ	$V_{DS} = -60 \text{ V}, V_{GS} = 0$
Zero gate voltage	HAT1126R	$I_{DSS}$	_	_	_	μΑ	$V_{DS} = -48 \text{ V}, V_{GS} = 0$
drain current	HAT1126RJ	$I_{DSS}$		_	-10	μΑ	Ta = 125°C
Gate to source leak current		$I_{GSS}$	_	_	±10	μΑ	$V_{GS} = \pm 16 \text{ V}, V_{DS} = 0$
Gate to source cutoff voltage		$V_{GS(off)}$	-1.0	_	-2.5	V	$V_{DS} = -10 \text{ V}, I_{D} = -1 \text{ mA}$
Forward transfer admittance		y <sub>fs</sub>	4.0	7.0	_	S	$I_D = -3.0 \text{ A}^{\text{Note5}}, V_{DS} = -10 \text{ V}$
Static drain to source on state		R <sub>DS(on)</sub>		40	50	mΩ	$I_D = -3.0 \text{ A}^{\text{Note5}}, V_{GS} = -10 \text{ V}$
resistance		R <sub>DS(on)</sub>		60	85	mΩ	$I_D = -3.0 \text{ A}^{\text{Note5}}, V_{GS} = -4.5 \text{ V}$
Input capacitance		Ciss	_	2300	_	pF	$V_{DS} = -10 \text{ V}, V_{GS} = 0$
Output capacitance		Coss	_	230	_	pF	f = 1 MHz
Reverse transfer capacitance		Crss	_	140	_	pF	
Total gate charge		Qg	_	37	_	nC	$V_{DD} = -25 \text{ V}$
Gate to source charge		Qgs	_	6.5		nC	$V_{GS} = -10 \text{ V}$
Gate to drain charge		Qgd	_	8	1	nC	$I_D = -6.0 \text{ A}$
Turn-on delay time		td(on)	_	20	1	ns	$V_{GS} = -10 \text{ V}, I_{D} = -3.0 \text{ A}$
Rise time		tr	_	15	1	ns	$V_{DD} \cong -30 \text{ V}$
Turn-off delay time		td(off)	_	55	1	ns	$R_L = 10 \Omega$
Fall time		tf	_	10		ns	$R_G = 4.7 \Omega$
Body-drain diode forward voltage		$V_{DF}$	_	-0.85	-1.1	V	$I_F = -6.0 \text{ A}, V_{GS} = 0^{\text{Note5}}$
Body-drain diode reverse recovery		trr	_	30	_	ns	$I_F = -6.0 \text{ A}, V_{GS} = 0$
time							diF/dt = 100 A / μs

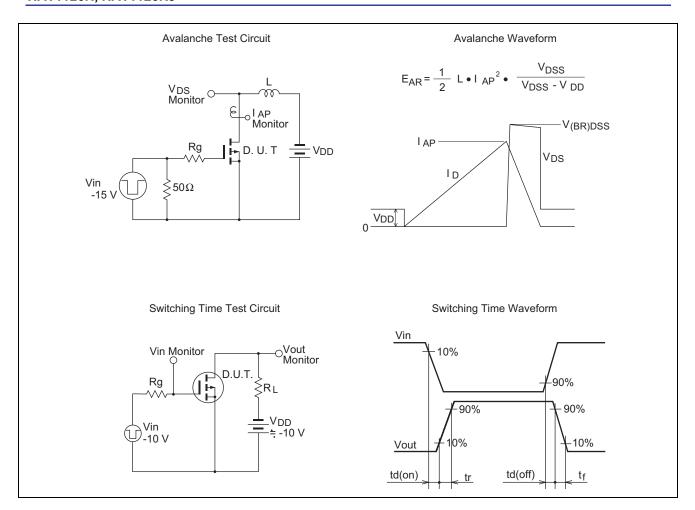
Notes: 5. Pulse test

#### **Main Characteristics**

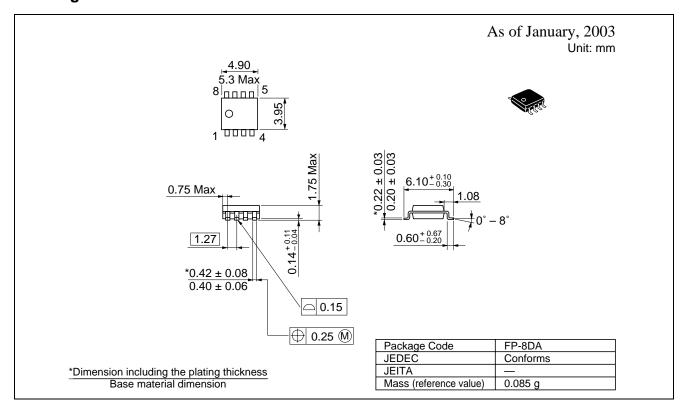








#### **Package Dimensions**



#### **Ordering Information**

Part Name	Quantity	Shipping Container
HAT1126R-EL-E	2500 pcs	Taping
HAT1126RJ-EL-E	2500 pcs	Taping

Note: For some grades, production may be terminated. Please contact the Renesas sales office to check the state of production before ordering the product.

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