TOSHIBA CMOS Digital Integrated Circuit Silicon Monolithic

TC7SZ14F, TC7SZ14FU

Schmitt Inverter

Features

High output current: ±24 mA (min) at V_{CC} = 3 V

• Super high speed operation: t_{pd} = 3.7 ns (typ.)

at $V_{CC} = 5 \text{ V}, 50 \text{ pF}$

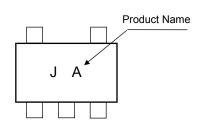
Operation voltage range: V_{CC (opr)} = 1.65 to 5.5 V

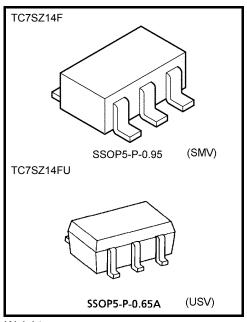
5.5-V tolerant input

• 5.5-V power down protection output

 Matches the performance of TC74LCX series when operated at 3 3-V Vcc

Marking





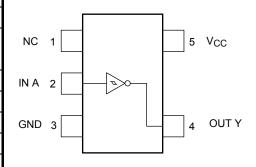
Weight:

SSOP5-P-0.95 : 0.016 g (typ.) SSOP5-P-0.65A : 0.006 g (typ.)

Absolute Maximum Ratings (Ta = 25°C)

Characteristics	Symbol	Rating	Unit	
Supply voltage range	V _{CC}	-0.5 to 6	V	
DC input voltage	V _{IN}	−0.5 to 6	V	
DC output voltage	Vour	-0.5 to 6 (Note 1)	V	
DC output voltage	Vout	-0.5 to V _{CC} +0.5 (Note 2)	V	
Input diode current	I _{IK}	-20	mA	
Output diode current	lok	-20 (Note 3)	mA	
DC output current	lout	±50	mA	
DC V _{CC} /ground current	Icc	±50	mA	
Power dissipation	PD	200	mW	
Storage temperature	T _{stg}	-65 to 150	°C	
Lead temperature (10 s)	TL	260	°C	

Pin Assignment (top view)



Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings and the operating ranges.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Note 1: V_{CC}=0 V

Note 2: High or Low state. Do not exceed I_{OUT} of absolute maximum ratings.

Note 3: VOUT < GND

Start of commercial production 2002-06

IEC Logic Symbol

Truth Table



Α	Υ
L	Н
Н	L

Operating Ranges

Characteristics	Symbol	Rating	Unit	
Supply voltage	Vaa	1.65 to 5.5	V	
Supply voltage	V _{CC}	1.5 to 5.5 (Note4)	\ \ \	
Input voltage	V _{IN}	0 to 5.5	V	
Output voltage	Vour	0 to 5.5 (Note 5)	V	
Output voltage	V _{OUT}	0 to V _{CC} (Note 6)	V	
Operating temperature	T _{opr}	−40 to 85	°C	

Note 4: Date retention only

Note 5: $V_{CC} = 0 V$

Note 6: High or Low State

Electrical Characteristics

DC Electrical Characteristics

Characteristics		Symbol Test Condition				Га = 25°C)	Ta = -40	to 85°C	Unit
Charac	Characteristics Cymbol Test Condition		rest Condition	V _{CC} (V)	Min	Тур.	Max	Min	Max	Offic
				1.65	0.6	1.0	1.4	0.65	1.4	
				1.8	0.7	1.1	1.5	0.7	1.5	
	High lovel			2.3	1.0	1.4	1.8	1.0	1.8	
High level	V _P	_	3.0	1.3	1.75	2.2	1.3	2.2		
			4.5	1.9	2.45	3.1	1.9	3.1		
Threshold				5.5	2.2	2.9	3.6	2.2	3.6	V
voltage Low leve			_	1.65	0.2	0.5	8.0	0.2	8.0	V
				1.8	0.25	0.55	0.9	0.25	0.9	
	Low lovel	V		2.3	0.40	0.75	1.15	0.40	1.15	
	Low level	el V _N		3.0	0.6	1.0	1.5	0.6	1.5	
				4.5	1.0	1.43	2.0	1.0	2.0	
				5.5	1.2	1.7	2.4	1.2	2.4	
				1.65	0.1	0.48	0.9	0.1	1.0	
Hysteresis voltage			1.8	0.15	0.54	1.0	0.15	1.0		
	14		2.3	0.25	0.65	1.1	0.25	1.1		
	ntage	V _H	_	3.0	0.4	0.77	1.2	0.4	1.2	V
				4.5	0.6	1.01	1.5	0.6	1.5	
				5.5	0.7	1.18	1.7	0.7	1.7	

Characteristics		Cumbal	Toot	Condition		-	Га = 25°C)	Ta = -40) to 85°C	Unit
Charac	ciensucs	Symbol	rest	Condition	V _{CC} (V)	Min	Тур.	Max	Min	Max	Offic
				I _{OH} = -100 μA	1.65	1.55	1.65		1.55		
					1.8	1.7	1.8	_	1.7	_	
					2.3	2.2	2.3	_	2.2	_	
					3.0	2.9	3.0	_	2.9	_	
	High level	Vou	\/ \/		4.5	4.4	4.5	_	4.4	_	
	High level	V _{OH}	$V_{IN} = V_N$	I _{OH} = -4 mA	1.65	1.29	1.52		1.29	_	
				I _{OH} = -8 mA	2.3	1.9	2.15		1.9	_	
				I _{OH} = -16 mA	3.0	2.4	2.8		2.4	_	
				I _{OH} = -24 mA	3.0	2.3	2.68		2.3	_	
Output				I _{OH} = -32 mA	4.5	3.8	4.2	_	3.8	_	V
voltage		V _{OL}	V _{IN} = V _P	I _{OL} = 100 μA	1.65	_	0	0.1	_	0.1	· ·
					1.8	_	0	0.1	_	0.1	
					2.3	_	0	0.1	_	0.1	
					3.0	_	0	0.1	_	0.1	
					4.5	_	0	0.1	_	0.1	
	Low level			I _{OL} = 4 mA	1.65	_	0.08	0.24	_	0.24	
				I _{OL} = 8 mA	2.3	_	0.1	0.3	_	0.3	
				I _{OL} = 16 mA	3.0	_	0.15	0.4	_	0.4	
				I _{OL} = 24 mA	3.0	_	0.22	0.55	_	0.55	
				I _{OL} = 32 mA	4.5	_	0.22	0.55	—	0.55	
Input leakage current I _{IN}		I _{IN}	V _{IN} = 5.5 V	or GND	0 to 5.5	_	_	±1	_	±10	μΑ
Power OFF leakage current		I _{OFF}	V _{IN} or V _{OUT} = 5.5 V		0.0	_	_	1	_	10	μА
Quiescent su	pply current	Icc	V _{IN} = 5.5 V	or GND	1.65 to 5.5	_	_	1	_	10	μА

AC Electrical Characteristics (Unless otherwise specified Input: $t_r = t_f = 3$ ns)

Characteristics	Cumbal	Test Condition	et Condition		Га = 25°C		Ta = -40~85°C		Unit
Characteristics	Symbol	rest Condition	V _{CC} (V)	Min	Тур.	Max	Min	Max	Onit
Propagation delay time		t_{pLH} t_{pHL} $C_{L} = 15 \text{ pF,}$ $R_{L} = 1 \text{ M}\Omega$ $C_{L} = 50 \text{ pF,}$ $R_{L} = 500 \Omega$	1.8 ± 0.15	2.0	9.1	15.0	2.0	15.6	ns
			2.5 ± 0.2	1.0	5.0	9.0	1.0	9.5	
	t _{pLH}		3.3 ± 0.3	1.0	3.7	6.3	1.0	6.5	
			5.0 ± 0.5	0.5	3.1	5.2	0.5	5.5	
			3.3 ± 0.3	1.5	4.4	7.2	1.5	7.5	
			5.0 ± 0.5	0.5	3.7	5.9	0.8	6.2	
Input capacitance	C _{IN}	_	0 to 5.5	_	4	_	_	_	pF
Power dissipation capacitance	0	(NInto 7)	3.3	_	24	_	_	_	pF
	C _{PD} (Note 7)		5.5	_	30	_	_	_	pF

Note 7: CPD is defined as the value of the internal equivalent capacitance which is Calculated from the operating current consumption without load.

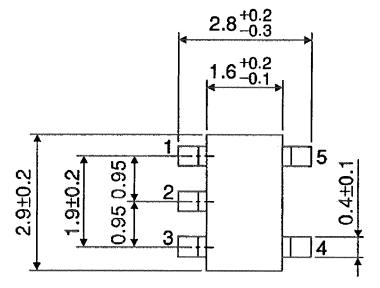
Average operating current can be obtained by the equation.

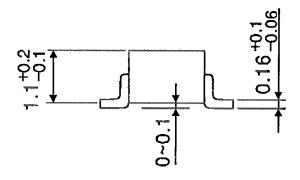
 $I_{CC (opr)} = C_{PD} \cdot V_{CC} \cdot f_{IN} + I_{CC}$

3 2014-03-01

Package Dimensions

SSOP5-P-0.95 Unit: mm





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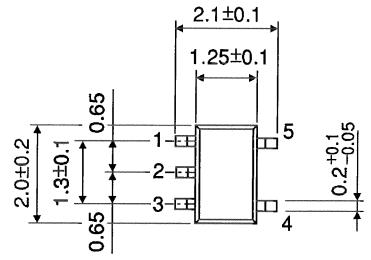
Weight: 0.016 g (typ.)

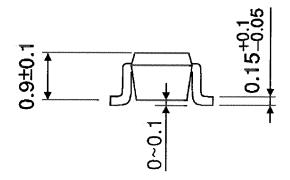


Package Dimensions

SSOP5-P-0.65A Unit: mm

TC7SZ14F/FU





Weight: 0.006 g (typ.)

5 2014-03-01

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