TOSHIBA CMOS Digital Integrated Circuit Silicon Monolithic

TC74VHC32F, TC74VHC32FT, TC74VHC32FK

Quad 2-Input OR Gate

The TC74VHC32 is an advanced high speed CMOS 2-INPUT OR GATE fabricated with silicon gate C^2MOS technology.

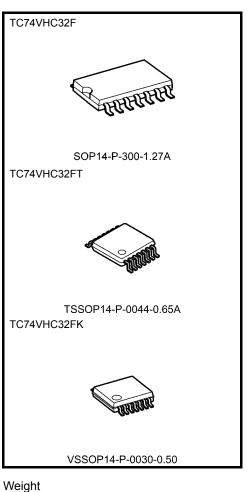
It achieves the high speed operation similar to equivalent Bipolar Schottky TTL while maintaining the CMOS low power dissipation.

The internal circuit is composed of 4 stages including buffer output, which provide high noise immunity and stable output.

An input protection circuit ensures that 0 to 5.5 V can be applied to the input pins without regard to the supply voltage. This device can be used to interface 5 V to 3 V systems and two supply systems such as battery back up. This circuit prevents device destruction due to mismatched supply and input voltages.

Features

- High speed: t_{pd} = 3.8 ns (typ.) at V_{CC} = 5 V
- Low power dissipation: $I_{\rm CC}$ = 2 μA (max) at Ta = 25°C
- High noise immunity: $V_{NIH} = V_{NIL} = 28\% V_{CC}$ (min)
- Power down protection is provided on all inputs.
- Balanced propagation delays: $t_{pLH} \simeq t_{pHL}$
- Wide operating voltage range: $V_{CC (opr)} = 2 V \text{ to } 5.5 V$
- Low noise: VOLP = 0.8 V (max)
- Pin and function compatible with 74ALS32

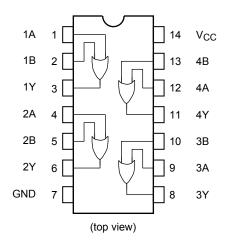


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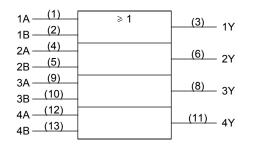
SOP14-P-300-1.27A	: 0.18 g (typ.)
TSSOP14-P-0044-0.65A	: 0.06 g (typ.)
VSSOP14-P-0030-0.50	: 0.02 g (typ.)

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Pin Assignment



IEC Logic Symbol



Truth Table

А	В	Y
Н	Н	Н
L	Н	Н
Н	L	Н
L	L	L

Absolute Maximum Ratings (Note)

Characteristics	Symbol	Rating	Unit
Supply voltage range	V _{CC}	-0.5 to 7.0	V
DC input voltage	VIN	-0.5 to 7.0	V
DC output voltage	V _{OUT}	-0.5 to V _{CC} + 0.5	V
Input diode current	IIК	-20	mA
Output diode current	I _{OK}	±20	mA
DC output current	IOUT	±25	mA
DC V _{CC} /ground current	ICC	±50	mA
Power dissipation	PD	180	mW
Storage temperature	T _{stg}	-65 to 150	°C

Note: Exceeding any of the absolute maximum ratings, even briefly, lead to deterioration in IC performance or even destruction.

Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings and the operating ranges.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Operating Ranges (Note)

Characteristics	Symbol	Rating	Unit
Supply voltage	V _{CC}	2.0 to 5.5	V
Input voltage	VIN	0 to 5.5	V
Output voltage	V _{OUT}	0 to V _{CC}	V
Operating temperature	T _{opr}	-40 to 85	°C
Input rise and fall time	dt/dv	0 to 100 (V _{CC} = 3.3 ± 0.3 V)	ns/V
	uvuv	0 to 20 (V _{CC} = 5 \pm 0.5 V)	115/ V

Note: The operating ranges must be maintained to ensure the normal operation of the device. Unused inputs must be tied to either V_{CC} or GND.

Electrical Characteristics

DC Characteristics

Characteristics	Symbol	Te		Ta = 25°C			Ta = −40 to 85°C		Unit	
	-			V _{CC} (V)	Min	Тур.	Max	Min	Max	
High-level input		_		2.0	1.50	_	_	1.50	_	
voltage	VIH			3.0 to 5.5	V _{CC} × 0.7	_	_	V _{CC} × 0.7	_	V
Low-level input				2.0	_	_	0.50		0.50	
voltage	VIL	—		3.0 to 5.5	_	_	V _{CC} × 0.3	_	V _{CC} × 0.3	V
				2.0	1.9	2.0	_	1.9	_	
	V _{OH}	V _{IN} = V _{IH} or V _{IL}	I _{OH} = −50 µA	3.0	2.9	3.0	—	2.9	—	
High-level output voltage				4.5	4.4	4.5	—	4.4	_	V
J. J			I _{OH} = −4 mA	3.0	2.58		—	2.48		
		I _{OH} = −8 mA	4.5	3.94		—	3.80	-		
		V _{IN} = V _{IL}		2.0	_	0.0	0.1		0.1	
	V _{OL}		I _{OL} = 50 μA	3.0	—	0.0	0.1	—	0.1	
Low-level output voltage				4.5	_	0.0	0.1	_	0.1	V
-			I _{OL} = 4 mA	3.0	—	_	0.36	—	0.44	
			I _{OL} = 8 mA	4.5	_	-	0.36	_	0.44	
Input leakage current	I _{IN}	V _{IN} = 5.5 V or GND		0 to 5.5	—	—	±0.1	_	±1.0	μA
Quiescent supply current	ICC	V _{IN} = V _{CC} or GND		5.5	_	_	2.0	_	20.0	μA

AC Characteristics (input: t_r = t_f = 3 ns)

Characteristics	Symbol	Test Condition		Ta = 25°C			Ta = −40 to 85°C		Unit	
Characteristic Cymbol			V _{CC} (V)	C _L (pF)	Min	Тур.	Max	Min	Max	
Propagation delay t _{pLH} time t _{pHL}		_	3.3 ± 0.3	15	_	5.5	7.9	1.0	9.5	ns
				50	_	8.0	11.4	1.0	13.0	
				15	_	3.8	5.5	1.0	6.5	115
		5.0 ± 0.5	50	_	5.3	7.5	1.0	8.5		
Input capacitance	C _{IN}	_			_	4	10	_	10	pF
Power dissipation capacitance	C _{PD}			(Note)	_	14	_	_	_	pF

Note: C_{PD} is defined as the value of the internal equivalent capacitance which is calculated from the operating current consumption without load.

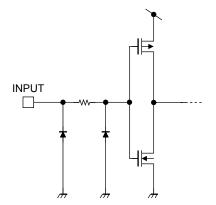
Average operating current can be obtained by the equation:

 $I_{CC (opr)} = C_{PD} \cdot V_{CC} \cdot f_{IN} + I_{CC}/4$ (per gate)

Noise Characteristics (input: t_r = t_f = 3 ns)

Characteristics	Symbol	Test Condition	Ta =	Unit			
Characteristics	Symbol		V _{CC} (V)	Тур.	Limit	Unit	
Quiet output maximum dynamic V_{OL}	V _{OLP}	C _L = 50 pF	5.0	0.3	0.8	V	
Quiet output minimum dynamic V_{OL}	V _{OLV}	C _L = 50 pF	5.0	-0.3	-0.8	V	
Minimum high level dynamic input voltage	VIHD	C _L = 50 pF	5.0	_	3.5	V	
Maximum low level dynamic input voltage	V _{ILD}	C _L = 50 pF	5.0	-	1.5	V	

Input Equivalent Circuit

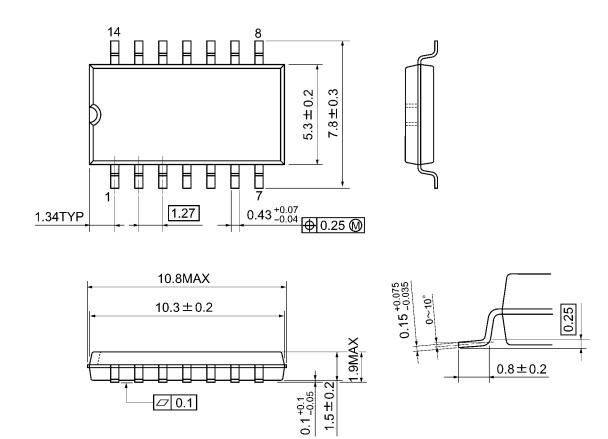




Package Dimensions

SOP14-P-300-1.27A

Unit: mm

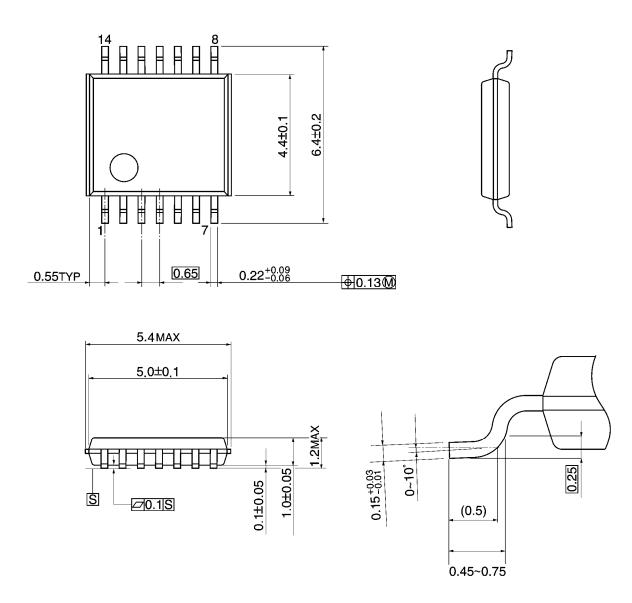


Weight: 0.18 g (typ.)

Package Dimensions

TSSOP14-P-0044-0.65A

Unit: mm



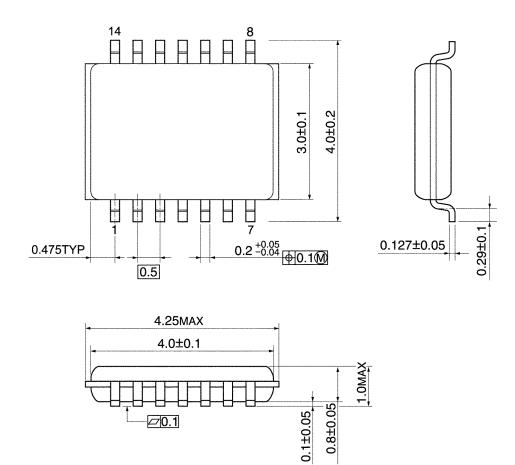
Weight: 0.06 g (typ.)

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Package Dimensions

VSSOP14-P-0030-0.50

Unit: mm



Weight: 0.02 g (typ.)

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