TOSHIBA CMOS Digital Integrated Circuit Silicon Monolithic

TC74LCX14F,TC74LCX14FT,TC74LCX14FK

Low-Voltage Hex Schmitt Inverter with 5-V Tolerant Inputs and Outputs

The TC74LCX14 is a high-performance CMOS schmitt inverter. Designed for use in 3.3-V systems, it achieves high-speed operation while maintaining the CMOS low power dissipation.

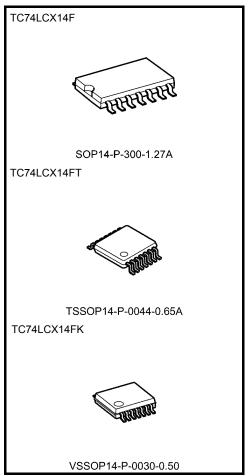
The device is designed for low-voltage (3.3 V) VCC applications, but it could be used to interface to 5-V supply environment for inputs.

Pin configuration and function are the same as the TC74LCX04 but the inputs have hysteresis and with Schmitt trigger function, the TC74LCX14F/FT/FK can be used as a line receivers which will receive slow input signals.

All inputs are equipped with protection circuits against static discharge.

Features

- Low-voltage operation: V_{CC} = 1.65 to 3.6 V
- High-speed operation: $t_{pd} = 6.5 \text{ ns (max)} (V_{CC} = 3.0 \text{ to } 3.6 \text{ V})$
- Ouput current: | I_{OH} | /I_{OL} = 24 mA (min) (V_{CC} = 3.0 V)
- Latch-up performance: $> \pm 500 \text{ mA}$
- Available in JEITA SOP, TSSOP and VSSOP (US)
- Power-down protection provided on all inputs and outputs
- Pin and function compatible with the 74 series (74AC/VHC/HC/F/ALS/LS etc.) 14 type

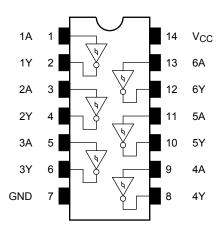


Weight

SOP14-P-300-1.27A : 0.18 g (typ.) TSSOP14-P-0044-0.65A : 0.06 g (typ.) VSSOP14-P-0030-0.50 : 0.02 g (typ.)

Note: The Electrical Characteristics of V_{CC} =1.8±0.15V is only applicable for products which manufactured from January 2009 onward.

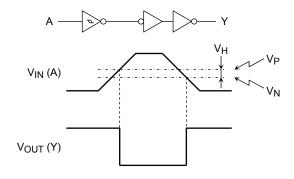
Pin Assignment (top view)



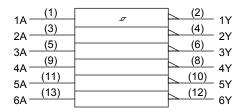
Truth Table

Inputs	Outputs
Α	Y
L	Н
Н	L

System Diagram and waveform



IEC Logic Symbol





Absolute Maximum Ratings (Note 1)

Characteristics	Symbol	Rating	Unit
Power supply voltage	V _{CC}	−0.5 to 7.0	V
DC input voltage	V _{IN}	-0.5 to 7.0	V
		-0.5 to 7.0 (Note 2)	
DC output voltage	Vout	-0.5 to V _{CC} + 0.5 (Note 3)	V
Input diode current	I _{IK}	-50	mA
Output diode current	lok	±50 (Note 4)	mA
DC output current	lout	±50	mA
Power dissipation	PD	180	mW
DC V _{CC} /ground current	I _{CC} /I _{GND}	±100	mA
Storage temperature	T _{stg}	-65 to 150	°C

Note 1: Exceeding any of the absolute maximum ratings, even briefly, lead to deterioration in IC performance or even destruction.

Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings and the operating ranges.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Note 2: $V_{CC} = 0 V$

Note 3: High or low state. IOUT absolute maximum rating must be observed.

Note 4: Vout < GND, Vout > Vcc

Operating Ranges (Note 1)

Characteristics	Symbol	Rating	Unit	
Power supply voltage	V _{CC}	1.65 to 3.6	V	
Tower supply voltage	VCC	1.5 to 3.6 (Note 2)	V	
Input voltage	V _{IN}	0 to 5.5	V	
Output voltage	V _{OUT}	0 to 5.5 (Note 3)	V	
Output voltage	VOU1	0 to V _{CC} (Note 4)		
Output current	I _{OH} /I _{OI}	±24 (Note 5)	mA	
Output current	iOH/iOL	±12 (Note 6)	IIIA	
Operating temperature	T _{opr}	-40 to 85	°C	

Note 1: The operating ranges must be maintained to ensure the normal operation of the device. Unused inputs must be tied to either V_{CC} or GND.

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Note 2: Data retention only

Note 3: $V_{CC} = 0 V$

Note 4: High or low state

Note 5: $V_{CC} = 3.0 \text{ to } 3.6 \text{ V}$

Note 6: $V_{CC} = 2.7 \text{ to } 3.0 \text{ V}$



Electrical Characteristics

DC Characteristics (Ta = -40 to 85°C)

Characteristics		Symbol	Test Condition			Min	Max	Unit	
	ı				V _{CC} (V)				
					1.65	0.7	1.35		
	H-level	V _P	_		2.3	0.95	1.7	V	
Threshold voltage						1.2	2.2		
					1.65	0.3	8.0		
	L-level	V _N	_		2.3	0.45	1.15		
					3.0	0.6	1.5		
					1.65	0.3	8.0		
Hysteresis voltage		VH	_		2.3	0.35	1.0	٧	
					3.0	0.4	1.2		
			$V_{IN} = V_{IL}$	$I_{OH} = -100 \mu A$	1.65 to 3.6	V _{CC} -0.2	_		
				I _{OH} = -4 mA	1.65	1.05	_		
	H-level V	.,		I _{OH} = -8 mA	2.3	1.7	_		
		V _{OH}		I _{OH} = -12 mA	2.7	2.2	_		
				$I_{OH} = -18 \text{ mA}$	3.0	2.4	_		
Outrot valta aa				I _{OH} = -24 mA	3.0	2.2	_	.,	
Output voltage				$I_{OL} = 100 \mu A$	1.65 to 3.6	_	0.2	- V - -	
				I _{OL} = 4 mA	1.65	_	0.45		
		.,	., .,	I _{OL} = 8 mA	2.3	_	0.7		
	L-level	V _{OL}	$V_{IN} = V_{IH}$	I _{OL} = 12 mA	2.7		0.4		
	I _{OL} = 16 mA	I _{OL} = 16 mA	3.0	_	0.4				
				loL	I _{OL} = 24 mA	3.0	_	0.55	
Input leakage current I_{IN} $V_{IN} = 0$ to 5.5 V			1.65 to 3.6	_	±5.0	μΑ			
Power-off leakage c	urrent	l _{OFF}	V _{IN} /V _{OUT} = 5.5 V		0	_	10.0	μΑ	
Quiescent supply cu	ırrent	Icc	V _{IN} = V _{CC} or GND		1.65 to 3.6	_	10.0		
Quiescent supply Co	<u></u>	100	V _{IN} = 3.6 to 5.5 V		1.65 to 3.6	_	±10.0	μΑ	
Increase in Icc per in	Increase in lcc per input ΔI_{CC} $V_{IH} = V_{CC} - 0.6 V$			2.7 to 3.6	_	500			



AC Characteristics (Ta = -40 to 85°C)

Characteristics	Symbol	Test Condition	V _{CC} (V)	Min	Max	Unit
			1.8 ± 0.15	_	25.0	
Propagation delay time	t _{pLH} t _{pHL}	Figure 4 Figure 2	2.5 ± 0.2		8.5	
		Figure 1, Figure 2	2.7	_	7.5	ns
			3.3 ± 0.3	1.5	6.5	
Output to output skew	t _{osLH}	(Note)	2.7	_		ns
Output to output skew	t _{osHL}				1.0	119

Note: Parameter guaranteed by design.

 $(t_{OSLH} = |t_{pLHm} - t_{pLHn}|, t_{OSHL} = |t_{pHLm} - t_{pHLn}|)$

Dynamic Switching Characteristics (Ta = 25°C, input: $t_r = t_f = 2.5$ ns, $C_L = 50$ pF, $R_L = 500$ Ω)

Characteristics	Symbol	Test Condition	V _{CC} (V)	Тур.	Unit
Quiet output maximum dynamic V _{OL}	V _{OLP}	$V_{IH} = 3.3 \text{ V}, V_{IL} = 0 \text{ V}$	3.3	0.8	V
Quiet output minimum dynamic V _{OL}	V _{OLV}	$V_{IH} = 3.3 \text{ V}, V_{IL} = 0 \text{ V}$	3.3	0.8	V

Capacitive Characteristics (Ta = 25°C)

Characteristics	Symbol	Test Condition	V _{CC} (V)	Тур.	Unit
Input capacitance	C _{IN}	_	3.3	7	pF
Output capacitance	C _{OUT}	_	0	8	pF
Power dissipation capacitance	C _{PD}	f _{IN} = 10 MHz (Note	3.3	25	pF

Note: C_{PD} is defined as the value of the internal equivalent capacitance which is calculated from the operating current consumption without load.

Average operating current can be obtained by the equation:

 $I_{CC (opr)} = C_{PD} \cdot V_{CC} \cdot f_{IN} + I_{CC}/6 \text{ (per gate)}$

AC Test Circuit

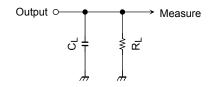


Figure 1

AC Waveform

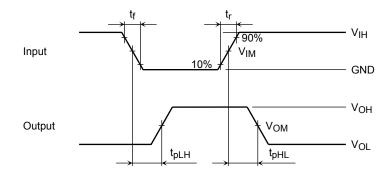


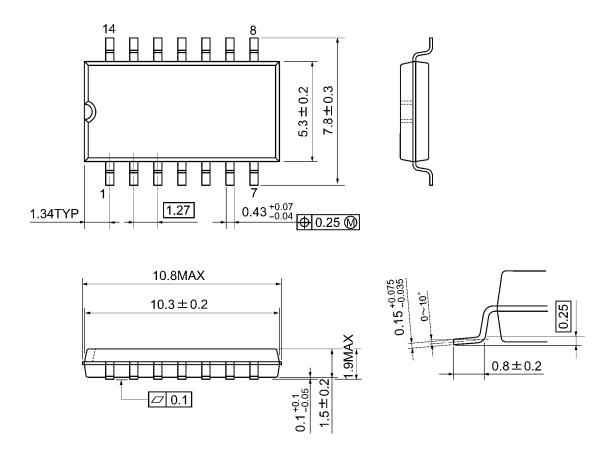
Figure 2 t_{pLH}, t_{pHL}

		Vcc					
	Symbol	$3.3 \pm 0.3 \text{ V}$ 2.7V	$2.5\pm0.2~\textrm{V}$	1.8 ± 0.15 V			
Input	V_{IH}	2.7V	V _{CC}	V _{CC}			
	V _{IM}	1.5V	V _{CC} /2	V _{CC} /2			
	tr,tf	2.5ns	2.0ns	2.0ns			
Output	V _{OM}	1.5V	V _{OH} /2	V _{OH} /2			
Load	CL	50pF	30pF	30pF			
	RL	500Ω	500Ω	1kΩ			

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Package Dimensions

SOP14-P-300-1.27A Unit: mm



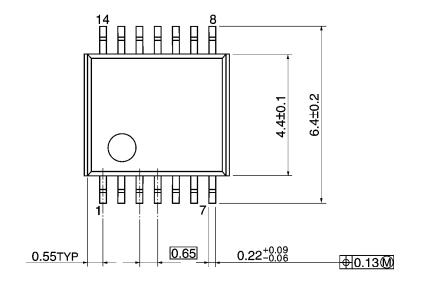
Weight: 0.18 g (typ.)

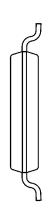
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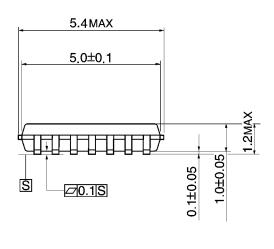
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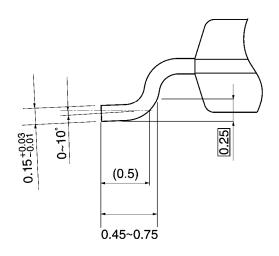
TSSOP14-P-0044-0.65A

Unit: mm







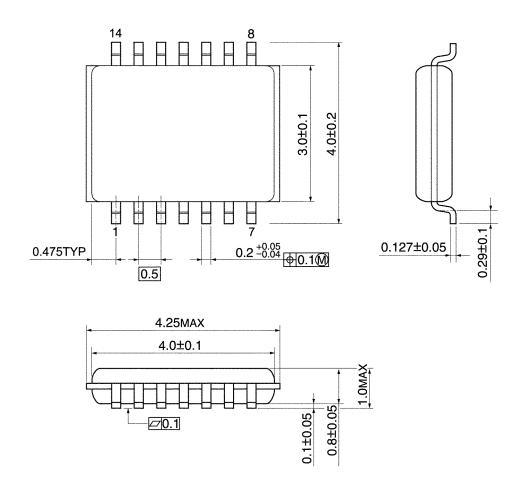


Weight: 0.06 g (typ.)



Package Dimensions

VSSOP14-P-0030-0.50 Unit: mm



Weight: 0.02 g (typ.)

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NLU1GU04CMUTCG NLU1GT14AMUTCG NLU1G04CMUTCG NL17SZU04P5T5G NL17SG14DFT2G 74LVC06ADTR2G
74LVC04ADR2G TC7SZ04AFS,L3J NLU1GT04AMUTCG NLV37WZ04USG NLX3G14FMUTCG NL17SZ04P5T5G NL17SG14P5T5G
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