

S	SPECIFICATIONS	
CUSTOMER	:	
SAMPLE CODE (Ver.) MASS PRODUCTION CODE (V	:	-HWB-H-Q (VER.A)
DRAWING NO. (Ver.)	PC-95005	
C	Customer Approved	
		Date:
Approved	QC Confirmed	Date: Designer
Approved 1.47,18/14-07	QC Confirmed	
Approval For Specifications Only.		Designer
Approval For Specifications Only. * This specification is subject to chan	nge without notice.	Designer A b A '8/i4-0)
Approval For Specifications Only. * This specification is subject to chan Please contact Powertip or it's repr Approval For Specifications and Sam	nge without notice. resentative before designing your prod	Designer A b A '8/i4-0)
Approval For Specifications Only. * This specification is subject to chan	nge without notice. resentative before designing your prod	Designer A b A '8/i4-07
Approval For Specifications Only. * This specification is subject to chan Please contact Powertip or it's repr Approval For Specifications and Sam	nge without notice. resentative before designing your prod	Designer A B A '8/14-07 duct based on this specification.
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Date	Ver.	Description	Page	Design by
2006/7/21	0	PC1602LRS-HWB-H-Q is the ROHS compliant part number based of Powertip's standard PC1602LRS-HWB-H		
2007/8/14	А	Update Timing Characteristics and Display Command		12,14

Total: 24 Page



Contents

1. SPECIFICATIONS

- **1.1 Features**
- **1.2 Mechanical Specifications**
- **1.3** Absolute Maximum Ratings
- **1.4 DC Electrical Characteristics**
- **1.5 Optical Characteristics**
- **1.6 Backlight Characteristics**

2. MODULE STRUCTURE

- 2.1 Counter Drawing
- 2.2 Interface Pin Description
- **2.3 Timing Characteristics**
- 2.4 Display Command
- 2.5 Character Pattern

3. QUALITY ASSURANCE SYSTEM

- 3.1 Quality Assurance Flow Chart
- **3.2 Inspection Specification**

4. RELIABILITY TEST

4.1 Reliability Test Condition

5. PRECAUTION RELATING PRODUCT HANDLING

- 5.1 Safety
- 5.2 Handling
- 5.3 Storage
- 5.4 Terms of Warranty

6. PACKING Specification

Note : For detailed information please refer to IC data sheet : <u>ST7066U, ST7065C</u>



1. SPECIFICATIONS

1.1 Features

Item	Standard Value
Display Type	16 * 2 Characters
LCD Type	STN Gray Transflective Positive Extended Temp.
Driver Condition	LCD Module 1/16 Duty , 1/4 Bias
Viewing Direction	6 O'clock
Backlight	Yellow-Green LED B/L
Weight	34.0g
Interface	—
	THIS PRODUCT CONFORMS THE ROHS OF PTC
ROHS	Detail information please refer web side :
	http://www.powertip.com.tw/news/LatestNews.asp

1.2 Mechanical Specifications

Item	Standard Value	Unit
Outline Dimension	85.0 (L) * 36.0 (w) * 14.5 (H)(Max)	mm
Viewing Area	66.0 (L) *16.0 (w)	mm
Active Area	56.21 (L) * 11.5 (w)	mm
Dot Size	0.56(L) * 0.66 (w)	mm
Dot Pitch	0.60 (L) * 0.70 (w)	mm

Note : For detailed information please refer to LCM drawing

1.3 Absolute Maximum Ratings

Item	Symbol	Condition	Min.	Max.	Unit
Power Supply Voltage	V_{DD}	_	-0.3	7.0	V
LCD Driver Supply Voltage	V_{LCD}	_	VDD-10.0	V _{DD} +0.3	V
Input Voltage	$V_{\rm IN}$		-0.3	V _{DD} +0.3	V
Operating Temperature	T _{OP}	Excluded B/L	-20	70	°C
Storage Temperature	T _{ST}	Excluded D/L	-30	80	°C
Storage Humidity	H _D	Ta<40 °C	-	90	%RH



1.4 DC Electrical Characteristics

$V_{DD} = 5.0 V \pm 0.5 V , V_{SS} = 0 V , Ta = 25^{\circ}C$					2	
Item	Symbol	Condition	Min.	Тур.	Max.	Unit
Logic Supply Voltage	V_{DD}	_	4.5	5.0	5.5	V
"H" Input Voltage	\mathbf{V}_{IH}	_	0.7 Vdd	-	Vdd	V
"L" Input Voltage	V_{IL}	_	-0.3	-	0.6	V
"H" Output Voltage	V _{OH}	l _{OH} =-0.1mA	3.9	-	V_{DD}	V
"L" Output Voltage	V _{OL}	l _{OL} =0.1mA	-	-	0.4	V
Supply Current	I _{DD}	$V_{DD} = 5.0 V$	-	1.5	3.0	mA
		-20°C	-	-	-	
LCM Driver Voltage	V _{OP}	25°C*	6.0	6.2	6.4	V
		70°C	-	-	-	

Note: *1. THE V_{OP} TEST POINT IS V_{DD} - V_{O} .

1.5 Optical Characteristics

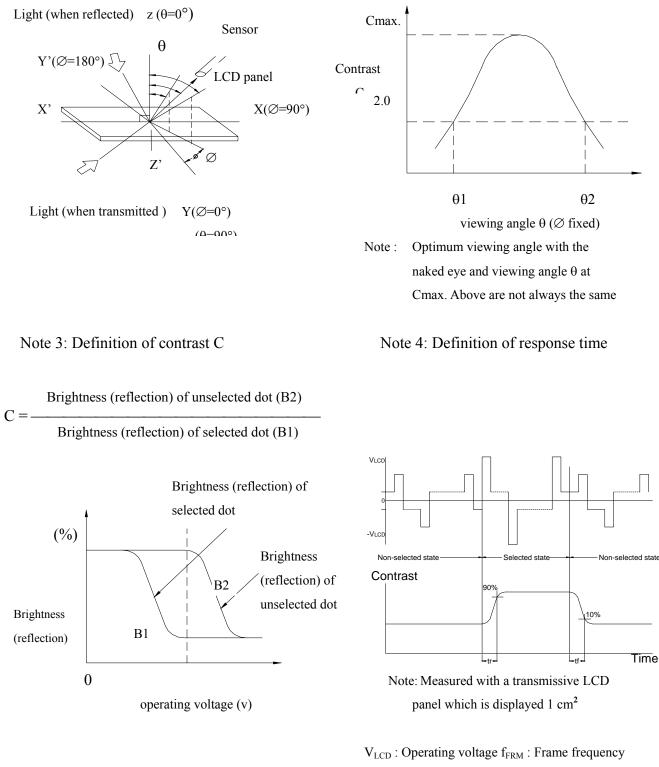
LCD Panel :
$$1/16$$
 Duty , $1/4$ Bias , $V_{LCD} = 6.7$ V , $Ta = 25^{\circ}C$

			5	-		
Item	Symbol	Conditions	Min.	Тур.	Max.	Reference
View Angle	θ	C \geq 2.0, \emptyset = 0°	0°	-	40°	Notes 1 & 2
Contrast Ratio	С	$\theta = 5^{\circ}, \emptyset = 0^{\circ}$	5	7	-	Note 3
Response Time(rise)	tr	$\theta = 5^{\circ}, \emptyset = 0^{\circ}$	-	150 ms	-	Note 4
Response Time(fall)	tf	$\theta = 5^{\circ}, \emptyset = 0^{\circ}$	-	300 ms	-	Note 4



Note 1: Definition of angles θ and \varnothing

Note 2: Definition of viewing angles $\theta 1$ and $\theta 2$



 t_r : Response time (rise) t_f : Response time (fall)



1.6 Backlight Characteristics

LCD Module with LED Backlight

Maximum Ratings

Item	Symbol	Conditions	Min.	Max.	Unit
Forward Current	IF	Ta =25°C	-	300	mA
Reverse Voltage	VR	Ta =25°C	-	8	V
Power Dissipation	РО	Ta =25°C	-	1.38	W

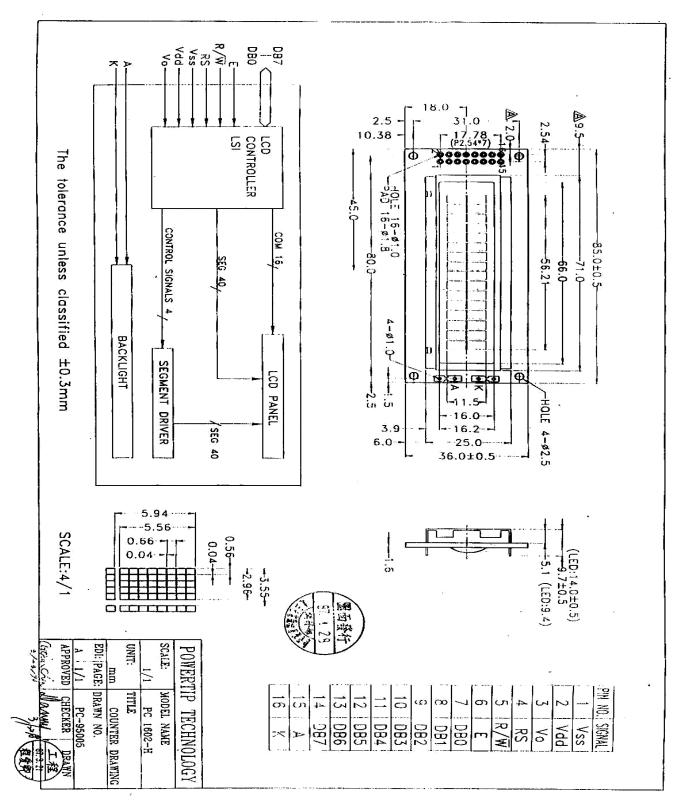
Electrical / Optical Characteristics

	•				Т	`a =25°C
Item	Symbol	Conditions	Min.	Тур.	Max.	Unit
Forward Voltage	VF	IF= 120mA	-	4.2	4.6	V
Reverse Current	IR	VR= 8 V	-	-	0.2	mA
Average Brightness (without LCD)	IV	IF=120mA	220	290	-	cd/m ²
Wavelength	Hue	IF=120mA	569	-	576	nm
Color	Yellow-Green					

POWERTIP

2. MODULE STRUCTURE

2.1 Counter Drawing

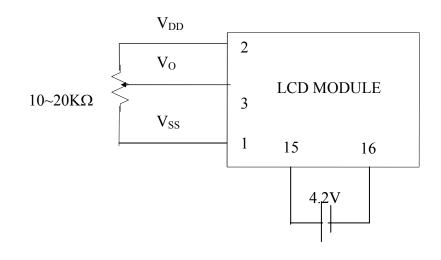




2.2 Interface Pin Description

Pin No.	Symbol	Function
1	Vss	Signal ground (GND)
2	VDD	Power Supply for logic (VDD> Vss)
3	Vo	Operating Voltage for LCD (variable)
		Register selection input
4	RS	High = Data register
4	KS	Low = Instruction register (for write)
		Busy flag address counter (for read)
5		Read/Write signal input is used to select the read/write
5	R/W	mode. High = Read mode, Low = Write mode
6	Е	Start enable signal to read or write the data
		Four low order bi-directional three-state data bus lines.
7-10	$DB0 \sim DB3$	Used
/-10	$DB0 \sim DB3$	for data transfer between the MPU and the LCD module.
		These four are not used during 4-bit operation.
		Four high order bi-directional three-state data bus lines.
11~14	DB4~DB7	Used for data transfer between the MPU and the LCD
11~14	DD4~DD/	module.
		DB7 can be used as a busy flag.
15	А	Power supply for LED backlight (+)
16	K	Power supply for LED backlight (-)

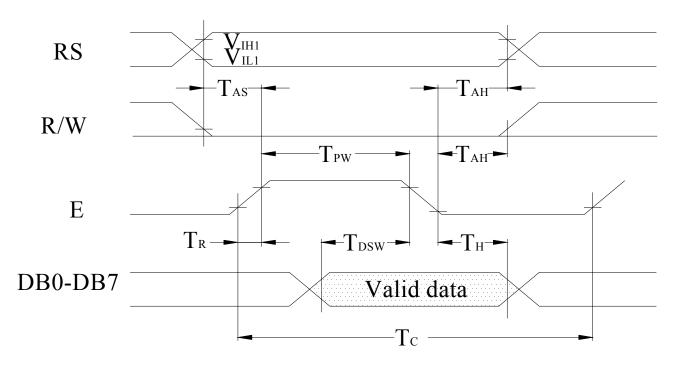
Contrast Adjust



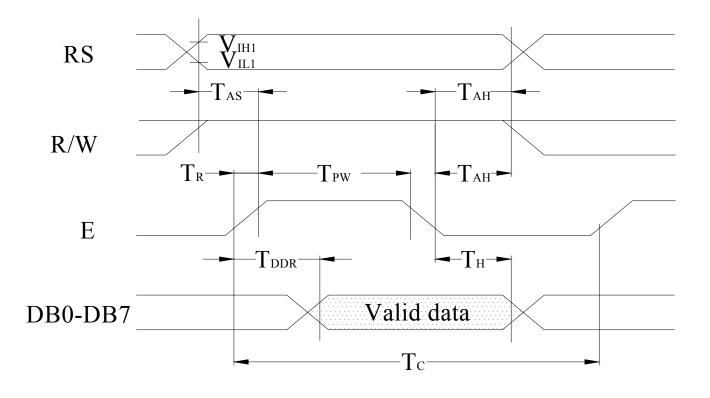


2.3 Timing Characteristics

• Writing data from MPU to ST7066U



• Reading data from ST7066U to MPU





• Write Mode (Writing data from MPU to ST7066U)

 $(VDD = +5V \pm 10\%, Ta = 25^{\circ}C)$

Symbol	Characteristics	Test Condition	Min.	Тур.	Max.	Unit
T _C	Enable Cycle Time	Pin E	1200	-	-	ns
T_{PW}	Enable Pulse Width	Pin E	140	-	-	ns
T_R, T_F	Enable Rise / Fall Time	Pin E	-	-	25	ns
T _{AS}	Address Setup Time	Pins: RS , RW,E	0	-	-	ns
$T_{\rm AH}$	Address Hold Time	Pins :RS,RW,E	10	-	-	ns
$T_{\rm DSW}$	Data Setup Time	Pins:DB0~DB7	40	-	-	ns
T _H	Data Hold Time	Pins:DB0~DB7	10	-	-	ns

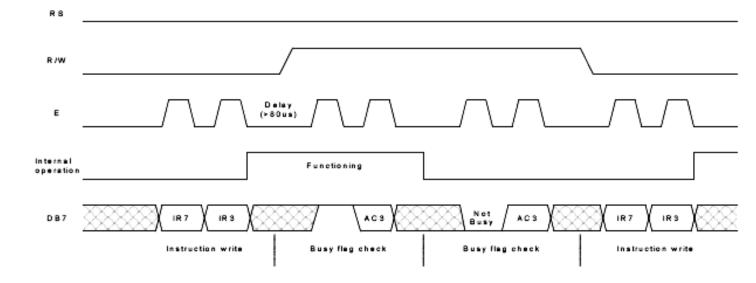
• Read Mode (Reading data from ST7066U to MPU)

	× •					
				(VDD =	+5V <u>+</u> 10%	,Ta=25°C)
Symbol	Characteristics	Test Condition	Min.	Тур.	Max.	Unit
T _C	Enable Cycle Time	Pin E	1200	-	-	ns
T _{PW}	Enable Pulse Width	Pin E	140	-	-	ns
T_R, T_F	Enable Rise / Fall Time	Pin E	-	-	25	ns
T _{AS}	Address Setup Time	Pins: RS , RW,E	0	-	-	ns
T _{AH}	Address Hold Time	Pins :RS,RW,E	10	-	-	ns
T _{DDR}	Data Setup Time	Pins:DB0~DB7	-	-	100	ns
T _H	Data Hold Time	Pins:DB0~DB7	10	-	-	ns

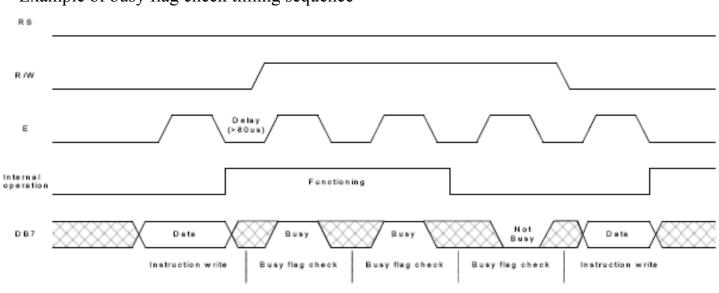


For 4-bit interface date, only four bus lines (DB4 to DB7) are used for transfer.

Example of busy flag check timing sequence



For 8-bit interface date, all eight bus lines (DB0 to DB7) are used .



Example of busy flag check timing sequence



2.4 Display Command

Instructions	Instruction Code										Description	Description Time (270KHz)
	RS	R/W	DB 7	DB 6	DB 5	DB 4	DB 3	DB 2	DB 1	DB 0		
Clear Display	0	0	0	0	0	0	0	0	0	1	Write "20H" to DDRAM. and set DDRAM address to "00H" from AC.	1.52ms
Return Home	0	0	0	0	0	0	0	0	1	×	Set DDRAM address to "00H" from AC and return cursor to it's original position if shifted. The contents of DDRAM are not changed.	1.52ms
Entry Mode Set	0	0	0	0	0	0	0	1	I/D	S	Sets cursor move direction and specifies display shift. These operations are performed during data write and read.	37µs
Display ON/OFF	0	0	0	0	0	0	1	D	С	В	D=1 : entire display on C=1 : cursor on B=1 : cursor position on	37µs
Cursor or Display Shift	0	0	0	0	0	1	S/ C	R/ L	×	×	Set cursor moving and display shift control bit, and the direction, without changing of DDRAM data.	37µs
Function Set	0	0	0	0	1	DL	N	F	×	×	DL: interface data is 8/4 bits NL: number of line is 2/1 F: font size is 5×11/5×8	37µs
Set CGRAM Address	0	0	0	1	AC 5	AC 4	A C 3	A C 2	AC 1	A C 0	Set CGRAM address in address counter.	37µs
Set DDRAM Address	0	0	1	A C 6	AC 5	AC 4	A C 3	A C 2	AC 1	A C 0	Set DDRAM address in address counter.	37µs



Read Busy Flag and Address	0	1	B F	A C 6	AC 5	AC 4	A C 3	A C 2	AC 1	C	Whether during internal operation or not can be known by reading BF. The contents of address counter can also be read.	0µs
Write Data to RAM	1	0	D 7	D 6	D5	D4	D 3	D 2	D1	D 0	Write data into internal RAM (DDRAM/CGRAM).	37µs
Read Data from RAM	1	1	D 7	D 6	D5	D4	D 3	D 2	D1		Read data from internal RAM (DDRAM/CGRAM).	37µs

Note:

Be sure the ST7066U is not in the busy state (BF=0) before sending an instruction from the MPU to the ST7066.

If an instruction is sent without checking the busy flag, the time between the first instruction and next instruction will take much longer than the instruction time itself.

Before checking BF, be sure to wait at least 80us.. Do not keep "E" always "High" for checking BF. Refer to Instruction Table for the list of each instruction execution time .

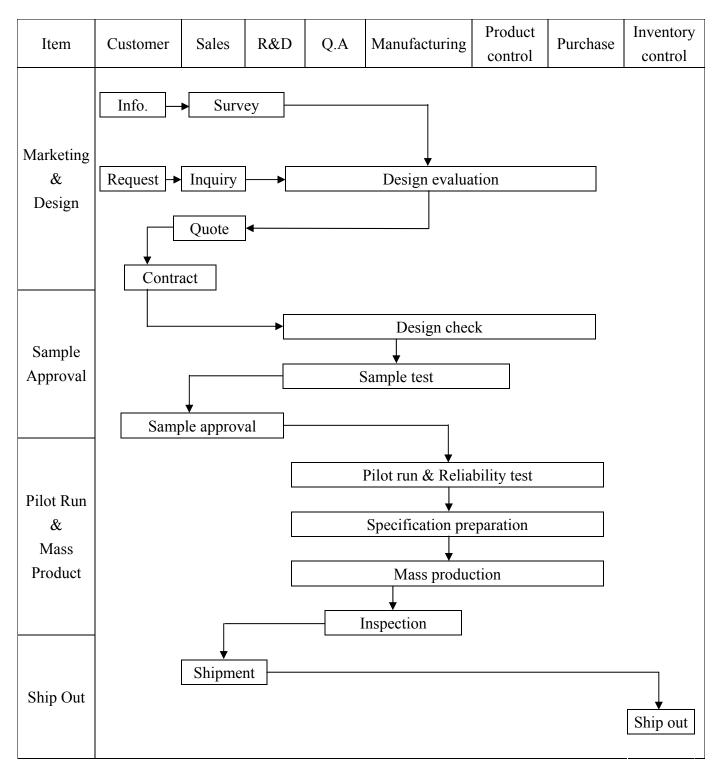
2.5 Character Pattern

CHARACTER PATTERN(EB,WB)

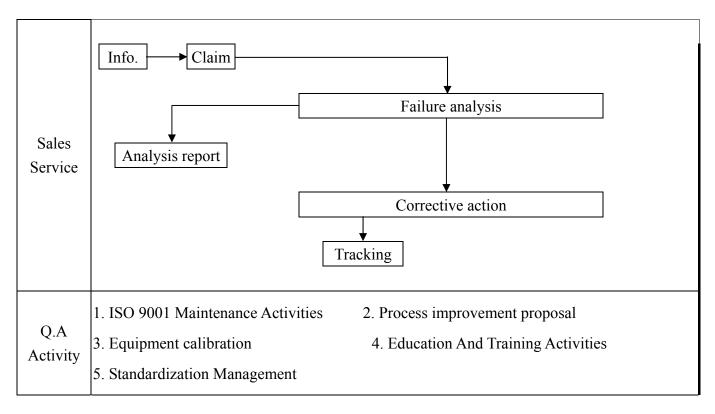
		and t	ligh	4-6	oit (C)4to	D7) of (Cha	ract	er C	ode) (H	exa	deci	mal)
		0	1	2	3	4	5	6	7	8	9	A	В	C	D	E	F
	O	CG RAM (1)	 				[•	=	;	: <u></u> -	:::.		-	ľ		6	.
	1	CG RAM (2)		1	1	 	<u>.</u>	-==	-::-]	· · · ••I	-	i		.]			L.>
	2	CG RAM (3)	'			<u> </u>	R	<u> </u>	I	<u></u>	FF	<u>.</u>	-:-	•:•:•	-	3	
() 	3	CG RAM (4)		#		[]]:	:;	: <u></u> -	<u> </u>	- :::	, 1	 !	-	["	41	===	·
decime	4	CG RAM (5)		3			Ī		Ŧ	-==		-		- <u>-</u> I	[\odot
(Hexa	5	CG RAM (6)	I <u>.</u>		1		II		I]			1	13	-†-	:::]		ЧĮ:
er Code	6	CG RAM (7)	 	8	5		II	Ŧ	II	-	<u>.</u>	÷	۱.,		E	8	j
haracte	7	CG RAM (8)	., !	:*			Ŀ		II	: <u></u>		Fi:	:-::	<u>-</u> i-	÷	I	11]
4-bit (D0 to D3) of Character Code (Hexadecimal)	8	CG RAM (1)		•	3	[]	;>;	ŀ¬] *:	<u></u>	 []	÷		- <u>-</u>		ŀ ::	IP.
D0 to D	9	CG RAM (2)	 		9]	۱ ۲	1	' <u></u> !	.	• •:::•	i	<u></u>	[Π	Л.	
the second se	A	CG RAM (3)	:::	:+:	11 11		·····			:	<u> </u>	 -		1		[].	
Lower	B	CG RAM (4)		[k:	[]	Ŀ	-;	1	 [*•.]			!	ſŗ.	l"	9
	С	CG RAM (5)	====	:=	-	!	••••]		Î	 	 	»]	\$	-	
	D	CG RAM (6)	ı'ı,:					[··I		1	_	Ö	÷		4	T	
	E	CG RAM (7)				ŀ	-• * •-	ŀ"ı	•*•••	Ē	<u></u>	93					
	F	CG RAM (8)	∃	- • • • •		[<u>]</u>		<u> </u>	<u>.</u>	ä		s			C	<u> </u>	



3.1 Quality Assurance Flow Chart





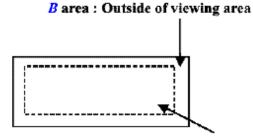




3.2 Inspection Specification

- ◆Inspection Standard : MIL-STD-105E Table Normal Inspection Single Sampling Level Ⅱ.
- ◆Equipment : Gauge、MIL-STD、Powertip Tester、Sample
- ◆Defect Level : Major Defect AQL 0.4; Minor Defect AQL 1.5 .
- OUT Going Defect Level : Sampling .
- ◆Manner of appearance test :
 - (1). The test be under $40W \times 2$ fluorescent light ' and distance of view must be at 30 cm.
 - (2). The test direction is base on about around 45° of vertical line. (Fig. 1)
 - (3). Definition of area . (Fig. 2)

eyes eyes



A area : viewing area

Specification:

NO	Item	Criterion	level			
		1.1 The part number is inconsistent with work order of Production.	Major			
01	Product condition	1.2 Mixed production types.	Major			
		1.3 Assembled in inverse direction.	Major			
02	Quantity	2.1 The quantity is inconsistent with work order of production.	Major			
03	Outline dimension	3.1 Product dimension and structure must conform to Structure diagram.	Major			
		4.1 Missing line character > dot and icon.	Major			
		4.2 No function or no display.				
04	Electrical Testing	4.3 Output data is error.				
		4.4 LCD viewing angle defect.				
		4.5 Current consumption exceeds product specifications.				
05	Black or white dot < scratch < contamination Round type	 5.1 Round type: 5.1.1 display only : • White and black spots on display ≤ 0.25mm, no more than Four white or black spots present. • Densely spaced : NO more than two spots or lines within 	Minor			
		3mm				

◆Specification :

	POW	ERTIP						
NO	Item	Criterion						
05	Black or white dot \cdot scratch \cdot contamination Round type $\downarrow x \downarrow \downarrow \downarrow$ $\Phi = (x+y)/2$ $\Phi = (x+y)/2$	$\begin{array}{c c} & \Phi \\ \hline 0.10 \text{mm} < \\ \hline 0.20 \text{mm} < \\ \hline 0.20 \text{mm} < \\ \hline \hline \\ \hline 0.20 \text{mm} < \\ \hline \hline \\ \hline$	$\frac{(\text{diameter}: \Phi)}{\leq 0.10\text{mm}}$ $\langle \Phi \leq 0.20\text{mm}$ $\langle \Phi \leq 0.25\text{mm}$ Total $(\Phi \leq 0.25\text{mm})$ $(\Phi \leq 0.25\text{mm})$ $(\Phi \leq 0.25\text{mm})$ $(\Phi \leq 0.25\text{mm})$	Acceptan Accept no de Accept no de Accept A area Accept no dense 4		Minor		
06	Polarizer Bubble	Dimension (diamet $\Phi \leq 0.20$ mm 0.20 mm < $\Phi \leq 0.$ 0.50 mm < $\Phi \leq 1.$ $\Phi > 1.00$ mnTotal quantit	n Acc 50mm 00mm 1	Acceptance(area cept no dense 3 2 0 4	Q'ty) B area Don't count Don't count Don't count Don't count Don't count	Minor		
07	The crack of glass	Glass Crack: 7.1 Crack on th	e circuit of elect $\frac{1}{X}$ $\frac{X}{X \le 1/5 \text{ a}}$					



● Sr	pecification :		
NO	Item	Criterion	Level
		 Glass Crack: 7.2 General glass crack and corner edge: 7.2.1 	Z
	The crack of	X Y	Z Minor
	glass	Neglect Out A area	Neglect
07	X: The length of Crack Y: The width of crack	7.2.2	
	Z: The thickness	X Y	Z
	of crack	Neglect Out A area	Neglect
	D: terminal length T: The thickness of glass A : The length of glass		Y 1/3 d



-	ecification :	T .					
NO	Item	Criterion			Level		
07	The crack of glass X: The length of Crack Y: The width of crack Z: The thickness of crack	7.4 Corner cr	rack and medial crack: $ \begin{array}{c} z \\ z \\ \hline y \\ \hline y \\ \hline (0K) \end{array} $	x SP NG]	Minor		
	D: terminal length T: The thickness of glass A : The length of glass	$ \begin{array}{r} X \\ $	Y Crack can't enter viewing area Crack can't exceed the half of width of SP width of SP	$\frac{Z}{\leq 1/2t}$ 1/2t <z 2t<="" \le="" td=""><td></td></z>			
		8.1 Backlight	can't work normally.		Major		
0.0	Backlight	8.2 Backlight doesn't light or color is wrong.					
08	elements	8.3 Illumination source flickers when lit.					
		9.1 pin type must match type in specification sheet					
		9.2 No short circuits in components on PCB or FPC					
09	General appearance	9.3Product packaging must the same as specified on packaging specification sheet.					
		9.4 The folding and peeled off in polarizer are not acceptable					
			B or FPC between B/L assembled PC) is ≤ 1.5 mm	distance	Major		



4. RELIABILITY TEST

4.1 Reliability Test Condition

NO.	TEST ITEM	TEST CONDITION					
1	High Temperature Storage Test	Keep in 80 $\pm 2^{\circ}$ C 96 hrs					
		Surrounding temperature, then storage at normal condition 4hrs					
2	Low Temperature Storage Test	Keep in -30 $\pm 2^{\circ}$ C 96 hrs					
		Surrounding temperature, then storage	ge at normal condition 4hrs				
		Keep in $+60^{\circ}$ C/90%RH duration for	96 hrs				
		Surrounding temperature, then storage	ge at normal condition 4hrs				
2	Llich Humidity Storage	(Excluding the polarizer)Or	0.61				
3	High Humidity Storage	Keep in $+40^{\circ}$ C/90%RH duration for					
		Surrounding temperature, then storag					
4	Vibration Test	1. Sine wave $10 \sim 55$ HZ frequency	· · · ·				
-	vibration rest	2. The amplitude of vibration :1.5					
		3. Each direction (XYZ) duration f	for 2 Hrs				
		Air Discharge:	Contact Discharge:				
		Apply 6 KV with 5 times	Apply 250V with 5 times				
		Discharge foreach polarity +/-	discharge foreach polarity +/-				
		1. Temperature ambinace: 15° C \sim 3	5℃				
		2. Humidity relative: $30\% \sim 60\%$					
5	ESD Test	3. Energy Storage Capacitance(Cs+Cd):150pF±10%					
		4. Discharge Resistance(Rd): $330 \Omega \pm 10\%$					
		5. Discharge, mode of operation:					
		Single Discharge (time between successive discharges at least 1 s)					
		(Tolerance If the output voltage indication: ±5%)					
		$-20^{\circ}\text{C} \rightarrow 25^{\circ}\text{C} \rightarrow 70^{\circ}$	$C \rightarrow 25^{\circ}C$				
		(30mins) (5mins) (30mins) (5mins)					
6	Temperature Cycling Test	4 10 Cycle					
		Surrounding temperature, then storage at normal condition 4hrs					
		1. Sine wave $10 \sim 55$ HZ frequency					
7	Vibration Test (Packaged)	2. The amplitude of vibration :1.5 r	× ,				
		 The amplitude of violation .1.5 mill Each direction (XYZ) duration for 2 Hrs 					
		Packing Weight (Kg)	Drop Height (cm)				
		0~45.4	122				
		45.4 ~ 90.8	76				
8	Drop Test (Packaged)	90.8 ~ 454	61				
		Over 454	46				
		Drop direction : % 3 comer /1 edges /6 sides etch 1 times					
			/ 1 cuges / 0 sides etchi 1 times				

5. PRECAUTION RELATING PRODUCT HANDLING

5.1 SAFETY

- 5.1.1 If the LCD panel breaks , be careful not to get the liquid crystal to touch your skin.
- 5.1.2 If the liquid crystal touches your skin or clothes, please wash it off immediately by using soap and water.

5.2 HANDLING

- 5.2.1 Avoid any strong mechanical shock which can break the glass.
- 5.2.2 Avoid static electricity which can damage the CMOS LSI—When working with the module , be sure to ground your body and any electrical equipment you may be using.
- 5.2.3 Do not remove the panel or frame from the module.
- 5.2.4 The polarizing plate of the display is very fragile. So , please handle it very carefully ,do not touch , push or rub the exposed polarizing with anything harder than an HB pencil lead (glass , tweezers , etc.)
- 5.2.5 Do not wipe the polarizing plate with a dry cloth, as it may easily scratch the surface of plate.
- 5.2.6 Do not touch the display area with bare hands , this will stain the display area.
- 5.2.7 Do not use ketonics solvent & aromatic solvent. Use with a soft cloth soaked with a cleaning naphtha solvent.
- 5.2.8 To control temperature and time of soldering is $320\pm10^{\circ}$ C and 3-5 sec.
- 5.2.9 To avoid liquid (include organic solvent) stained on LCM .

5.3 STORAGE

- 5.3.1 Store the panel or module in a dark place where the temperature is 25° C $\pm 5^{\circ}$ C and the humidity is below 65% RH.
- 5.3.2 Do not place the module near organics solvents or corrosive gases.
- 5.3.3 Do not crush , shake , or jolt the module.

5.4 TERMS OF WARRANTY

5.4.1 Applicable warrant period

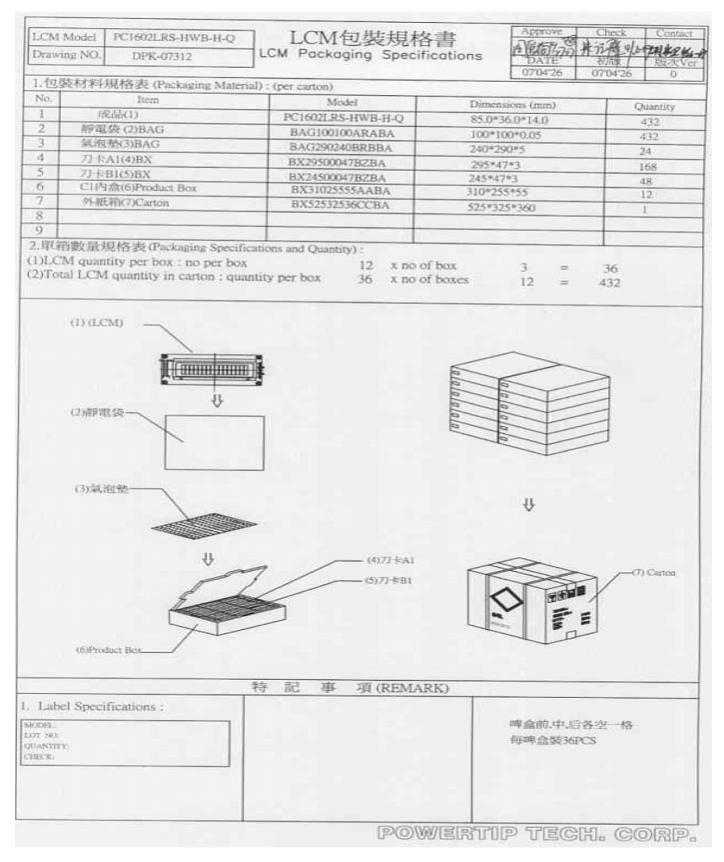
The period is within thirteen months since the date of shipping out under normal using and storage conditions.

5.4.2 Unaccepted responsibility

This product has been manufactured to your company's specification as a part for use in your company's general electronic products. It is guaranteed to perform according to delivery specifications. For any other use apart from general electronic equipment, we cannot take responsibility if the product is used in nuclear power control equipment, aerospace equipment, fire and security systems or any other applications in which there is a direct risk to human life and where extremely high levels of reliability are required.



6. PACKING Specification



PC1602LRS-HWB-H-Q Rev.A (DK)

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 PH800480T013-IHA02
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 PH480272T009-IHA01
 PH800480T024-IHC
 NPC1602LRU-JWT-H
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 PH800480T013-IHA01
 PH800480T013-IHA06
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 PC1601LRU-AWB-B-Q

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 PH320240T023-IHA01
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 PH128800T004-ZFC
 PH320240T023-IHA03
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 PC1604LRS-AWA-B-Q
 NPC1602LRS-FWT-H

 NPC2002WRP-BWA-I
 PC2002LRU-BWT-H-Q
 PE12864LRF-042-H-Q
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