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KA5x0165Rxx-SERIES

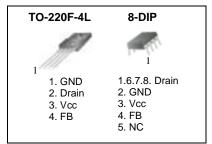
KA5H0165R/RN, KA5M0165R/RN, KA5L0165R/RN, KA5H0165RVN Fairchild Power Switch(FPS)

Features

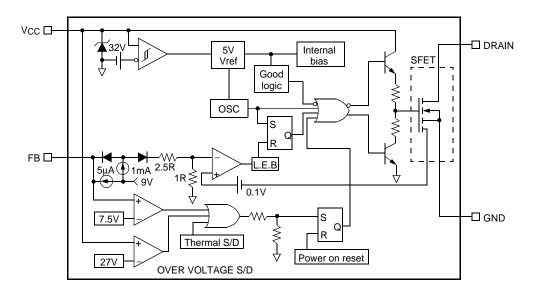
- Precision Fixed Operating Frequency (100/67/50kHz)
- Low Start-up Current (Typ. 100uA)
- Pulse by Pulse Current Limiting
- · Over Load Protection
- Over Voltage Protection (Min. 25V)
 - except KA5H0165RVN
- Internal Thermal Shutdown Function
- Under Voltage Lockout
- Internal High Voltage Sense FET
- · Auto-Restart Mode

Description

The Fairchild Power Switch(FPS) product family is specially designed for an off-line SMPS with minimal external components. The Fairchild Power Switch(FPS) consist of high voltage power SenseFET and current mode PWM IC. Included PWM controller features integrated fixed oscillator, under voltage lock out, leading edge blanking, optimized gate turn-on/turn-off driver, thermal shut down protection, over voltage protection, and temperature compensated precision current sources for loop compensation and fault protection circuitry compared to discrete MOSFET and controller or RCC switching converter solution, The Fairchild Power Switch(FPS) can reduce total component count, design size, weight and at the same time increase efficiency, productivity, and system reliability. It is well suited for cost effective design of flyback converters.



Internal Block Diagram



Absolute Maximum Ratings

(Ta=25°C, unless otherwise specified)

Characteristic	Symbol	Value	Unit
Drain-Gate Voltage (RGS=1MΩ)	VDGR	650	V
Gate-Source (GND) Voltage	Vgs	±30	V
Drain Current Pulsed (1)	IDM	4.0	ADC
Continuous Drain Current (T _C =25°C)	ID	1.0	ADC
Continuous Drain Current (Tc=100°C)	ID	0.7	ADC
Single Pulsed Avalanche Energy (2)	EAS	95	mJ
Maximum Supply Voltage	VCC,MAX	30	V
Analog Input Voltage Range	V _{FB}	-0.3 to V _{SD}	V
Total Power Dissipation	PD	40	W
Total Fower Dissipation	Derating	0.32	W/°C
Operating Junction Temperature.	TJ	+160	°C
Operating Ambient Temperature.	TA	-25 to +85	°C
Storage Temperature Range.	T _{STG}	-55 to +150	°C

Note:

- 1. Repetitive rating: Pulse width limited by maximum junction temperature
- 2. L=24mH, starting Tj=25°C

Electrical Characteristics (SFET Part)

(Ta=25°C unless otherwise specified)

Parameter	Symbol	Condition	Min.	Тур.	Max.	Unit
Drain-Source Breakdown Voltage	BVDSS	VGS=0V, ID=50μA	650	-	-	V
Zero Gate Voltage Drain Current	IDSS	V _{DS} =Max. Rating, V _{GS} =0V	-	-	50	μΑ
		V _{DS} =0.8Max. Rating, V _{GS} =0V, T _C =125°C	-	-	200	μΑ
Static Drain-Source on Resistance (Note)	RDS(ON)	VGS=10V, ID=0.5A	-	8	10	Ω
Forward Transconductance (Note)	gfs	V _{DS} =50V, I _D =0.5A	0.5	-	-	S
Input Capacitance	Ciss	\\ \\\\\\\ \\\\\\\\\\\\\\\\\\\\\	-	250	-	
Output Capacitance	Coss	VGS=0V, VDS=25V, f=1MHz	-	25	-	pF
Reverse Transfer Capacitance	Crss	1-111112	-	10	-	
Turn on Delay Time	td(on)	VDD=0.5B VDSS, ID=1.0A	-	12	-	
Rise Time	tr	(MOSFET switching time is	-	4	-	nS
Turn Off Delay Time	td(off)	essentially independent of	-	30	-	113
Fall Time	tf	operating temperature)	-	10	-	
Total Gate Charge (Gate-Source+Gate-Drain)	Qg	V _{GS} =10V, I _D =1.0A, V _{DS} =0.5B V _{DS} S (MOSFET	-	-	21	
Gate-Source Charge	Qgs	switching time is essentially	-	3	-	nC
Gate-Drain (Miller) Charge	Qgd	independent of operating temperature)	-	9	-	

Note

^{2.}
$$S = \frac{1}{R}$$

^{1.} Pulse test: Pulse width $\leq 300 \mu S,$ duty cycle $\leq 2\%$

Electrical Characteristics (Control Part) (Continued)

(Ta=25°C unless otherwise specified)

Parameter	Symbol	Condition	Min.	Тур.	Max.	Unit		
UVLO SECTION								
Start Threshold Voltage	VSTART	V _{FB} =GND	14	15	16	V		
Stop Threshold Voltage	VSTOP	VFB=GND	8.2	8.8	9.4	V		
OSCILLATOR SECTION								
Initial Accuracy	Fosc	KA5H0165Rxx	90	100	110	kHz		
Initial Accuracy	Fosc	KA5M0165Rx	61	67	73	kHz		
Initial Accuracy	Fosc	KA5L0165Rx	45	50	55	kHz		
Frequency Change With Temperature (2)	ΔΕ/ΔΤ	-25°C ≤ Ta ≤ +85°C	-	±5	±10	%		
Maximum Duty Cycle	Dmax	KA5H0165Rxx	62	67	72	%		
Maximum Duty Cycle	Dmax	KA5M0165Rx KA5L0165Rx	72	77	82	%		
FEEDBACK SECTION								
Feedback Source Current	IFB	Ta=25°C, 0V ≤ Vfb ≤ 3V	0.7	0.9	1.1	mA		
Shutdown Feedback Voltage	VSD	Vfb ≥ 6.5V	6.9	7.5	8.1	V		
Shutdown Delay Current	Idelay	Ta=25°C, 3V ≤ Vfb ≤ VsD	4	5	6	μΑ		
REFERENCE SECTION						•		
Output Voltage (1)	Vref	Ta=25°C	4.80	5.00	5.20	V		
Temperature Stability (1)(2)	Vref/∆T	-25°C ≤ Ta ≤ +85°C	•	0.3	0.6	mV/°C		
CURRENT LIMIT(SELF-PROTECTION)S	ECTION					•		
Peak Current Limit	k Current Limit IOVER Max. inductor current		0.53	0.6	0.67	Α		
PROTECTION SECTION								
Thermal Shutdown Temperature (1)	TSD	-	140	160	-	°C		
Over Voltage Protection	Vovp	except KA5H0165RVN	25	27	29	V		
TOTAL STANDBY CURRENT SECTION								
Start-up Current	ISTART	V _{CC} =14V	-	100	170	μΑ		
Operating Supply Current (Control Part Only)	lop	V _C C ≤ 28	-	7	12	mA		

Note:

- 1. These parameters, although guaranteed, are not 100% tested in production
- 2. These parameters, although guaranteed, are tested in EDS (wafer test) process

Typical Performance Characteristics

(These characteristic graphs are normalized at Ta=25°C)

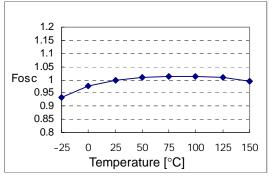


Figure 1. Operating Frequency

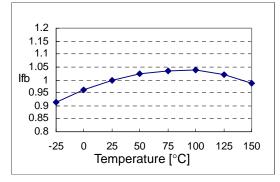


Figure 2. Feedback Source Current

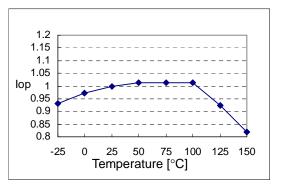


Figure 3. Operating Supply Current

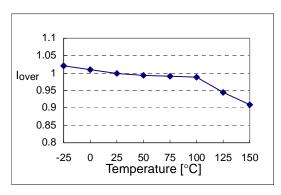


Figure 4. Peak Current Limit

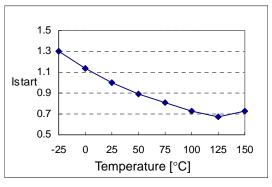


Figure 5. Start up Current

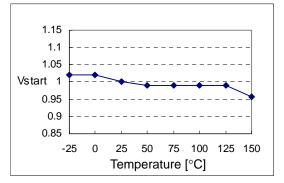


Figure 6. Start Threshold Voltage

Typical Performance Characteristics (Continued)

(These characteristic graphs are normalized at Ta=25°C)

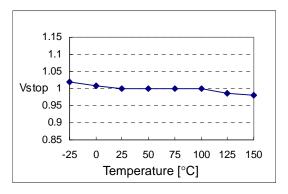


Figure 7. Stop Threshold Voltage

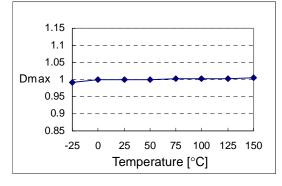


Figure 8. Maximum Duty Cycle

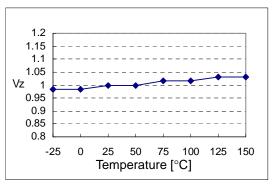


Figure 9. V_{CC} Zener Voltage

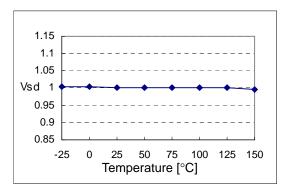


Figure 10. Shutdown Feedback Voltage

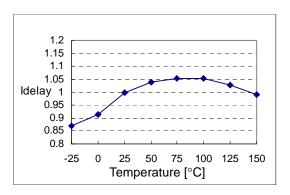


Figure 11. Shutdown Delay Current

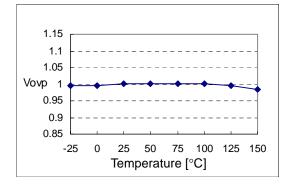


Figure 12. Over Voltage Protection

Typical Performance Characteristics (Continued)

(These characteristic graphs are normalized at Ta=25°C)

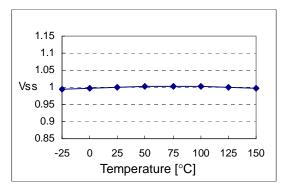


Figure 13. Soft Start Voltage

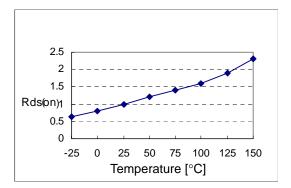
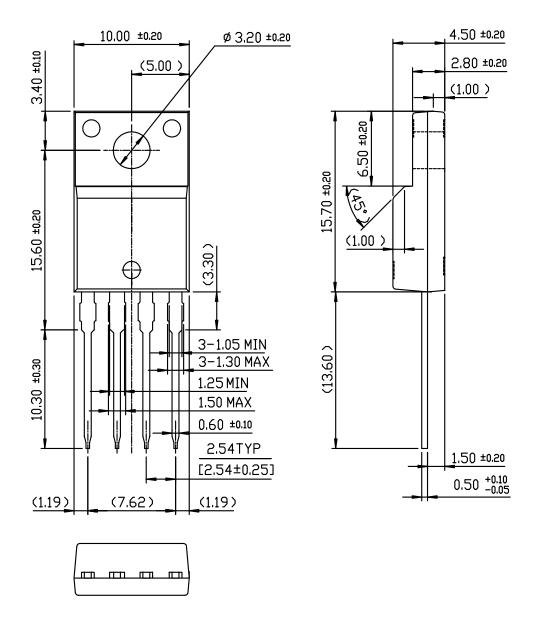


Figure 14. Static Drain-Source on Resistance

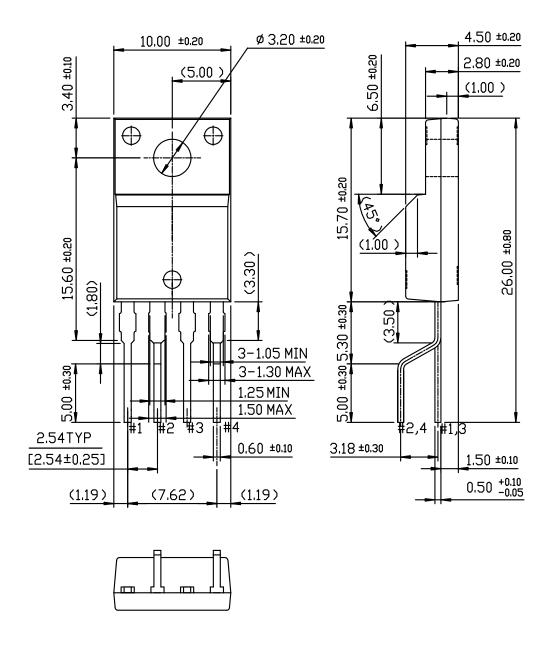
Package Dimensions

TO-220F-4L



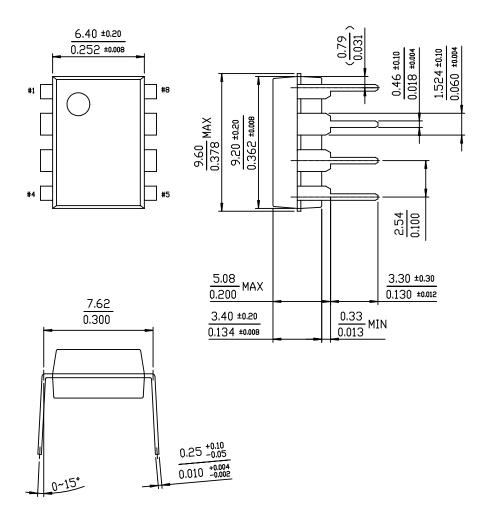
Package Dimensions (Continued)

TO-220F-4L(Forming)



Package Dimensions (Continued)

8-DIP



Ordering Information

Product Number	Package	Marking Code	BVDSS	Fosc	RDS(on)	
KA5H0165RTU	TO-220F-4L	5H0165R	650V	100kHz	8Ω	
KA5H0165RYDTU	TO-220F-4L(Forming)	31101031	030 V	TOOKITZ	052	
KA5M0165RTU	TO-220F-4L	5M0165R	650V	67kHz	8Ω	
KA5M0165RYDTU	TO-220F-4L(Forming)	3101010310	030 V	OTRITZ	052	
KA5L0165RTU	TO-220F-4L	5L0165R	650V	50kHz	8Ω	
KA5L0165RYDTU	TO-220F-4L(Forming)	32010310	050 V	JON 12	022	
KA5H0165RN	8-DIP	5H0165R	650V	100kHz	8Ω	
KA5M0165RN	8-DIP	5M0165R	650V	67kHz	8Ω	
KA5L0165RN	8-DIP	5L0165R	650V	50kHz	8Ω	

Product Number	Package	Marking Code	BVDSS	Fosc	RDS(on)
KA5H0165RVN	8-DIP	5H0165RV	650V	100kHz	Ω 8

TU : Non Forming Type YDTU : Forming Type

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