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Package Dimensions

Features

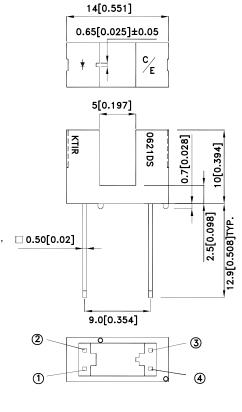
- High sensing accuracy.
- High current transfer ratio.
- Both-sides mounting type.
- RoHS Compliant.

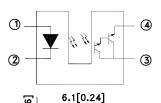
Applications

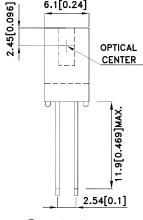
- printers, facsimiles, etc.
- VCRs.











- 1 : Anode
- (2): Cathode
- 3 : Collector
- Emitter

- Notes:
 1. All dimensions are in millimeters (inches).
- 2. Tolerance is $\pm 0.25(0.01")$ unless otherwise noted.
- Lead spacing is measured where the leads emerge from the package.
 The specifications, characteristics and technical data described in the datasheet are subject to change without prior notice.

*Absolute Maximum Ratings(Ta=25°C)

Parameter			Rating	Unit
Input	Forward current		50	mA
	Reverse voltage	V _R	6	V
	Power dissipation	Pd	75	mW
	Peak Forward Current (Pulse Width ≤100uS, Duty Cycle =1%)	I _{FP}	1	А
	Collector-emitter voltage	V _{CEO}	35	٧
Output	Emitter-collector voltage	V _{ECO}	6	V
Output	Collector current	Ic	40	mA
	Collector power dissipation	Pc	75	mW
Operating temperature		Topr	-25~+85	°C
Storage temperature		Tstg	-40~+100	°C
soldering te	emperature (1/16 inch from body for 5 seconds)	Tsol 260 °C		°C

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*Electro-optical Characteristics(Ta=25°C)

Parameter			Symbol	Conditions	Min.	TYP.	Max.	Unit
Input	Forward Voltage		V _F	I _F =20mA	1.0	1.2	1.5	V
	Peak forward voltage		V_{FM}	I _{FM} = 0.5A	-	2	3	V
	Reverse Current		I _R	V _R =6V	-	-	10	μΑ
	Peak Wavelength		λР	I _F =20mA	-	940	-	nm
Output	Collector dark current		I _{CEO}	V _{CE} =10V I _F =0mA	-	-	10 ⁻⁶	Α
Transfer charact- eristics	Current transfer ratio		CTR	V _{CE} =2V I _F =1mA	-	200	-	%
	Collector-emitter saturation voltage		V _{CE (SAT)}	I _F =2mA I _C =1mA	-	-	1.0	V
	Response time	Rise time	tr	V_{CE} =2V I_{C} =10mA R_{L} =100 Ω	-	90	400	μsec
		Fall time	tf		-	80	300	μsec

Fig. 1 Forward Current vs. Forward Voltage

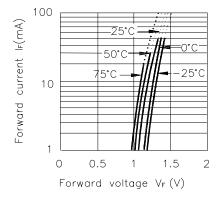


Fig. 3 Collector Current vs.
Collector-emitter Voltage

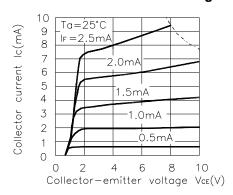
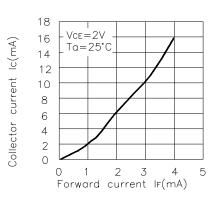


Fig. 2 Collector Current vs. Forward Current



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Fig. 4 Collector Current vs.

Ambient Temperature

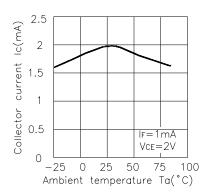


Fig.5 Collector-emitter Saturation Voltage vs.Ambient Temperature

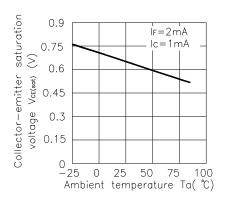


Fig.6 Relative Collector Current vs. Shield Distance (1)

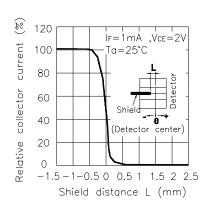


Fig.7 Relative Collector Current vs. Shield Distance (2)

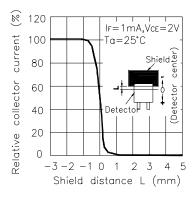
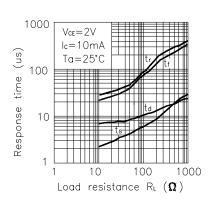
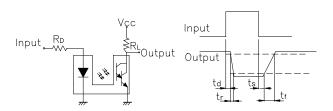


Fig.8 Response Time vs Load Resistance



Test Circuit for Response Time

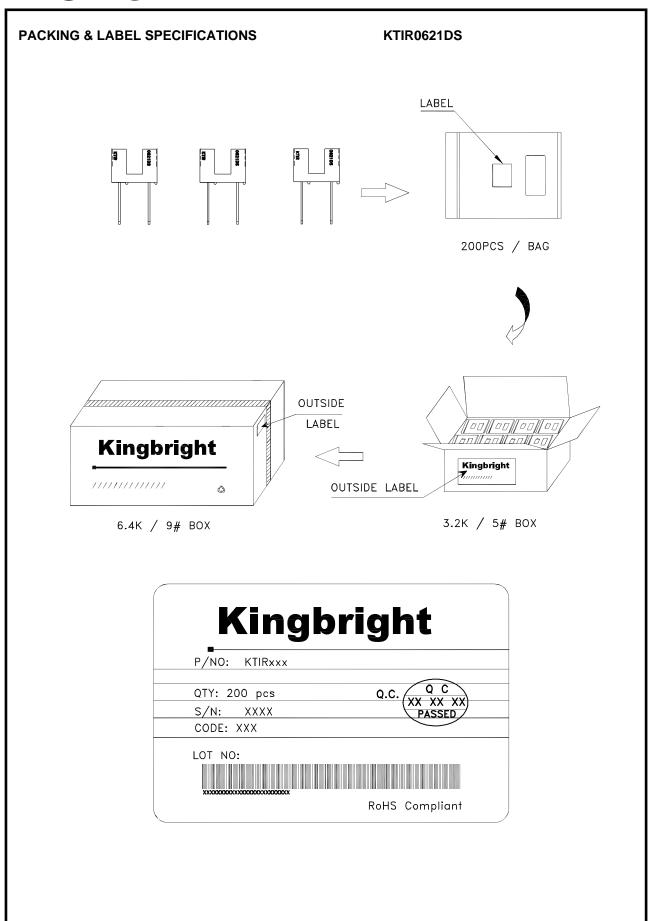


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