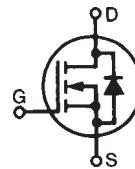


# TrenchMV™ Power MOSFET

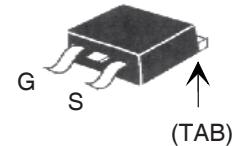
**IXTA180N10T**  
**IXTP180N10T**

N-Channel Enhancement Mode  
Avalanche Rated

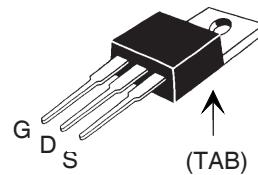


**V<sub>DSS</sub>** = 100V  
**I<sub>D25</sub>** = 180A  
**R<sub>DS(on)</sub>** ≤ 6.4mΩ

**TO-263 (IXTA)**



**TO-220 (IXTP)**



G = Gate      D = Drain  
S = Source      TAB = Drain

Symbol	Test Conditions	Maximum Ratings	
V <sub>DSS</sub>	T <sub>J</sub> = 25°C to 175°C	100	V
V <sub>DGR</sub>	T <sub>J</sub> = 25°C to 175°C, R <sub>GS</sub> = 1MΩ	100	V
V <sub>GSM</sub>	Transient	± 30	V
I <sub>D25</sub>	T <sub>C</sub> = 25°C	180	A
I <sub>LRMS</sub>	Lead Current limit, RMS	75	A
I <sub>DM</sub>	T <sub>C</sub> = 25°C, pulse width limited by T <sub>JM</sub>	450	A
I <sub>AR</sub>	T <sub>C</sub> = 25°C	25	A
E <sub>AS</sub>	T <sub>C</sub> = 25°C	750	mJ
P <sub>D</sub>	T <sub>C</sub> = 25°C	480	W
T <sub>J</sub>		-55 ... +175	°C
T <sub>JM</sub>		175	°C
T <sub>stg</sub>		-55 ... +175	°C
T <sub>L</sub>	1.6mm (0.062in.) from case for 10s	300	°C
T <sub>SOLD</sub>	Plastic body for 10 seconds	260	°C
M <sub>d</sub>	Mounting torque (TO-220)	1.13/10	Nm/lb.in
Weight	TO-263	2.5	g
	TO-220	3.0	g

Symbol	Test Conditions	Characteristic Values		
	(T <sub>J</sub> = 25°C unless otherwise specified)	Min.	Typ.	Max.
BV <sub>DSS</sub>	V <sub>GS</sub> = 0V, I <sub>D</sub> = 250μA	100		V
V <sub>GS(th)</sub>	V <sub>DS</sub> = V <sub>GS</sub> , I <sub>D</sub> = 250μA	2.5		V
I <sub>GSS</sub>	V <sub>GS</sub> = ± 20V, V <sub>DS</sub> = 0V		± 100 nA	
I <sub>DSS</sub>	V <sub>DS</sub> = V <sub>DSS</sub> V <sub>GS</sub> = 0V		5 μA	
	T <sub>J</sub> = 150°C		100 μA	
R <sub>DS(on)</sub>	V <sub>GS</sub> = 10V, I <sub>D</sub> = 25A, Notes 1, 2	5.7	6.4 mΩ	

## Features

- Ultra-low On Resistance
- Unclamped Inductive Switching (UIS) rated
- Low package inductance
  - easy to drive and to protect
- 175 °C Operating Temperature

## Advantages

- Easy to mount
- Space savings
- High power density

## Applications

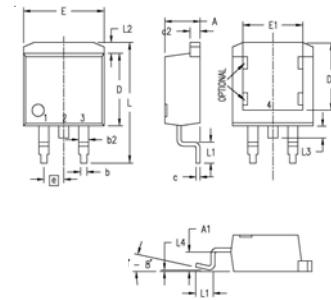
- Automotive
  - Motor Drives
  - 42V Power Bus
  - ABS Systems
- DC/DC Converters and Off-line UPS
- Primary Switch for 24V and 48V Systems
- Distributed Power Architectures and VRMs
- Electronic Valve Train Systems
- High Current Switching Applications
- High Voltage Synchronous Rectifier

Symbol	Test Conditions	Characteristic Values		
	( $T_J = 25^\circ C$ unless otherwise specified)	Min.	Typ.	Max.
$g_{fs}$	$V_{DS} = 10V, I_D = 60A$ , Note 1	70	110	S
$C_{iss}$		6900	pF	
$C_{oss}$	$V_{GS} = 0V, V_{DS} = 25V, f = 1MHz$	923	pF	
$C_{rss}$		162	pF	
$t_{d(on)}$		33	ns	
$t_r$		54	ns	
$t_{d(off)}$	$V_{GS} = 10V, V_{DS} = 0.5 \cdot V_{DSS}, I_D = 25A$	42	ns	
$t_f$	$R_G = 3.3\Omega$ (External)	31	ns	
$Q_{g(on)}$		151	nC	
$Q_{gs}$	$V_{GS} = 10V, V_{DS} = 0.5 \cdot V_{DSS}, I_D = 25A$	39	nC	
$Q_{gd}$		45	nC	
$R_{thJC}$			0.31 $^\circ C/W$	
$R_{thCH}$	TO-220	0.50		$^\circ C/W$

**Source-Drain Diode**

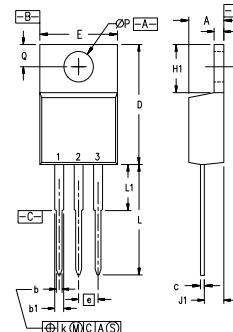
Symbol	Test Conditions	Characteristic Values		
	( $T_J = 25^\circ C$ unless otherwise specified)	Min.	Typ.	Max.
$I_s$	$V_{GS} = 0V$		180	A
$I_{SM}$	Repetitive, pulse width limited by $T_{JM}$		450	A
$V_{SD}$	$I_F = 25A, V_{GS} = 0V$ , Note 1		0.95	V
$t_{rr}$		72	ns	
$I_{RM}$	$I_F = 90A, V_{GS} = 0V$	5.1	A	
$Q_{RM}$	$-di/dt = 100A/\mu s$	0.18	$\mu C$	
	$V_R = 0.5 \cdot V_{DSS}$			

- Notes:
1. Pulse test,  $t \leq 300\mu s$ , duty cycle,  $d \leq 2\%$ .
  2. On through-hole packages,  $R_{DS(on)}$  Kelvin test contact location must be 5mm or less from the package body.

**TO-263 (IXTA) Outline**

Pins: 1 - Gate      2 - Drain  
3 - Source      4, TAB - Drain

Dim.	Millimeter		Inches	
	Min.	Max.	Min.	Max.
A	4.06	4.83	.160	.190
A1	2.03	2.79	.080	.110
b	0.51	0.99	.020	.039
b2	1.14	1.40	.045	.055
c	0.46	0.74	.018	.029
c2	1.14	1.40	.045	.055
D	8.64	9.65	.340	.380
D1	7.11	8.13	.280	.320
E	9.65	10.29	.380	.405
E1	6.86	8.13	.270	.320
e	2.54	BSC	.100	BSC
L	14.61	15.88	.575	.625
L1	2.29	2.79	.090	.110
L2	1.02	1.40	.040	.055
L3	1.27	1.78	.050	.070
L4	0	0.38	0	.015
R	0.46	0.74	.018	.029

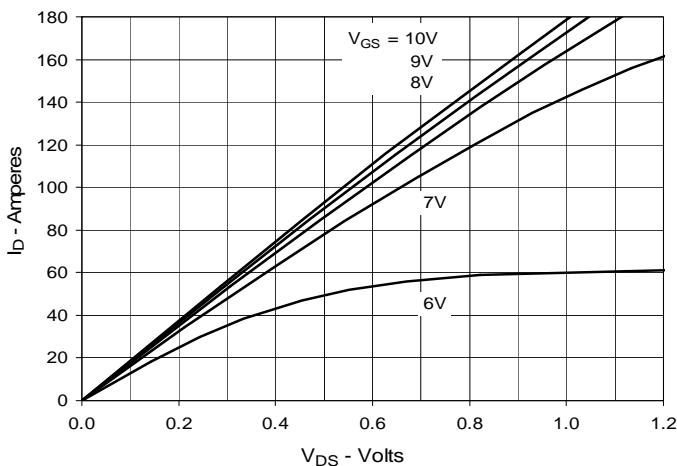
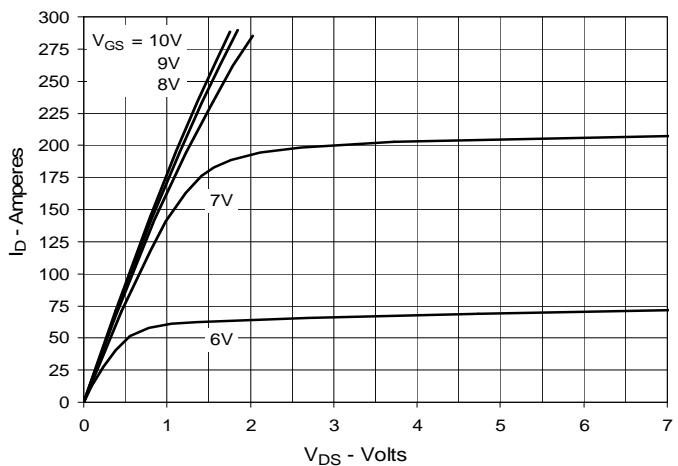
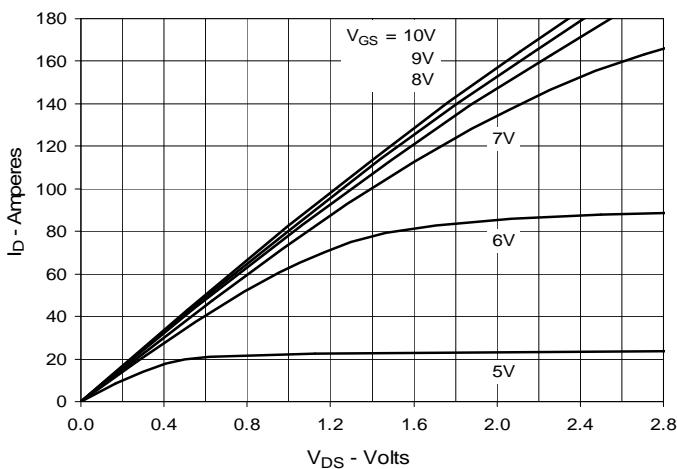
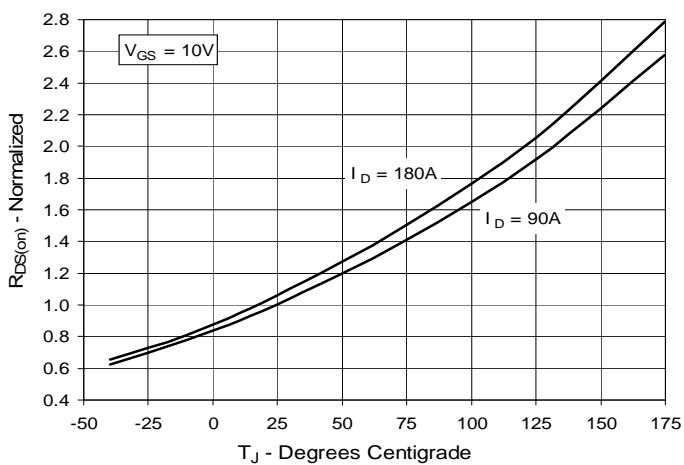
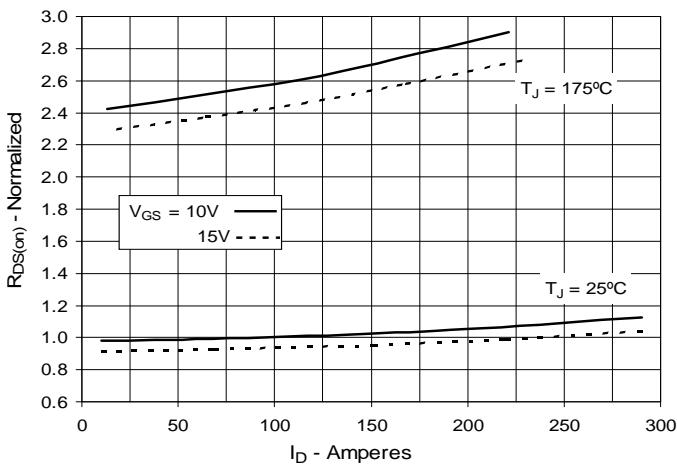
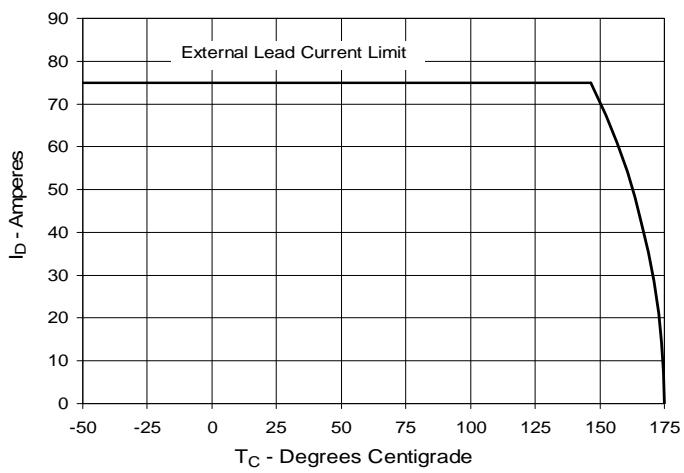
**TO-220 (IXTP) Outline**

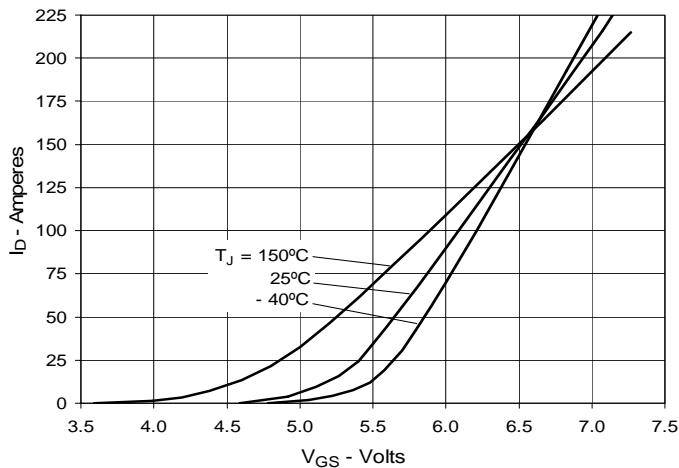
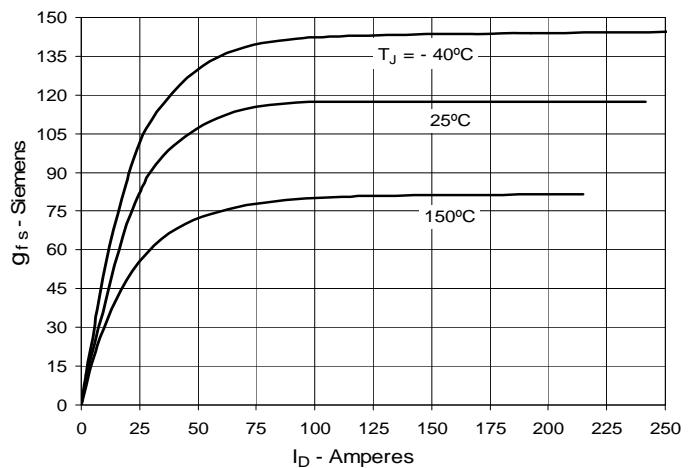
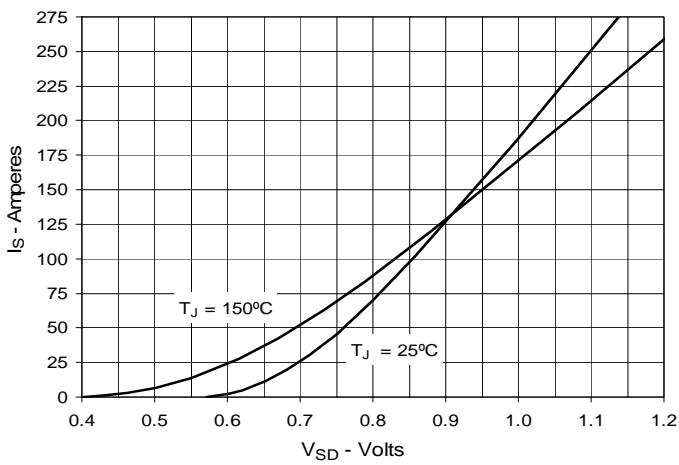
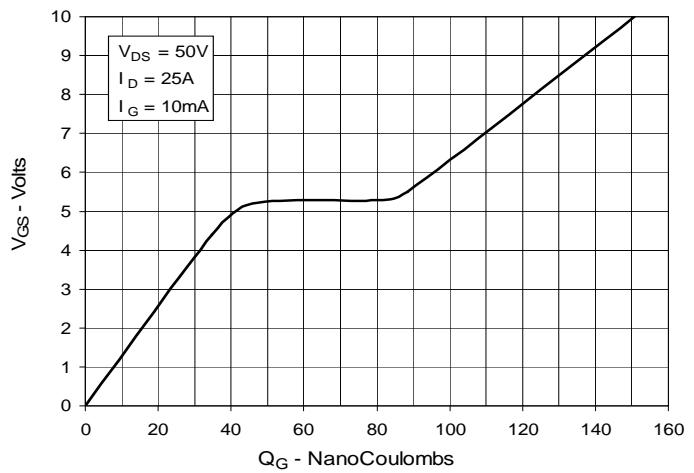
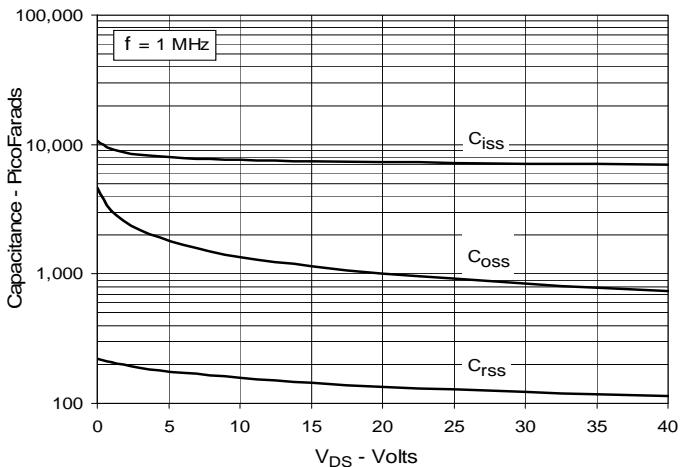
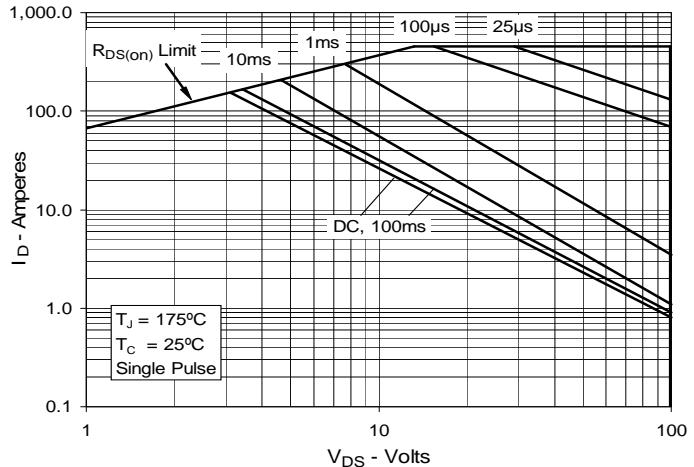
Pins: 1 - Gate      2 - Drain  
3 - Source      4, TAB - Drain

SYM	INCHES		MILLIMETERS	
	MIN	MAX	MIN	MAX
A	.170	.190	4.32	4.83
b	.025	.040	0.64	1.02
b1	.045	.065	1.15	1.65
c	.014	.022	0.35	0.56
D	.580	.630	14.73	16.00
E	.390	.420	9.91	10.66
e	.100	BSC	2.54	BSC
F	.045	.055	1.14	1.40
H1	.230	.270	5.85	6.85
J1	.090	.110	2.29	2.79
k	0	.015	0	0.38
L	.500	.550	12.70	13.97
L1	.110	.230	2.79	5.84
$\emptyset P$	.139	.161	3.53	4.08
Q	.100	.125	2.54	3.18

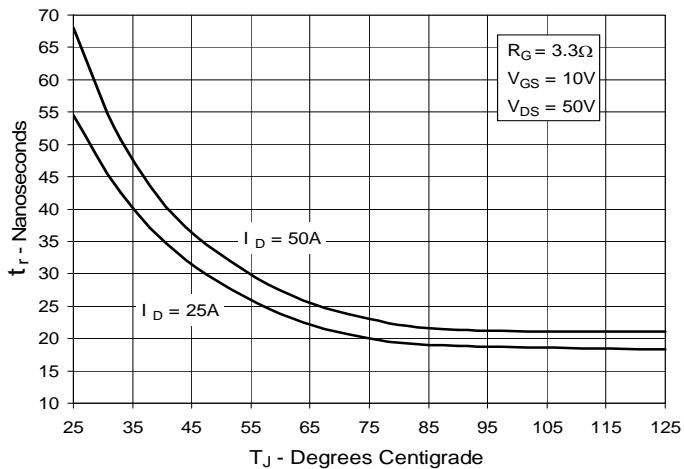
IXYS reserves the right to change limits, test conditions, and dimensions.

IXYS MOSFETs and IGBTs are covered by one or more of the following U.S. patents: 4,835,592 4,931,844 5,049,961 5,237,481 6,162,665 6,404,065 B1 6,683,344 6,727,585 7,005,734 B2 7,157,338B2 4,850,072 5,017,508 5,063,307 5,381,025 6,259,123 B1 6,534,343 6,710,405 B2 6,759,692 7,063,975 B2 4,881,106 5,034,796 5,187,117 5,486,715 6,306,728 B1 6,583,505 6,710,463 6,771,478 B2 7,071,537

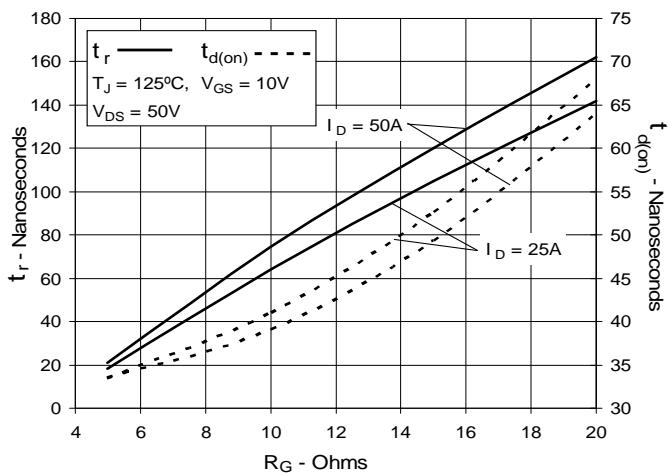
**Fig. 1. Output Characteristics  
@ 25°C****Fig. 2. Extended Output Characteristics  
@ 25°C****Fig. 3. Output Characteristics  
@ 150°C****Fig. 4.  $R_{DS(on)}$  Normalized to  $I_D = 90A$  Value  
vs. Junction Temperature****Fig. 5.  $R_{DS(on)}$  Normalized to  $I_D = 90A$  Value  
vs. Drain Current****Fig. 6. Drain Current vs. Case Temperature**

**Fig. 7. Input Admittance**

**Fig. 8. Transconductance**

**Fig. 9. Forward Voltage Drop of Intrinsic Diode**

**Fig. 10. Gate Charge**

**Fig. 11. Capacitance**

**Fig. 12. Forward-Bias Safe Operating Area**


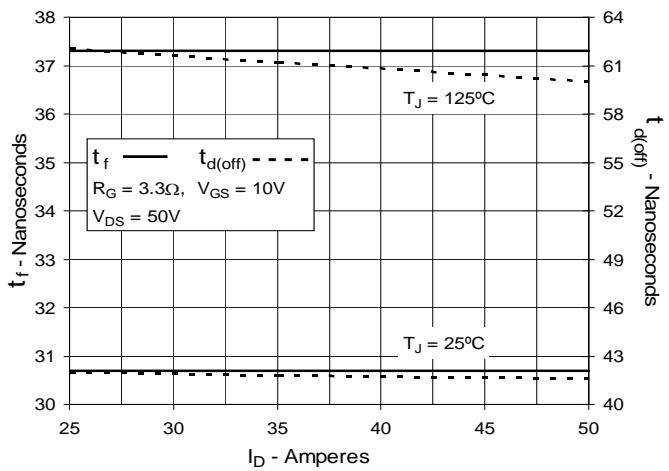
**Fig. 13. Resistive Turn-on**  
**Rise Time vs. Junction Temperature**



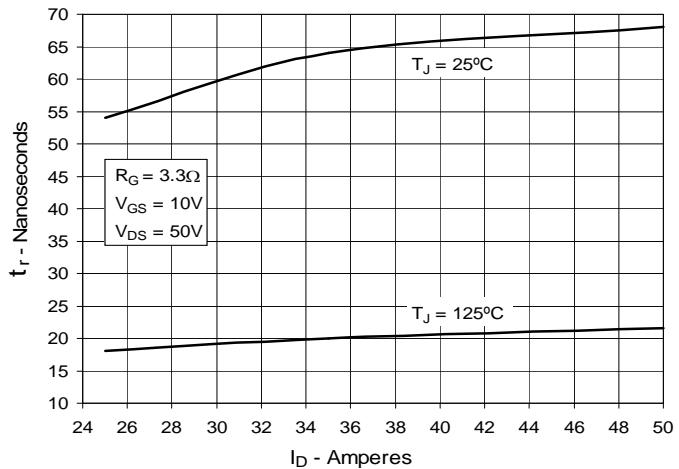
**Fig. 15. Resistive Turn-on**  
**Switching Times vs. Gate Resistance**



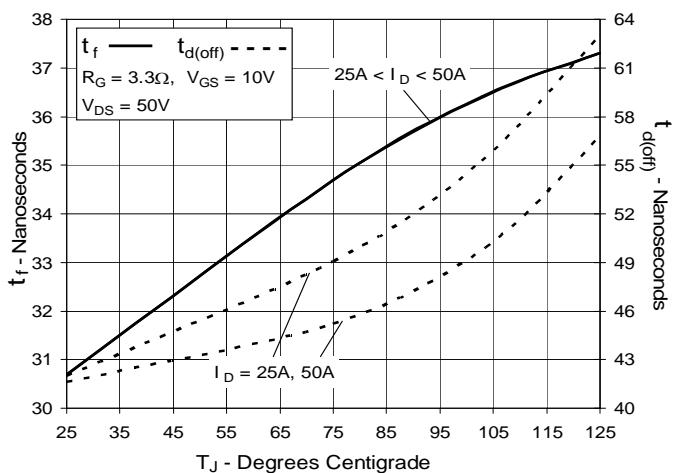
**Fig. 17. Resistive Turn-off**  
**Switching Times vs. Drain Current**



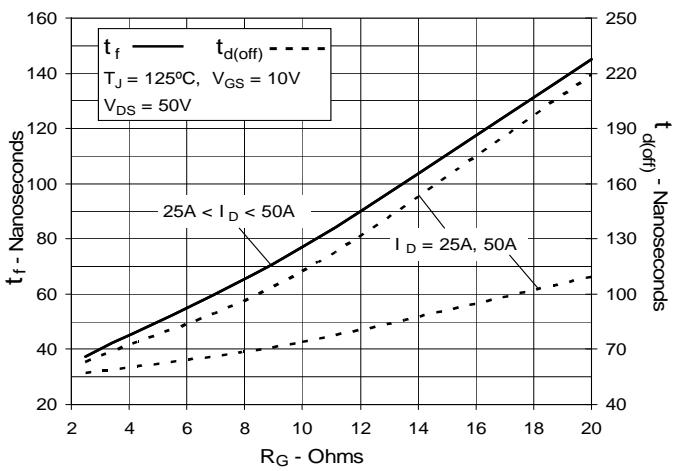
**Fig. 14. Resistive Turn-on**  
**Rise Time vs. Drain Current**

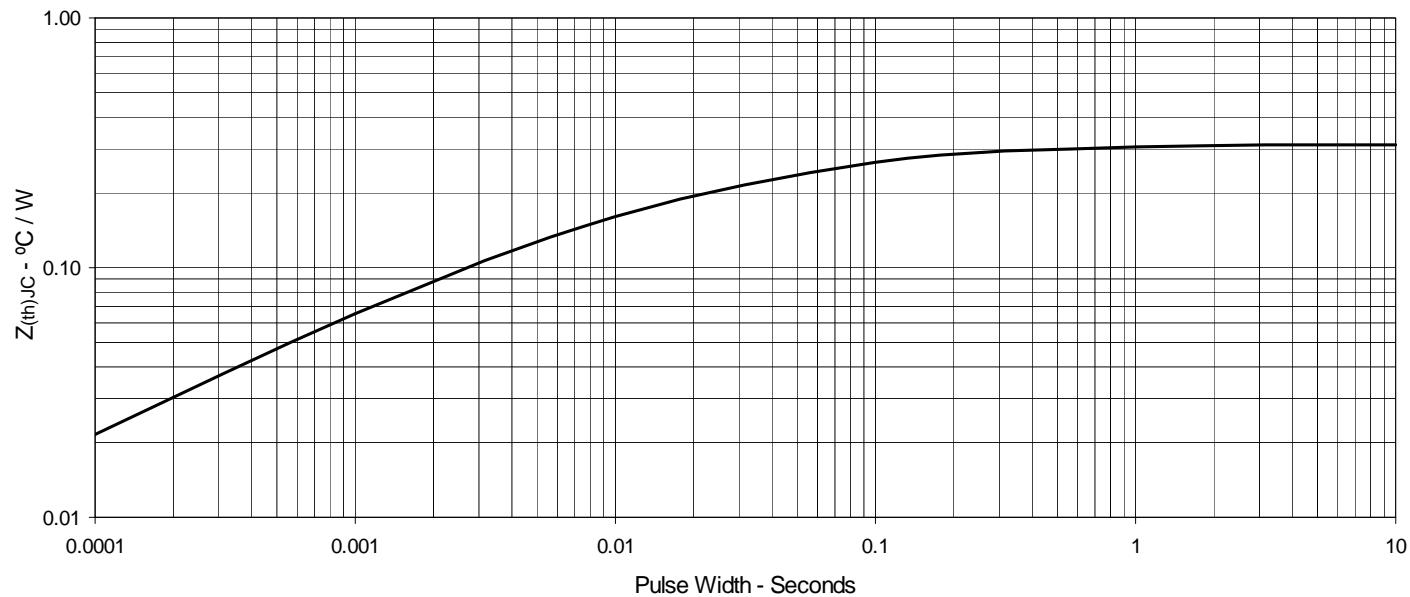


**Fig. 16. Resistive Turn-off**  
**Switching Times vs. Junction Temperature**



**Fig. 18. Resistive Turn-off**  
**Switching Times vs. Gate Resistance**



**Fig. 19. Maximum Transient Thermal Impedance**

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