## CRYSTAL OSCILLATOR (SPXO) OUTPUT : CMOS <br> NEW <br> SG5032CAN / CBN /CCN SG7050CAN / CBN / CCN

\author{

- Frequency range <br> -Supply voltage <br> -Function <br> - Output <br> : 1 MHz to 170 MHz (Fundamental mode) <br> : 1.8 V to 5.0 V <br> : Standby( $\overline{\text { ST }})$...SGxxxxCAN / CBN <br> : Output enable(OE) $\cdots$ SGxxxxCCN <br> : CMOS
}


Product Number (please contact us) SG5032CAN: X1G004451xxxx00 SG5032CBN: X1G004461xxxx00 SG5032CCN: X1G004471xxxx00 SG7050CAN: X1G004481xxxx00 SG7050CBN: X1G004491xxxx00 SG7050CCN: X1G004501xxxx00

SG5032CAN/CBN/CCN
$(5.0 \times 3.2 \times 1.1 \mathrm{~mm}$ )
Actual size

## Specifications (characteristics)

| Item | Symbol | Specifications |  |  | Conditions / Remarks |
| :---: | :---: | :---: | :---: | :---: | :---: |
|  |  | SG5032CAN SG7050CAN | $\begin{aligned} & \text { SG5032CBN } \\ & \text { SG7050CBN } \end{aligned}$ | $\begin{aligned} & \text { SG5032CCN } \\ & \text { SG7050CCN } \end{aligned}$ |  |
| Output frequency range | fo | 1 MHz to 75 MHz | 80 MHz to 170 MHz | 2.5 MHz to 50 MHz | Please contact us about available frequencies. |
| Supply voltage | Vcc | $\mathrm{T}: 1.6 \mathrm{~V}$ to 3.6 V |  | $\mathrm{H}: 4.5 \mathrm{~V}$ to 5.5 V |  |
| Storage temperature | T_stg | $-40^{\circ} \mathrm{C}$ to $+125^{\circ} \mathrm{C}$ |  |  | Storage as single product. |
| Operating temperature | T_use |  |  |  |  |
|  |  | H: $-40^{\circ} \mathrm{C}$ to $+105^{\circ} \mathrm{C}$ G: $-40^{\circ} \mathrm{C}$ to $+85^{\circ} \mathrm{C}$ |  |  |  |
| Frequency tolerance | f_tol | $\mathrm{J}: \pm 50 \times 10^{-6}$ |  |  | $-40^{\circ} \mathrm{C}$ to $+85^{\circ} \mathrm{C}$ |
|  |  | $\mathrm{L}: \pm 100 \times 10^{-6}$ | - | - | $-40^{\circ} \mathrm{C}$ to $+105^{\circ} \mathrm{C}$ |
| Current consumption | Icc | 3.0 mA Max. | 11 mA Max. | 20 mA Max. | No load condition Maximum frequency. |
| Stand-by current | I_std | $2.7 \mu \mathrm{~A}$ Max. | $10 \mu \mathrm{~A}$ Max. | - | $\overline{\text { ST }}=$ GND |
| Disable current | I_dis |  |  | 10 mA Max. | OE=GND |
| Symmetry | SYM | $45 \% \text { to } 55 \%$ |  | $40 \%$ to $60 \%$ | $50 \%$ Vcc level, L_CMOS $\leq 15 \mathrm{pF}$ |
| Output voltage | VOH | Vcc-0.4 Min. |  |  |  |
|  | VoL | 0.4 V Max. |  |  |  |
| Output load condition (CMOS) | L_CMOS | 15 pF Max. |  | 50 pF Max. |  |
| Input voltage | VIH | 80 \% Vcc Min. |  |  | $\overline{\text { st }}$, OE terminal |
|  | VIL | 20 \% Vcc Max. |  |  |  |
| Rise time / Fall time | tr/ tf | 4 ns Max. | 3 ns Max. | 5 ns Max. | $20 \%$ Vcc to $80 \%$ Vcc level, L_CMOS $=15 \mathrm{pF}$ |
| Start-up time | t_str | 3 ms Max. | 5 ms Max . |  | $\mathrm{t}=0$ at $90 \% \mathrm{Vcc}+85^{\circ} \mathrm{C},\left(+105^{\circ} \mathrm{C}\right)$ |
| Frequency aging | f_aging | $\pm 3 \times 10^{-6} /$ year Max. | $\pm 5 \times 10^{-6} /$ year Max. |  | $+25^{\circ} \mathrm{C}$, First year. |

Product Name
(Standard form)

SG5032 C AN $25.000000 \mathrm{MHz} \mathrm{I} \underline{\mathrm{J}} \underline{\operatorname{G}} \underline{A} \quad$ (©(๑) JH is not available)
(1) (2) (3) (4)(6)(7)
(1)Model (2)Output (C:CMOS) (3)Frequency (4)Supply voltage (5Frequency tolerance
© 0 Operating temperature range © Internal identification code

| (4)Supply voltage |  | (9Frequency tolerance |  | ©Operating temperature range |  |
| :---: | :---: | :---: | :---: | :---: | :---: |
| T | 1.6 to 3.6 V | J | $\pm 50 \times 10^{-6}$ | G | -40 to $+85^{\circ} \mathrm{C}$ |
| H | 4.5 to 5.5 V | L | $\pm 100 \times 10^{-6}$ | H | -40 to $+105^{\circ} \mathrm{C}$ |



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