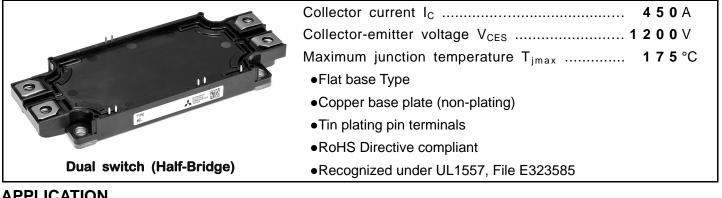


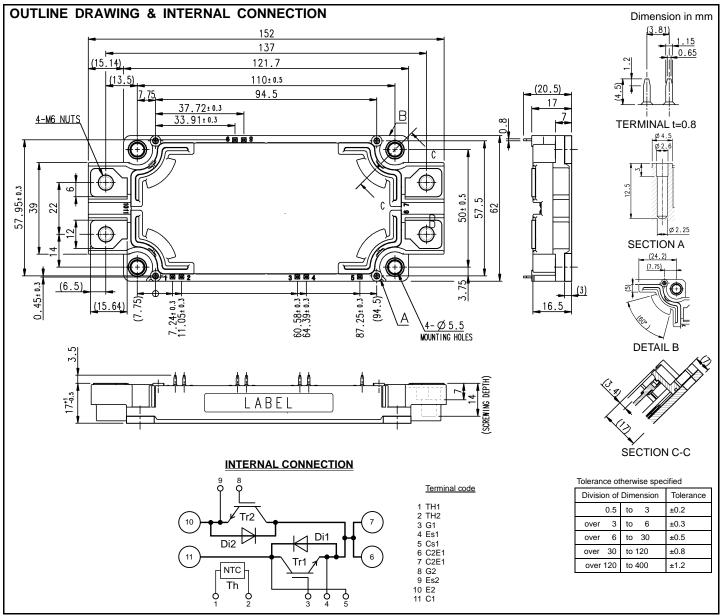
# < IGBT MODULES > CM450DX-24S1

**HIGH POWER SWITCHING USE INSULATED TYPE** 



# **APPLICATION**

AC Motor Control, Motion/Servo Control, Power supply, etc.



# MAXIMUM RATINGS (T\_j=25 °C, unless otherwise specified) INVERTER PART IGBT/DIODE

Symbol	Item	Conditions	Rating	Unit
V <sub>CES</sub>	Collector-emitter voltage	G-E short-circuited	1200	V
$V_{\text{GES}}$	Gate-emitter voltage	C-E short-circuited	± 20	V
Ic	Collector ourrent	DC, T <sub>C</sub> =107 °C (Note2, 4)	450	^
I <sub>CRM</sub>	Collector current	Pulse, Repetitive (Note3)	900	— A
P <sub>tot</sub>	Total power dissipation	T <sub>C</sub> =25 °C (Note2, 4)	2775	W
IE (Note1)		DC (Note2)	450	^
I <sub>ERM</sub> (Note1)	<ul> <li>Emitter current</li> </ul>	Pulse, Repetitive (Note3)	900	— A

## MODULE

Symbol	Item	Conditions	Rating	Unit
Visol	Isolation voltage	Terminals to base plate, RMS, f=60 Hz, AC 1 min	4000	V
T <sub>jmax</sub>	Maximum junction temperature	Instantaneous event (overload)	175	°C
T <sub>Cmax</sub>	Maximum case temperature	(Note4)	125	C
Tjop	Operating junction temperature	Continuous operation (under switching)	-40 ~ +150	°C
T <sub>stg</sub>	Storage temperature	-	-40 ~ +125	

# ELECTRICAL CHARACTERISTICS (T $_{\rm j}$ =25 °C, unless otherwise specified) INVERTER PART IGBT/DIODE

Symbol	ltem	Conditions			Limits		Unit
Symbol	itern	Conditions		Min.	Тур.	Max.	Unit
ICES	Collector-emitter cut-off current	V <sub>CE</sub> =V <sub>CES</sub> , G-E short-circuited		I	-	1.0	mA
I <sub>GES</sub>	Gate-emitter leakage current	$V_{GE}=V_{GES}$ , C-E short-circuited	V <sub>GE</sub> =V <sub>GES</sub> , C-E short-circuited		-	0.5	μA
V <sub>GE(th)</sub>	Gate-emitter threshold voltage	I <sub>C</sub> =45 mA, V <sub>CE</sub> =10 V		5.4	6.0	6.6	V
		I <sub>C</sub> =450 A, V <sub>GE</sub> =15 V,	T <sub>j</sub> =25 °C	-	1.80	2.25	
V <sub>CEsat</sub> (Terminal)		Refer to the figure of test circuit	T <sub>j</sub> =125 °C	-	2.00	-	V
(Terminar)	Collector omitter acturation valtage	(Note5)	T <sub>j</sub> =150 °C	-	2.05	-	
	Collector-emitter saturation voltage	I <sub>C</sub> =450 A,	T <sub>j</sub> =25 °C	-	1.70	2.15	
V <sub>CEsat</sub> (Chip)		V <sub>GE</sub> =15 V,	T <sub>j</sub> =125 °C	-	1.90	-	V
(Chip)		(Note5)	T <sub>j</sub> =150 °C	-	1.95	-	
Cies	Input capacitance			-	-	45	
C <sub>oes</sub>	Output capacitance	V <sub>CE</sub> =10 V, G-E short-circuited		-	-	9.0	nF
Cres	Reverse transfer capacitance			-	-	0.75	
Q <sub>G</sub>	Gate charge	$V_{cc}$ =600 V, I <sub>c</sub> =450 A, V <sub>GE</sub> =15 V		-	945	-	nC
t <sub>d(on)</sub>	Turn-on delay time	- V <sub>CC</sub> =600 V, I <sub>C</sub> =450 A, V <sub>GE</sub> =±15 V,		-	-	800	
tr	Rise time			-	-	200	1
t <sub>d(off)</sub>	Turn-off delay time			-	-	600	ns
t <sub>f</sub>	Fall time	- R <sub>G</sub> =0 Ω, Inductive load		-	-	300	
(Noto1)		I <sub>E</sub> =450 A, G-E short-circuited,	T <sub>j</sub> =25 ℃	-	2.60	3.40	
V <sub>EC</sub> (Note1)		Refer to the figure of test circuit	T <sub>j</sub> =125 °C	-	2.16	-	V
(Terminal)			T <sub>j</sub> =150 °C	-	2.10	-	
(Noto1)	Emitter-collector voltage	I <sub>E</sub> =450 A,	T <sub>j</sub> =25 ℃	-	2.50	3.30	
V <sub>EC</sub> (Note1)		G-E short-circuited,	T <sub>j</sub> =125 °C	-	2.06	-	V
(Chip)		(Note5)	T <sub>j</sub> =150 °C	-	2.00	-	
t <sub>rr</sub> <sup>(Note1)</sup>	Reverse recovery time	V <sub>cc</sub> =600 V, I <sub>E</sub> =450 A, V <sub>GE</sub> =±15 V,		-	-	300	ns
Q <sub>rr</sub> (Note1)	Reverse recovery charge	$R_{G}=0 \Omega$ , Inductive load		-	12	-	μC
Eon	Turn-on switching energy per pulse	$V_{CC}$ =600 V, $I_{C}$ = $I_{E}$ =450 A,		-	35.8	-	
E <sub>off</sub>	Turn-off switching energy per pulse	V <sub>GE</sub> =±15 V, R <sub>G</sub> =0 Ω, T <sub>j</sub> =150 °C,		-	52.4	-	mJ
Err (Note1)	Reverse recovery energy per pulse	Inductive load		-	27.9	-	mJ
R <sub>CC'+EE'</sub>	Internal lead resistance	Main terminals-chip, per switch, T <sub>C</sub> =25 °C (Note2)		-	-	0.7	mΩ
r <sub>g</sub>	Internal gate resistance	Per switch		-	4.3	-	Ω

## ELECTRICAL CHARACTERISTICS (cont.; T<sub>j</sub>=25 °C, unless otherwise specified) NTC THERMISTOR PART

Symbol	ltem	Conditions	Limits	Limits			Unit
	liem	Conditions	Min.	Тур.	Max.	Offic	
R <sub>25</sub>	Zero-power resistance	T <sub>C</sub> =25 °C <sup>(Note4)</sup>	4.85	5.00	5.15	kΩ	
ΔR/R	Deviation of resistance	R <sub>100</sub> =493 Ω, T <sub>C</sub> =100 °C <sup>(Note4)</sup>	-7.3	-	+7.8	%	
B <sub>(25/50)</sub>	B-constant	Approximate by equation (Note6)	-	3375	-	К	
P <sub>25</sub>	Power dissipation	T <sub>c</sub> =25 °C <sup>(Note4)</sup>	-	-	10	mW	

### THERMAL RESISTANCE CHARACTERISTICS

Symbol	ltom	Conditions	Limits		Unit	
	Item	Conditions	Min.	Тур.	Max.	Unit
R <sub>th(j-c)Q</sub>	Thermal resistance	Junction to case, per Inverter IGBT (Note4)	-	-	54	K/kW
R <sub>th(j-c)D</sub>		Junction to case, per Inverter DIODE (Note4)	-	-	86	r/kvv
R <sub>th(c-s)</sub>		Case to heat sink, per 1 module,		45		
	Contact thermal resistance	Thermal grease applied (Note4, 7)	-	15	-	K/kW

## MECHANICAL CHARACTERISTICS

Symbol	ltom	Conditions		Limits			Unit	
	Item	Conditions		Min.	Тур.	Max.	Unit	
Mt	Mounting torque	Main terminals	M 6 screw	3.5	4.0	4.5	N∙m	
Ms	Mounting torque	Mounting to heat sink	M 5 screw	2.5	3.0	3.5	N∙m	
m	mass	-		-	350	-	g	
d	Creepage distance	Terminal to terminal		17	-	-	mm	
ds		Terminal to base plate		18.5	-	-		
d	Clearance	Terminal to terminal		10	-	-		
da	Clearance	Terminal to base plate		16.3	-	-	mm	
ec	Flatness of base plate	On the centerline X, Y (Note8)		±0	-	+100	μm	

Note1. Represent ratings and characteristics of the anti-parallel, emitter-collector free wheeling diode (DIODE)

2. Junction temperature ( $T_j$ ) should not increase beyond  $T_{jmax}$  rating.

3. Pulse width and repetition rate should be such that the device junction temperature ( $T_j$ ) dose not exceed  $T_{jmax}$  rating.

4. Case temperature ( $T_c$ ) and heat sink temperature ( $T_s$ ) are defined on the each surface (mounting side) of base plate and heat sink just under the chips. Refer to the figure of chip location.

5. Pulse width and repetition rate should be such as to cause negligible temperature rise.

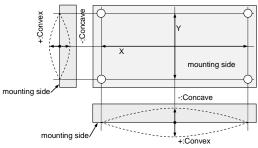
6. 
$$B_{(25/50)} = \ln(\frac{R_{25}}{R_{50}}) / (\frac{1}{T_{25}} - \frac{1}{T_{50}})$$
,

 $R_{25}\!\!:$  resistance at absolute temperature  $T_{25}$  [K];  $T_{25}\!\!=\!\!25$  [°C]+273.15=298.15 [K]

 $R_{50}$ : resistance at absolute temperature  $T_{50}$  [K];  $T_{50}$ =50 [°C]+273.15=323.15 [K]

7. Typical value is measured by using thermally conductive grease of  $\lambda$ =0.9 W/(m·K).

8. The base plate (mounting side) flatness measurement points (X, Y) are as follows of the following figure.



9. Use the following screws when mounting the printed circuit board (PCB) on the stand offs. " $\phi$ 2.6×10 or  $\phi$ 2.6×12 B1 tapping screw"

The length of the screw depends on thickness (t1.6~t2.0) of the PCB.

## **RECOMMENDED OPERATING CONDITIONS**

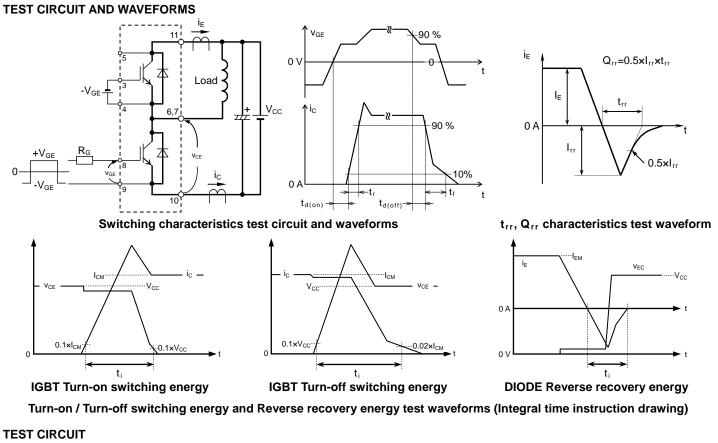
Symbol	ltom	Conditions	Limits			Unit
	Item	Conditions	Min.	Min. Typ. Max.	Max.	Unit
V <sub>cc</sub>	(DC) Supply voltage	Applied across C1-E2 terminals	-	600	850	V
V <sub>GEon</sub>	Gate (-emitter drive) voltage	Applied across G1-Es1/G2-Es2 terminals	13.5	15.0	16.5	V
R <sub>G</sub>	External gate resistance	Per switch	0	-	10	Ω

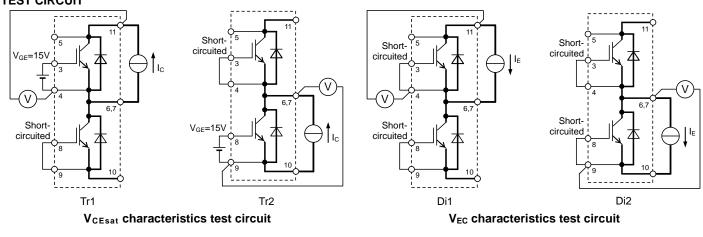
## CHIP LOCATION (Top view)

(152)(110)6 🖂 🖂 8 + 35.0-U -Tr2<sup>\_\_</sup>Di2\_Tr2 Di2 - Tr2 - Di235.2 33.7 (50)101 34.0 ſ 19.6-'Tr1—Di1 Di1 Tr1--Tr1 19.6 Di1= 18.4 18.0 Th 11.0 ÷ Ċ ) 3 8 8 4 0Ф Φ0 Œ 4 ŕ ò ் ் ഹ LABEL SIDE 0 72 25 37 49 84 97

Tr1/Tr2: IGBT, Di1/Di2: DIODE, Th: NTC thermistor

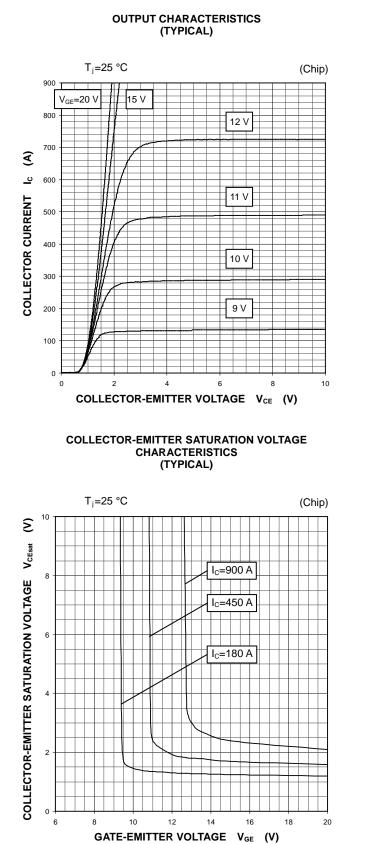
Dimension in mm, tolerance: ±1 mm



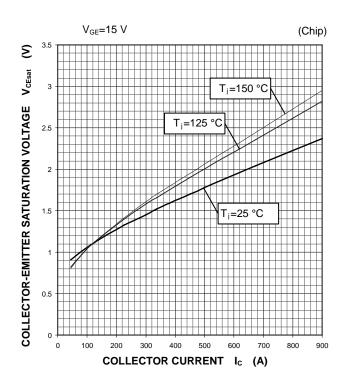


## PERFORMANCE CURVES

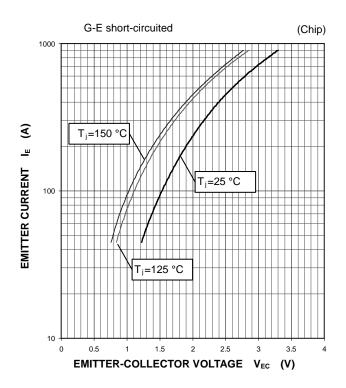
## **INVERTER PART**



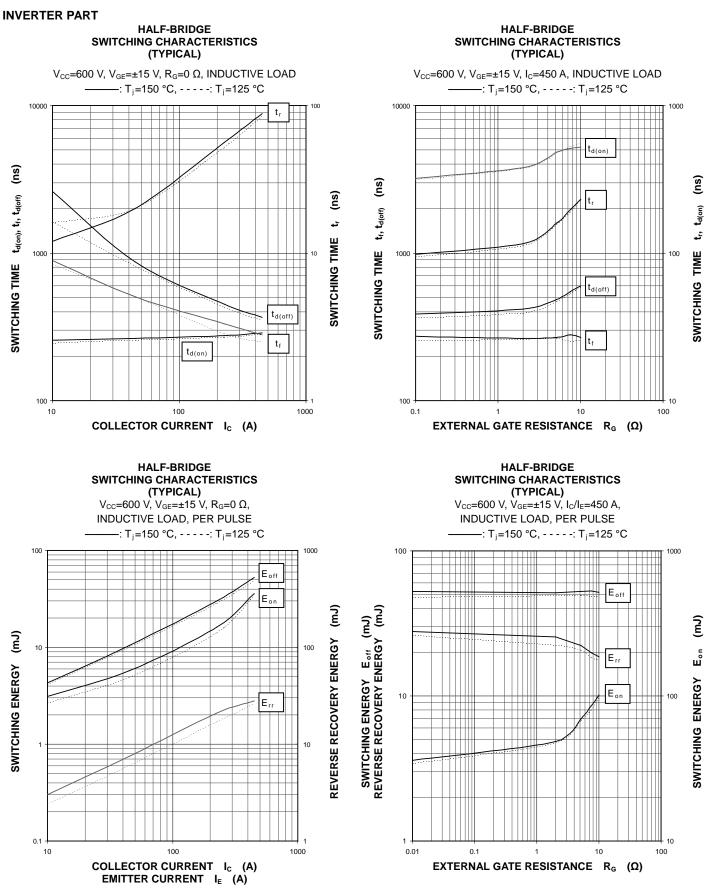
### COLLECTOR-EMITTER SATURATION VOLTAGE CHARACTERISTICS (TYPICAL)



#### FREE WHEELING DIODE FORWARD CHARACTERISTICS (TYPICAL)

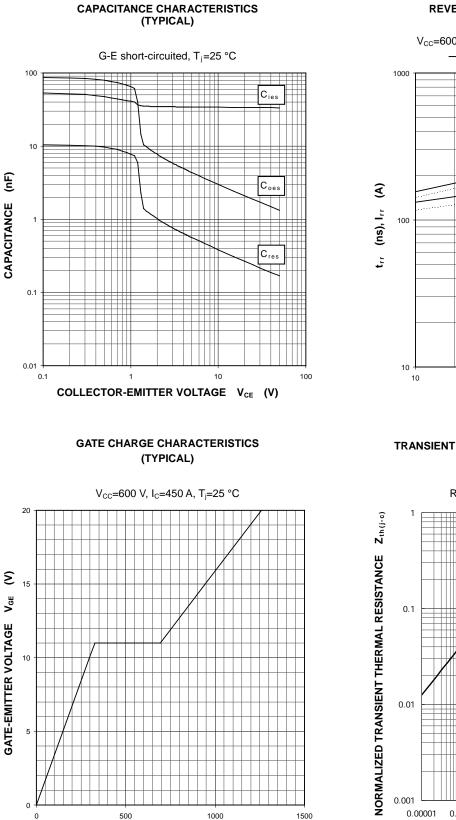


### PERFORMANCE CURVES

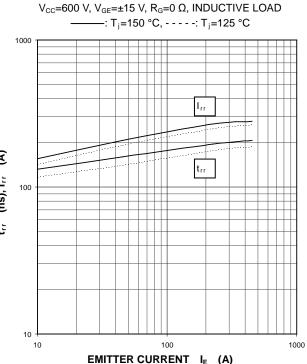


## PERFORMANCE CURVES

## **INVERTER PART**

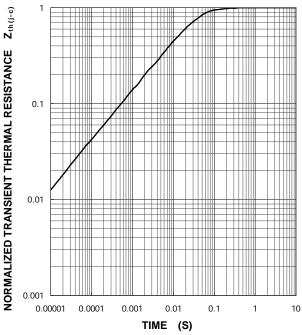


#### FREE WHEELING DIODE REVERSE RECOVERY CHARACTERISTICS (TYPICAL)



# TRANSIENT THERMAL IMPEDANCE CHARACTERISTICS (MAXIMUM)

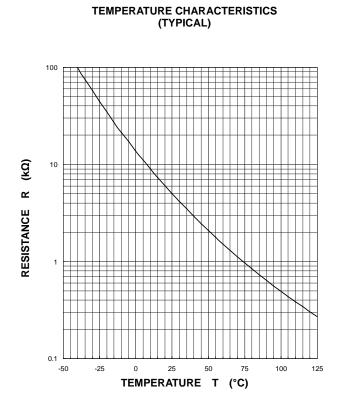
Single pulse, T\_c=25 °C R\_th(j-c)Q=54 K/kW, R\_th(j-c)D=86 K/kW



GATE CHARGE Q<sub>G</sub> (nC)

## PERFORMANCE CURVES

## NTC thermistor part



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