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April 1<sup>st</sup>, 2010 Renesas Electronics Corporation

Issued by: Renesas Electronics Corporation (http://www.renesas.com)

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# **2SJ526** Silicon P Channel MOS FET

REJ03G0876-0600 Rev.6.00 Jun 05, 2006

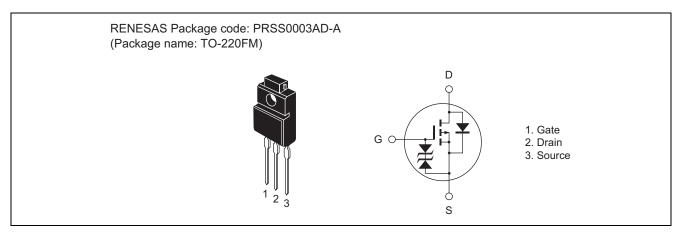
### Description

High speed power switching

### Features

- Low on-resistance
  - $R_{DS (on)} = 0.11 \ \Omega \ typ.$
- Low drive current
- 4 V gate drive devices
- High speed switching

### Outline





### Absolute Maximum Ratings

			(Ta = 25°C)
Item	Symbol	Value	Unit
Drain to source voltage	V <sub>DSS</sub>	-60	V
Gate to source voltage	V <sub>GSS</sub>	±20	V
Drain current	ID	-12	A
Drain peak current	I <sub>D (pulse)</sub> Note 1	-48	A
Body to drain diode reverse drain current	I <sub>DR</sub>	-12	A
Avalanche current	I <sub>AP</sub> Note 3	-12	A
Avalanche energy	E <sub>AR</sub> Note 3	12	mJ
Channel dissipation	Pch Note 2	25	W
Channel temperature	Tch	150	C°
Storage temperature	Tstg	-55 to +150	°C

Notes: 1.  $PW \le 10 \ \mu s$ , duty cycle  $\le 1\%$ 

2. Value at Tc = 25°C

3. Value at Tch =  $25^{\circ}$ C, Rg  $\geq 50 \Omega$ 

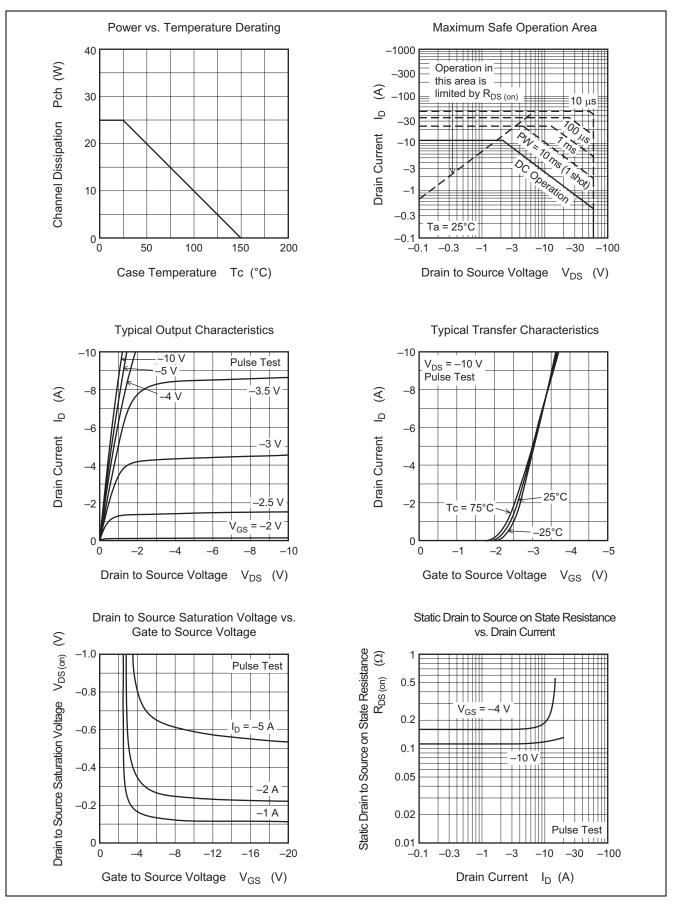
### **Electrical Characteristics**

						$(Ta = 25^{\circ}C)$
ltem	Symbol	Min	Тур	Max	Unit	Test Conditions
Drain to source breakdown voltage	V (BR) DSS	-60	_	_	V	$I_D = -10 \text{ mA}, V_{GS} = 0$
Gate to source breakdown voltage	V (BR) GSS	±20	_	_	V	$I_G = \pm 100 \ \mu A, \ V_{DS} = 0$
Zero gate voltage drain current	I <sub>DSS</sub>	—	—	-10	μA	$V_{DS} = -60 \text{ V}, \text{ V}_{GS} = 0$
Gate to source leak current	I <sub>GSS</sub>	—	—	±10	μA	$V_{GS} = \pm 16 \text{ V}, \text{ V}_{DS} = 0$
Gate to source cutoff voltage	V <sub>GS (off)</sub>	-1.0	—	-2.0	V	$I_D = -1 \text{ mA}, V_{DS} = -10 \text{ V}$
Static drain to source on state resistance	R <sub>DS (on)</sub>		0.11	0.15	Ω	$I_D = -6 \text{ A}, V_{GS} = -10 \text{ V}^{\text{Note 4}}$
	R <sub>DS (on)</sub>		0.16	0.23	Ω	$I_D = -6 \text{ A}, V_{GS} = -4 \text{ V}^{Note 4}$
Forward transfer admittance	y <sub>fs</sub>	5	8		S	$I_D = -6 \text{ A}, V_{DS} = -10 \text{ V}^{\text{Note 4}}$
Input capacitance	Ciss		580		pF	$V_{DS} = -10 \text{ V}$
Output capacitance	Coss		300		pF	$V_{GS} = 0$
Reverse transfer capacitance	Crss		85		pF	f = 1 MHz
Turn-on delay time	t <sub>d (on)</sub>		10		ns	$V_{GS} = -10 \text{ V}$
Rise time	tr		55		ns	I <sub>D</sub> =6 A
Turn-off delay time	t <sub>d (off)</sub>		85		ns	$R_L = 5 \Omega$
Fall time	t <sub>f</sub>		60		ns	
Body to drain diode forward voltage	V <sub>DF</sub>	—	-1.2	—	V	$I_F = -12 \text{ A}, V_{GS} = 0$
Body to drain diode reverse recovery time	t <sub>rr</sub>	—	60	—	ns	$I_F = -12 \text{ A}, V_{GS} = 0$
						di <sub>F</sub> /dt = 50 A/µs

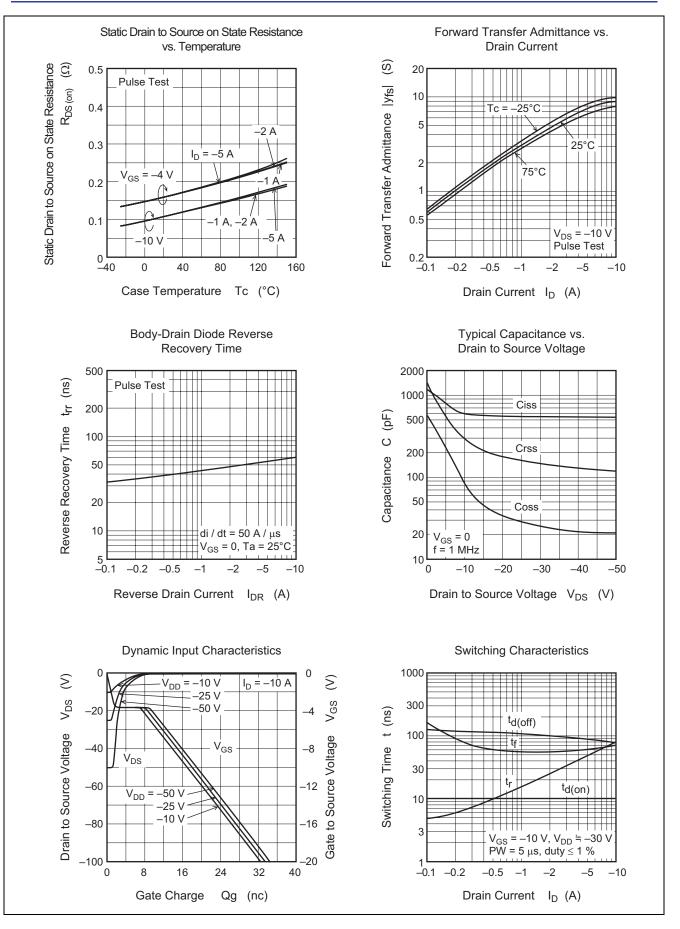
Note: 4. Pulse test



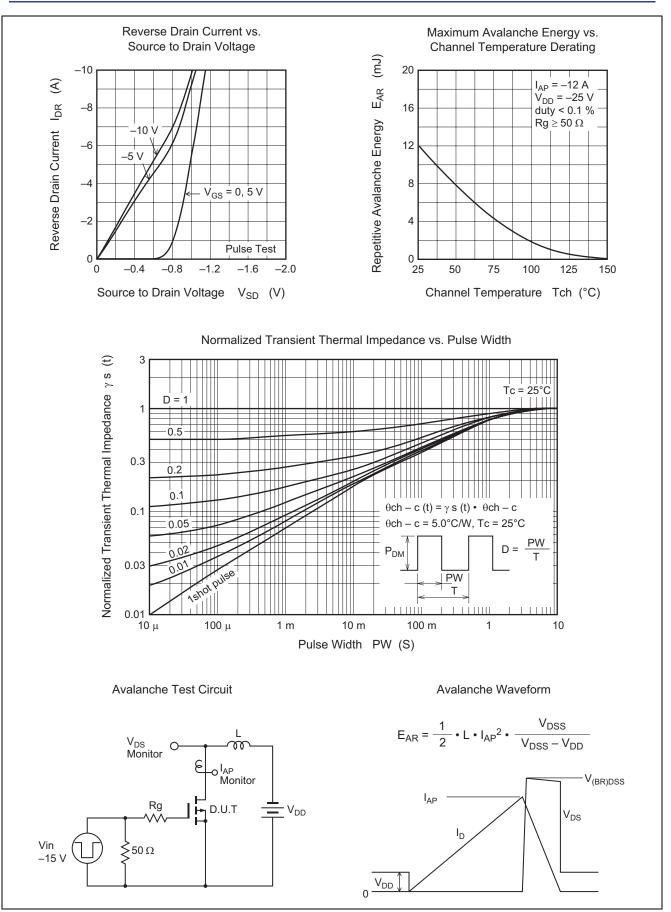
### **Main Characteristics**



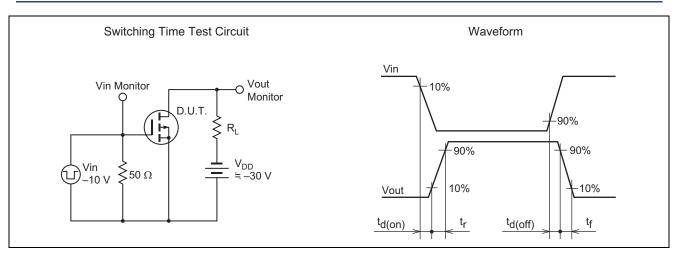






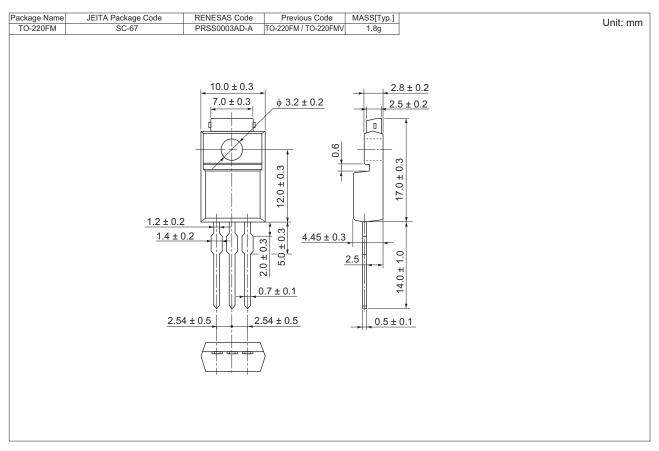








### **Package Dimensions**



### **Ordering Information**

Part Name	Quantity	Shipping Container
2SJ526-E	500 pcs	Box (Sack)

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