

2SK2788

Silicon N Channel MOS FET High Speed Power Switching

R07DS0511EJ0300 (Previous: REJ03G1033-0200)

> Rev.3.00 Jul 27, 2011

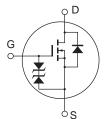
Features

- Low on-resistance
 - $R_{DS(on)} = 0.12 \Omega \text{ typ } (V_{GS} = 10 \text{ V}, I_D = 1 \text{ A})$
- Low drive current
- High speed switching
- 4 V gate drive devices.

Outline

RENESAS Package code: PLZZ0004CA-A (Package name: UPAK)





- 1. Gate
- 2. Drain
- 3. Source
- 4. Drain

Note: Marking is "VY"

Absolute Maximum Ratings

 $(Ta = 25^{\circ}C)$

Item	Symbol	Ratings	Unit
Drain to source voltage	V_{DSS}	60	V
Gate to source voltage	V_{GSS}	±20	V
Drain current	I _D	2	Α
Drain peak current	I _{D(pulse)} *1	4	А
Body to drain diode reverse drain current	I _{DR}	2	А
Channel dissipation	Pch*2	1	W
Channel temperature	Tch	150	°C
Storage temperature	Tstg	−55 to +150	°C

Notes: 1. PW \leq 100 μ s, duty cycle \leq 10 %

2. When using the alumina ceramic board (12.5 x 20 x 0.7 mm)

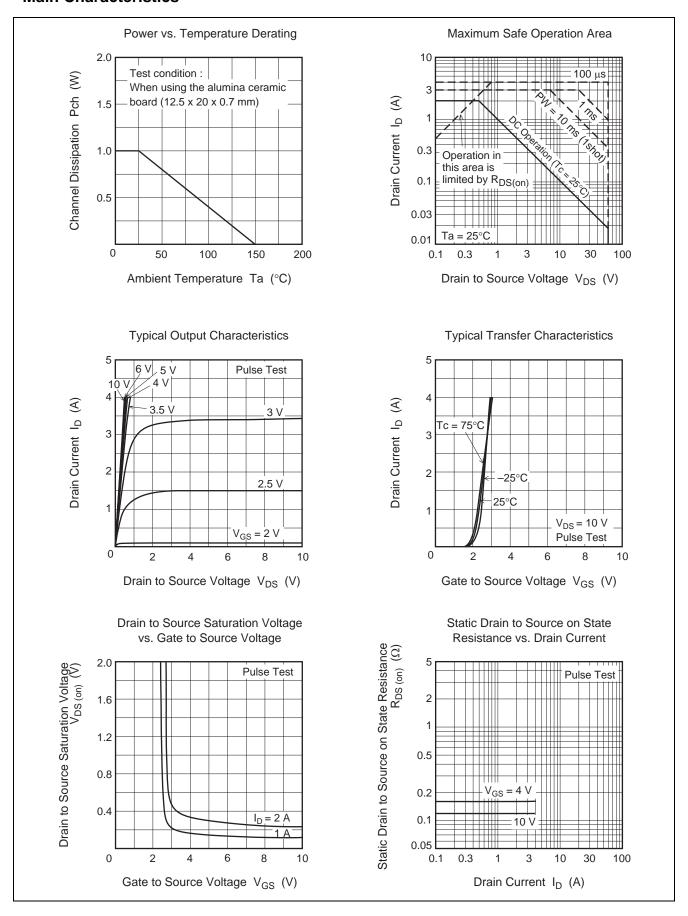
Electrical Characteristics

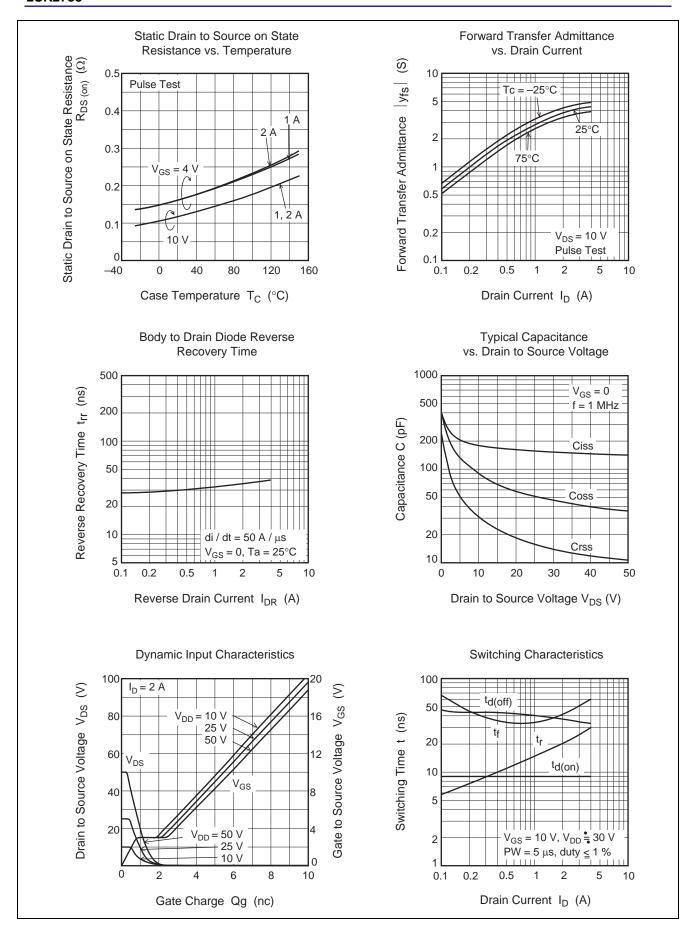
 $(Ta = 25^{\circ}C)$

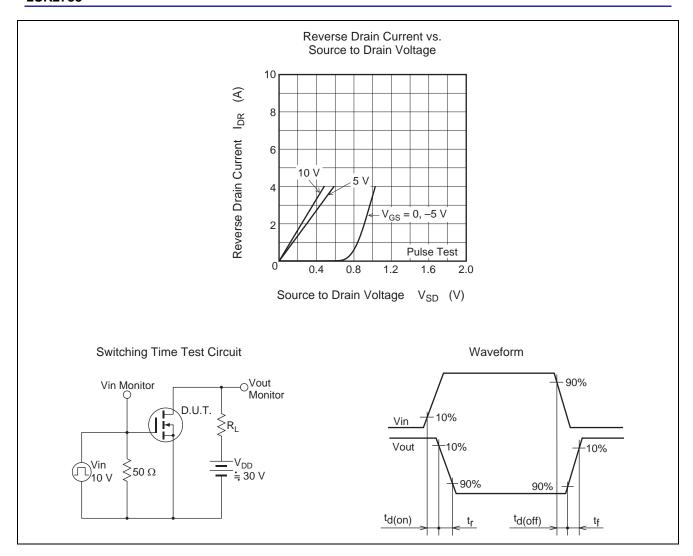
Item	Symbol	Min	Тур	Max	Unit	Test Conditions
Drain to source breakdown voltage	$V_{(BR)DSS}$	60	_	_	V	$I_D = 10 \text{ mA}, V_{GS} = 0$
Gate to source breakdown voltage	$V_{(BR)GSS}$	±20	_	_	V	$I_G = \pm 100 \ \mu A, \ V_{DS} = 0$
Zero gate voltage drain current	I _{DSS}	_	_	10	μΑ	$V_{DS} = 60 \text{ V}, V_{GS} = 0$
Gate to source leak current	I _{GSS}	_	_	±10	μΑ	$V_{GS} = \pm 16 \text{ V}, V_{DS} = 0$
Gate to source cutoff voltage	$V_{GS(off)}$	1.0	_	2.0	V	$I_D = 1 \text{ mA}, V_{DS} = 10 \text{ V}$
Static drain to source on state	R _{DS(on)}	_	0.12	0.16	Ω	$I_D = 1 \text{ A}, V_{GS} = 10 \text{ V}^{*3}$
resistance	R _{DS(on)}	_	0.16	0.25	Ω	$I_D = 1 A, V_{GS} = 4 V^{*3}$
Forward transfer admittance	y _{fs}	1.6	2.8	_	S	$I_D = 1 \text{ A}, V_{DS} = 10 \text{ V}^{*3}$
Input capacitance	Ciss	_	180	_	pF	$V_{DS} = 10 \text{ V}, V_{GS} = 0,$
Output capacitance	Coss	_	90	_	pF	f = 1 MHz
Reverse transfer capacitance	Crss	_	30	_	pF	
Turn-on delay time	t _{d(on)}	_	9	_	ns	$V_{GS} = 10 \text{ V}, I_D = 1 \text{ A},$
Rise time	t _r	_	15	_	ns	R _L = 30 Ω
Turn-off delay time	t _{d(off)}	_	40	_	ns	
Fall time	t _f	_	35	_	ns	
Body to drain diode forward voltage	V_{DF}	_	0.9	_	V	$I_D = 2 A, V_{GS} = 0$
Body to drain diode reverse	t _{rr}	_	35	_	ns	I _F = 2 A, V _{GS} = 0
recovery time						$di_F/dt = 50A/\mu s$

Notes: 3. Pulse test

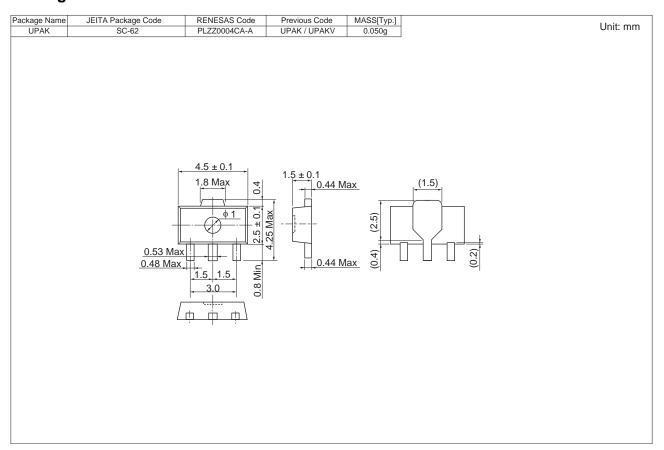
Main Characteristics







Package Dimensions



Ordering Information

Orderable Part Number	Quantity	Shipping Container
2SK2788VYTL-E	1000 pcs	Taping
2SK2788VYTR-E	1000 pcs	Taping

Note: For some grades, production may be terminated. Please contact the Renesas sales office to check the state of production before ordering the product.

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Renesas Electronics Canada Limited 1101 Nicholson Road, Newmarket, Ontario L3Y 9C3, Canada Tel: +1-905-898-5441, Fax: +1-905-898-3220

Renesas Electronics Europe Limited Dukes Meadow, Millboard Road, Bourne End, Buckinghamshire, SL8 5FH, U.K Tel: +444-1628-585-100, Fax: +444-1628-585-900 Renesas Electronics Europe GmbH

Arcadiastrasse 10, 40472 Düsseldorf, Germany Tel: +49-211-65030, Fax: +49-211-6503-1327

Renesas Electronics (China) Co., Ltd.
7th Floor, Quantum Plaza, No.27 ZhiChunLu Haidian District, Beijing 100083, P.R.China
Tel: +86-10-2353-1155, Fax: +86-10-8235-7679

Renesas Electronics Hong Kong Limited
Unit 1601-1613, 161F., Tower 2, Grand Century Place, 193 Prince Edward Road West, Mongkok, Kowloon, Hong Kong
Tel: +852-2868-9318, Fax: +852-2886-9022/9044

Renesas Electronics Taiwan Co., Ltd. 13F, No. 363, Fu Shing North Road, Taipei, Taiv Tel: +886-2-8175-9600, Fax: +886 2-8175-9670

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