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Renesas Electronics website: http://www.renesas.com

April 1st, 2010 Renesas Electronics Corporation

Issued by: Renesas Electronics Corporation (http://www.renesas.com)

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RENESAS

2SK2925(L),2SK2925(S)

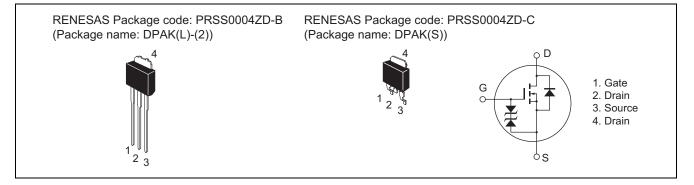
Silicon N Channel MOS FET High Speed Power Switching

> REJ03G1039-0500 (Previous: ADE-208-454B) Rev.5.00 Sep 07, 2005

Features

- Low on-resistance $R_{DS} = 0.060 \ \Omega$ typ.
- High speed switching
- 4 V gate drive device can be driven from 5 V source

Outline





Absolute Maximum Ratings

			$(Ta = 25^{\circ}C)$
Item	Symbol	Ratings	Unit
Drain to source voltage	V _{DSS}	60	V
Gate to source voltage	V _{GSS}	±20	V
Drain current	ID	10	А
Drain peak current	Note1 I _{D(pulse)}	40	А
Body-drain diode reverse drain current	I _{DR}	10	А
Avalanche current	I _{AP} Note3	10	А
Avalanche energy	EAR Note3	8.5	mJ
Channel dissipation	Pch Note2	20	W
Channel temperature	Tch	150	°C
Storage temperature	Tstg	-55 to +150	°C

Notes: 1. PW \leq 10 μ s, duty cycle \leq 1 %

2. Value at Tc = 25°C

3. Value at Tch = 25°C, Rg \ge 50 Ω

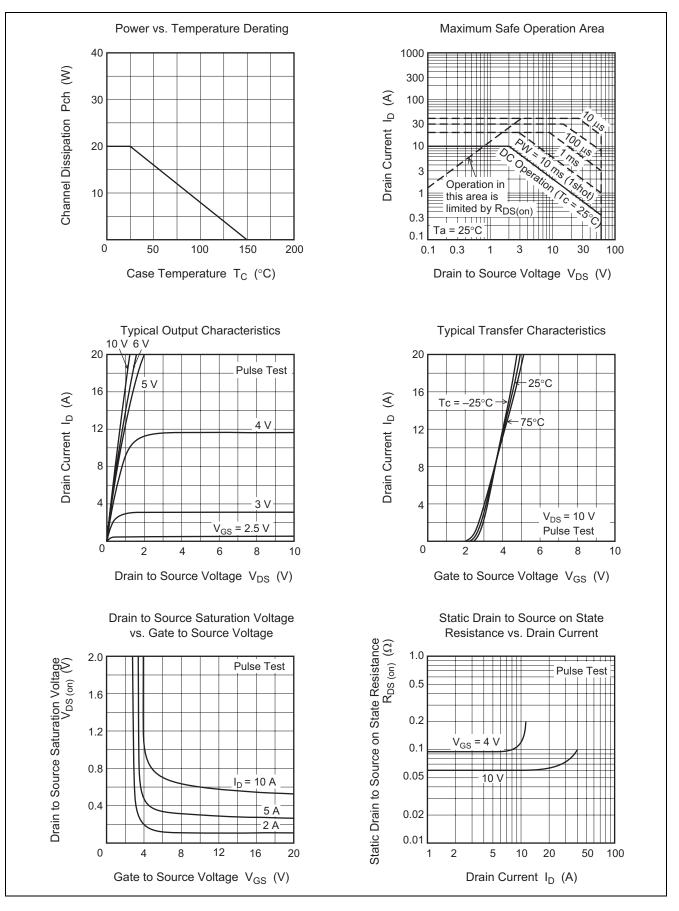
Electrical Characteristics

						(Ta = 25°C)
Item	Symbol	Min	Тур	Max	Unit	Test Conditions
Drain to source breakdown voltage	V _{(BR)DSS}	60	—	—	V	$I_D = 10 \text{ mA}, V_{GS} = 0$
Gate to source breakdown voltage	V _{(BR)GSS}	±20	—	—	V	$I_{G} = \pm 100 \ \mu A, \ V_{DS} = 0$
Gate to source leak current	I _{GSS}	_	—	±10	μA	$V_{GS} = \pm 16 \text{ V}, V_{DS} = 0$
Zero gate voltage drain current	I _{DSS}	_	—	10	μA	$V_{DS} = 60 \text{ V}, V_{GS} = 0$
Gate to source cutoff voltage	V _{GS(off)}	1.5	—	2.5	V	$I_D = 1 \text{ mA}, V_{DS} = 10 \text{ V}$
Static drain to source on state	R _{DS(on)}	_	0.060	0.080	Ω	$I_D = 5 \text{ A}, V_{GS} = 10 \text{ V}^{Note4}$
resistance	R _{DS(on)}	_	0.095	0.160	Ω	$I_D = 5 \text{ A}, V_{GS} = 4 \text{ V}^{Note4}$
Forward transfer admittance	y _{fs}	5	8		S	$I_D = 5 \text{ A}, V_{DS} = 10 \text{ V}^{\text{Note4}}$
Input capacitance	Ciss		350		pF	$V_{DS} = 10 \text{ V}, \text{ V}_{GS} = 0,$ f = 1 MHz
Output capacitance	Coss		190		pF	
Reverse transfer capacitance	Crss		70		pF	
Turn-on delay time	t _{d(on)}		10		ns	$I_{D} = 5 \text{ A}, \text{ V}_{GS} = 10 \text{ V},$ $R_{L} = 6 \Omega$
Rise time	tr		55		ns	
Turn-off delay time	t _{d(off)}		60		ns	
Fall time	t _f		70		ns	
Body-drain diode forward voltage	V _{DF}	_	0.9		V	$I_F = 10 \text{ A}, V_{GS} = 0$
Body–drain diode reverse recovery time	t _{rr}	_	50	—	ns	I _F = 10 A, V _{GS} = 0, diF/ dt =50A/μs

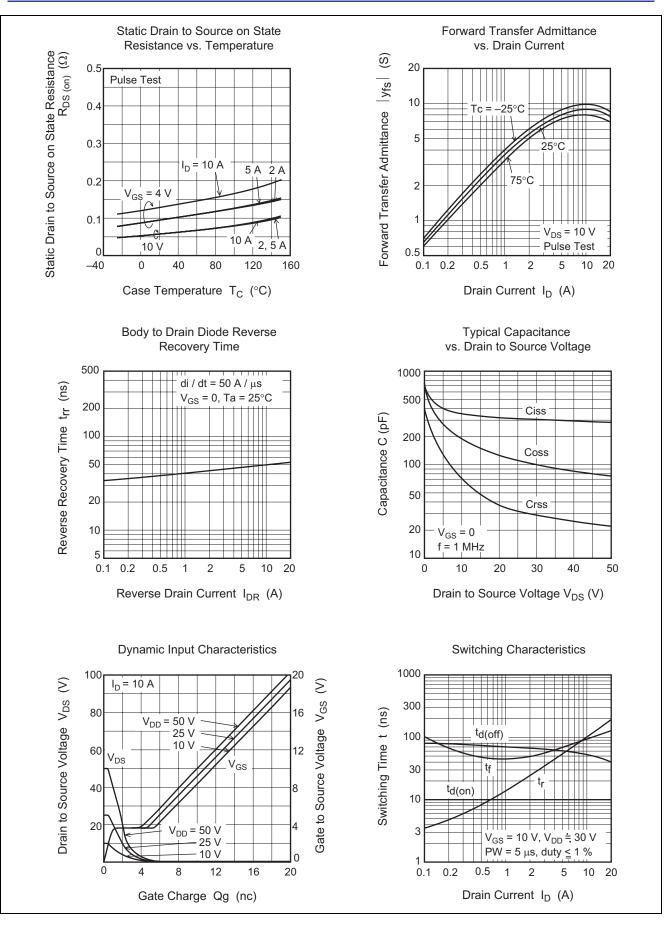
Note: 4. Pulse test



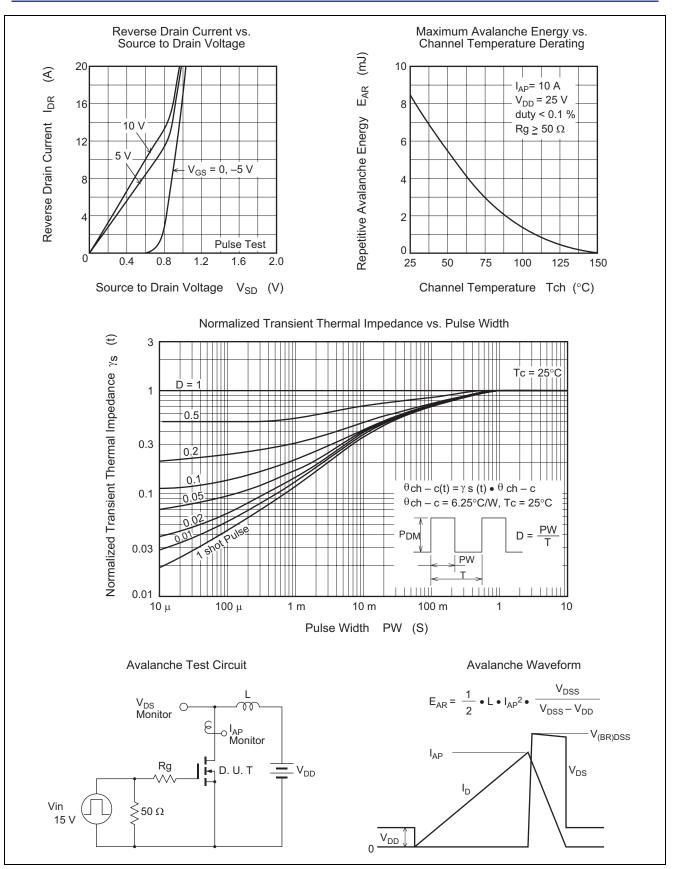
Main Characteristics



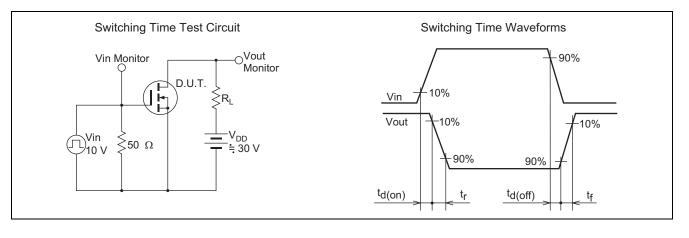






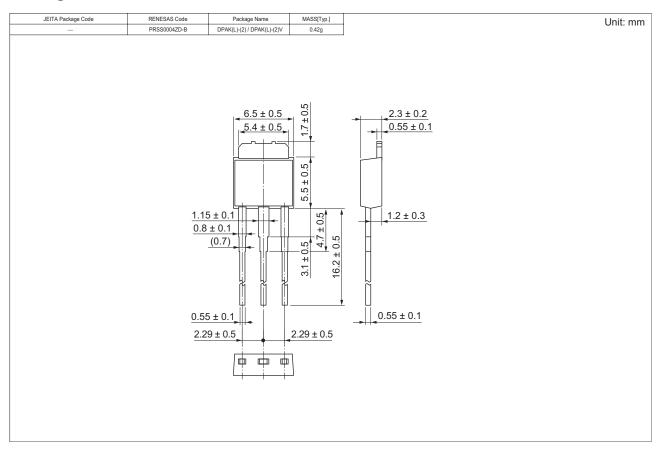


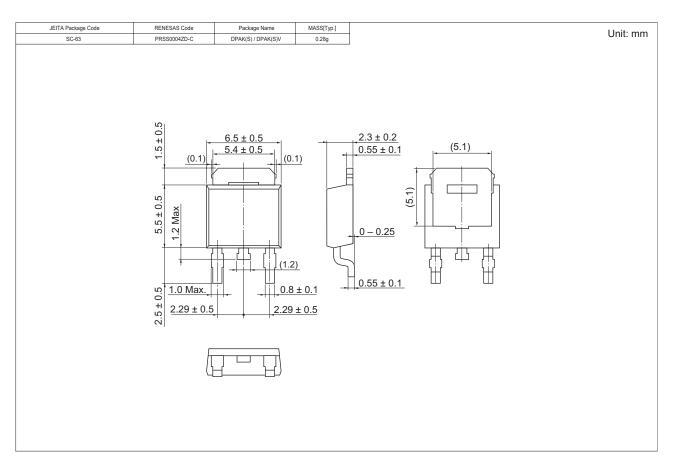






Package Dimensions







Ordering Information

Part Name	Quantity	Shipping Container
2SK2925L-E	3200 pcs	Box (Sack)
2SK292ST5L-E	3000 pcs	Taping

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