TOSHIBA Field Effect Transistor Silicon N-Channel MOS Type (U-MOS III)

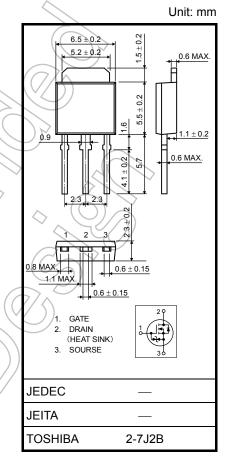
# 2SK4017

Chopper Regulator, DC-DC Converter and Motor Drive Applications

- 4-V gate drive
- Low drain-source ON-resistance:  $R_{DS (ON)} = 0.07 \Omega$  (typ.)
- High forward transfer admittance: |Y<sub>fs</sub>| = 6.0 S (typ.)
- Low leakage current:  $I_{DSS} = 100 \ \mu A \ (max) \ (V_{DS} = 60 \ V)$
- Enhancement mode:  $V_{th}$  = 1.3 to 2.5 V ( $V_{DS}$  = 10 V,  $I_D$  = 1 mA)

### Absolute Maximum Ratings (Ta = 25°C)

iatia			
Characteristic		Rating	Unit
	V <sub>DSS</sub>	60	V
<sub>GS</sub> = 20 kΩ)	V <sub>DGR</sub>	60	V
	V <sub>GSS</sub>	±20	V
DC (Note 1)	۱ <sub>D</sub>	5	А
Pulse (Note 1)	I <sub>DP</sub>	20	A
on (Tc = 25°C)	PD	20	< w
e energy (Note 2)	EAS	40.5	mJ
		5	A
energy (Note 3)	EAR	2	MJ
((	7/(T <sub>ch</sub>	150	°C
ange	Tstg	-55 to 150	°C
	Pulse (Note 1) on (Tc = 25°C) e energy	$\begin{array}{c c} GS = 20 \text{ k}\Omega \end{pmatrix} & V_{DGR} \\ \hline V_{GSS} \\ \hline DC & (Note 1) & I_D \\ \hline Pulse (Note 1) & I_{DP} \\ e \text{ energy} \\ (Note 2) & E_{AS} \\ \hline & I_{AR} \\ energy (Note 3) & E_{AR} \\ \hline & T_{ch} \end{array}$	$\begin{array}{c c c c c c c c c c c c c c c c c c c $



Weight: 0.36 g (typ.)

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings. Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc.).

# **Thermal Characteristics**

Characteristic	Symbol	Мах	Unit
Thermal resistance, channel to case	R <sub>th (ch-c)</sub>	6.25	°C / W
Thermal resistance, channel to ambient	R <sub>th (ch−a)</sub>	125	°C / W

Note 1: Ensure that the channel temperature does not exceed 150°C.

Note 2:  $V_{DD}$  = 25 V,  $T_{ch}$  = 25°C (initial), L = 2.2 mH,  $R_G$  = 25  $\Omega$ ,  $I_{AR}$  = 5 A

Note 3: Repetitive rating: pulse width limited by maximum channel temperature

This transistor is an electrostatic-sensitive device. Handle with care.

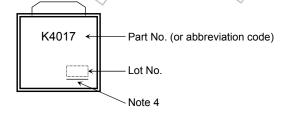
# Electrical Characteristics (Ta = 25°C)

Chara	cteristic	Symbol	Test Condition	Min	Тур.	Max	Unit
Gate leakage cu	urrent	I <sub>GSS</sub>	V <sub>GS</sub> = ±16 V, V <sub>DS</sub> = 0 V	_	—	±10	μA
Drain cutoff curr	rent	I <sub>DSS</sub>	V <sub>DS</sub> = 60 V, V <sub>GS</sub> = 0 V	_	_	100	μA
Drain-source breakdown voltage		V (BR) DSS	I <sub>D</sub> = 10 mA, V <sub>GS</sub> = 0 V	60	_	_	V
		V (BR) DSX	I <sub>D</sub> = 10mA, V <sub>GS</sub> = -20V	35	1	_	V
Gate threshold	voltage	V <sub>th</sub>	V <sub>DS</sub> = 10 V, I <sub>D</sub> = 1 mA	1.3	))	2.5	V
Drain-source ON-resistance		Pro (o) II	V <sub>GS</sub> = 4 V, I <sub>D</sub> = 2.5 A	77	0.09	0.15	Ω
		R <sub>DS (ON)</sub>	V <sub>GS</sub> = 10 V, I <sub>D</sub> = 2.5 A	H	0.07	0.10	
Forward transfe	r admittance	Y <sub>fs</sub>	V <sub>DS</sub> = 10 V, I <sub>D</sub> = 2.5 A	3.0	6.0	_	S
Input capacitance		C <sub>iss</sub>		_	730	_	
Reverse transfe	r capacitance	C <sub>rss</sub>	V <sub>DS</sub> = 10 V, V <sub>GS</sub> = 0 V, f = 1 MHz	_	60	7	pF
Output capacita	nce	C <sub>oss</sub>		_	95	$\geq$	
Switching time	Rise time	tr			10		
	Turn-on time	t <sub>on</sub>			20	_	ns
	Fall time	t <sub>f</sub>	<sup>1</sup>	$\mathbb{R}$	4		113
	Turn-off time	t <sub>off</sub>	Duty≤1%, t <sub>w</sub> =10 µs	)_	35		
Total gate charg plus gate-drain)		Qg	$\bigcirc > \bigcirc > \bigcirc >$	_	15	_	_
Gate-source ch	arge	Qgs	V <sub>DD</sub> ≈48 V, V <sub>GS</sub> = 10 V, I <sub>D</sub> = 5 A	—	11	—	nC
Gate-drain ("Miller") charge		Q <sub>gd</sub>		_	4	_	

# Source-Drain Ratings and Characteristics (Ta = 25°C)

Characteristic	Symbol	Test Condition	Min	Тур.	Max	Unit
Continuous drain reverse current (Note 1)		-	_		5	А
Pulse drain reverse current (Note 1)	I <sub>DRP</sub>	-	_		20	А
Forward voltage (diode)	VDSF	I <sub>DR</sub> = 5 A, V <sub>GS</sub> = 0 V	_	-	-1.7	V
Reverse recovery time	trr	l <sub>DR</sub> = 5 A, V <sub>GS</sub> = 0 V, dl <sub>DR</sub> / dt = 50 A / μs		34		ns
Reverse recovery charge	Qrr		_	28	_	nC

### Marking

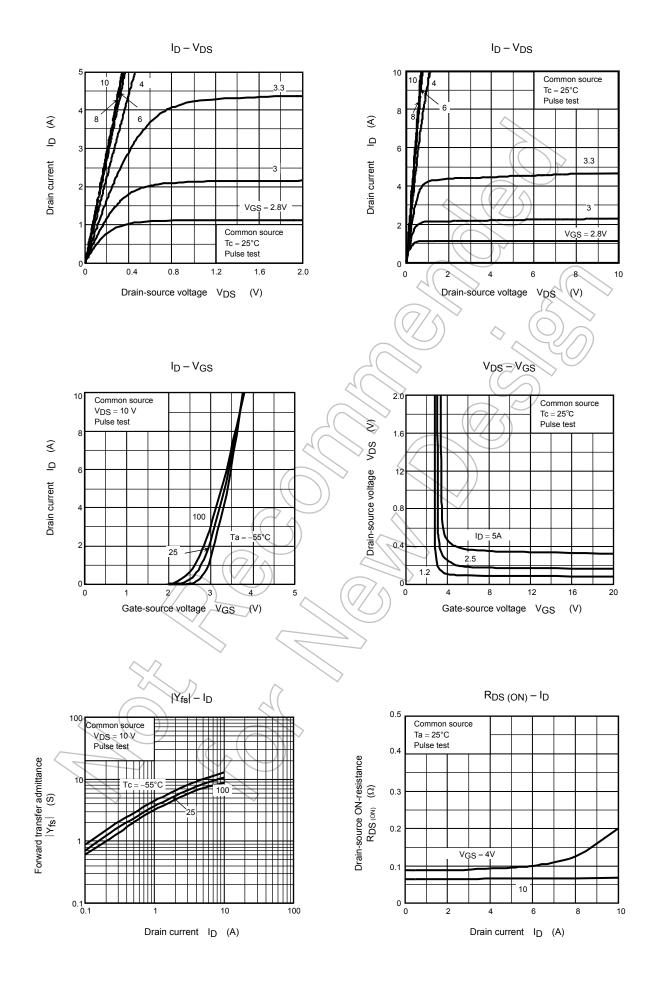


Note 4: A line under a Lot No. identifies the indication of product Labels.

#### [[G]]/RoHS COMPATIBLE or [[G]]/RoHS [[Pb]]

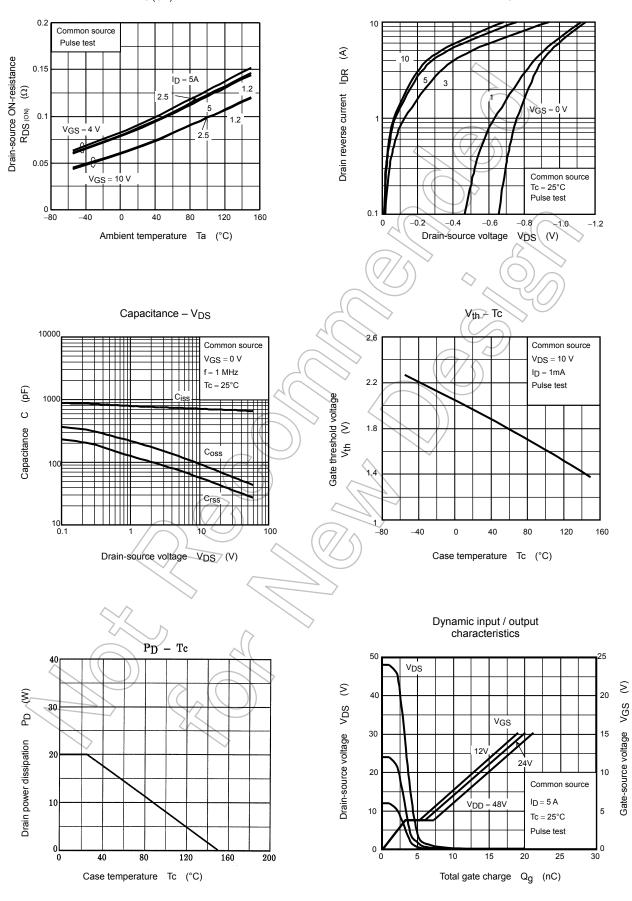
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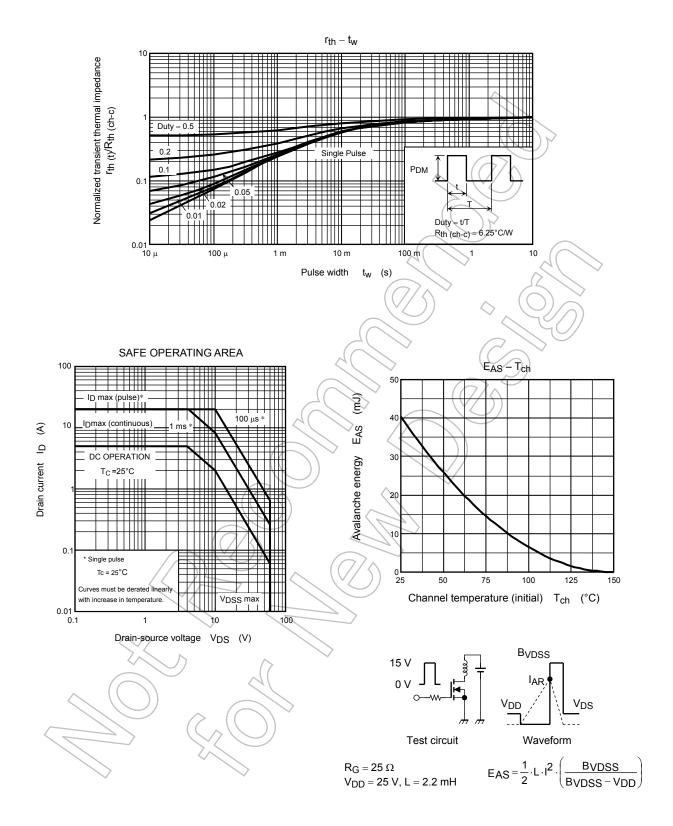
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R<sub>DS (ON)</sub> – Ta

 $I_{DR} - V_{DS}$ 





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