FOXElectronics



Farnell Order Code: 1641045 FOX MODEL: FXO-HC536R-40 FOX Part Number: 767-40-24 HCMOS, CERAMIC, SMD, OSCILLATOR

Specifications

Parameters			
Frequency	40.000 MHz		
Frequency Stability ¹	±25 PPM		
Temperature Range			
Operating (T _{OPR})	-40°C ~ +85°C		
Storage (T _{STG})	-55°C ~ +125⁰C		
Supply Voltage (V _{DD})	3.3V ± 5%		
Input Current (I _{DD})	35 mA Max		
Output Voltage (VoL)	10% V _{DD} Max		
(V _{OH})	90% V _{DD} Min		
Output Symmetry (50% V _{p-p} Level)	45% ~ 55%		
Rise Time (10% ~ 90% V _{p-p}) (TR)	3 nS Max		
Fall Time (90% ~ 10% V _{p-p}) (TF)	3 nS Max		
Output Load (HCMOS)	15 pF Max		
Startup Time (T _s)	10 ms Max		
Output Enable / Disable Time ²	100 nS Max		
Phase Jitter (12kHz ~ 20MHz)	< 1.1pS Typ.		
Maximum Soldering Temp / Time	260°C / 10 Seconds		
Moisture Sensitivity Level (MSL)	y Level (MSL) 1		
Termination Finish Au			

¹ Inclusive of 25°C tolerance, operating temperature range, input voltage change, load change, aging, shock, and vibration.

²An internal pullup resistor from pin 1 to pin 4 allows active output if pin 1 is left open.

ENABLE/DISABLE FUNCTION ²				
Pin 1	OUTPUT (Pin 3)			
OPEN ²	ACTIVE			
'1' Level VIH ≥ 70%V _{DD}	ACTIVE			
'0' Level VIL $\leq 30\%V_{DD}$	High Z			

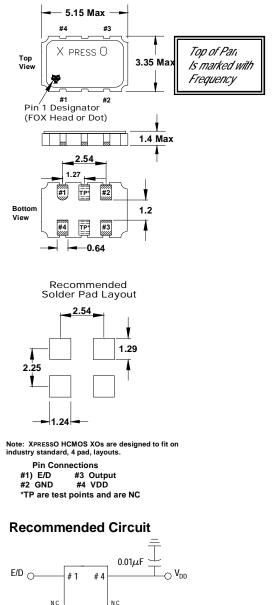
NOTE: A 0.01 μ F bypass capacitor should be placed between V_{DD} (Pin4) and GND (Pin 2) to minimize power supply line noise. **RoHS Compliance Status: Compliant**



Drawing is for reference to critical specifications defined by size measurements. Certain non-critical visual attributes, such as side castellations,

reference pin shape, etc. may vary

Mechanical / Dimensions (millimeters):



OUT

HCMOS LOAD

#2 #3

<u>NOTE</u>: The above specifications, having been carefully prepared and checked, is believed to be accurate at the time of publication; however, no responsibility is assumed by Fox Electronics for inaccuracies.

Reference / Comments	Title / Description: Product Specifications				
	Drawing Number: DWG-4548-29 Rev: 1 Part Number: 767-40-24			Size: A	
				Cage: 61429	
	Draftsperson: AR Approved: BN		Date: 08/25/2008		

GND

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