## Farnell Order Code: 1640985 FOX MODEL: FXO-HC736R-1 <br> FOX Part Number: 768-1-26 <br> HCMOS, CERAMIC, SMD, OSCILLATOR

A. Specifications

| Parameters |  |
| :---: | :---: |
| Frequency | 1.000 MHz |
| Frequency Stability ${ }^{1}$ | $\pm 25$ PPM |
| Temperature Range |  |
| $\begin{array}{ll}\text { Operating } & \text { (TOPR) } \\ \text { Storage } & \left(T_{\text {STG }}\right)\end{array}$ | $\begin{aligned} & -40^{\circ} \mathrm{C} \sim+85^{\circ} \mathrm{C} \\ & -55^{\circ} \mathrm{C} \sim+125^{\circ} \mathrm{C} \end{aligned}$ |
| Supply Voltage ( $\mathrm{V}_{\mathrm{DD}}$ ) | $3.3 \mathrm{~V} \pm 5 \%$ |
| Input Current (lod | 32 mA Max |
| $\begin{array}{ll}\text { Output Voltage } & \left(\begin{array}{l}\left(\mathrm{VOLL}_{\mathrm{O}}\right) \\ \left(\mathrm{VOH}_{\text {O }}\right.\end{array}\right.\end{array}$ | $\begin{aligned} & 10 \% \mathrm{~V}_{\mathrm{DD}} \operatorname{Max} \\ & 90 \% \mathrm{~V}_{\mathrm{DD}} \operatorname{Min} \\ & \hline \end{aligned}$ |
| Output Symmetry ( $50 \% \mathrm{~V}_{\mathrm{p} \text {-p }}$ Level) | 45\% ~ 55\% |
| Rise Time ( $10 \% \sim 90 \% \mathrm{~V}_{\text {p-p }}$ ) (TR) | 3 nS Max |
| Fall Time ( $90 \% \sim 10 \% \mathrm{~V}_{\text {p-p }}$ ) (TF) | 3 nS Max |
| Output Load (HCMOS) | 15 pF Max |
| Startup Time ( $\mathrm{T}_{\mathrm{s}}$ ) | 10 ms Max |
| Output Enable / Disable Time ${ }^{2}$ | 100 nS Max |
| Phase Jitter ( $12 \mathrm{kHz} \sim 20 \mathrm{MHz}$ ) | < 1.1 pS Typ. |
| Maximum Soldering Temp / Time | $260^{\circ} \mathrm{C} / 10$ Seconds |
| Moisture Sensitivity Level (MSL) | 1 |
| Termination Finish | Au |

${ }^{1}$ Inclusive of $25^{\circ} \mathrm{C}$ tolerance, operating temperature range, input voltage change, load change, aging, shock, and vibration.
${ }^{2}$ An internal pullup resistor from pin 1 to pin 4 allows active output if pin 1 is left open.

| ENABLE/DISABLE FUNCTION ${ }^{2}$ |  |
| :---: | :---: |
| Pin 1 | OUTPUT (Pin 3) |
| OPEN $^{2}$ | ACTIVE |
| '1' Level VIH $\geq 70 \% \mathrm{~V}_{\mathrm{DD}}$ | ACTIVE |
| '0' Level VIL $\leq 30 \% \mathrm{~V}_{\mathrm{DD}}$ | High Z |

NOTE: A $0.01 \mu \mathrm{~F}$ bypass capacitor should be placed between $\mathrm{V}_{\mathrm{DD}}$ (Pin4) and GND (Pin 2) to minimize power supply line noise.

## RoHS Compliance Status: Compliant



Drawing is for reference to critical specifications defined by size measurements.
Certain non-critical visual attributes, such as side castellations, reference pin shape, etc. may vary
B. Mechanical / Dimensions (millimeters):


Note: XPRESSO HCMOS XOs are designed to fit or industry standard, 4 pad, layouts.

Pin Connections
\#1) E/D \#3 Output
\#2 GND \#4 VDD
*TP are test points and are NC
Recommended Circuit


NOTE: The above specifications, having been carefully prepared and checked, is believed to be accurate at the time of publication; however, no responsibility is assumed by Fox Electronics for inaccuracies.

| Title / Description: Product Specifications |  |  |  |
| :--- | :--- | :--- | :---: |
| Drawing Number: DWG-4549-6 | Rev: 1 | Size: A |  |
| Part Number: 768-1-26 | Cage: 61429 |  |  |
| Draftsperson: AR $\quad$ Approved: BN | Date: 08/25/2008 |  |  |

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