

SOT-23



Pin Definition:

1. Gate
2. Source
3. Drain

PRODUCT SUMMARY

V_{DS} (V)	R_{DS(on)}(mΩ)	I_D (A)
30	30 @ V _{GS} = 10V	5.8
	43 @ V _{GS} = 4.5V	5.0

Features

- Advance Trench Process Technology
- High Density Cell Design for Ultra Low On-resistance

Application

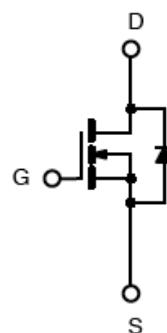
- Load Switch
- PA Switch

Ordering Information

Part No.	Package	Packing
TSM3404CX RFG	SOT-23	3Kpcs / 7" Reel

Note: "G" denotes Halogen Free Product.

Block Diagram



N-Channel MOSFET

Absolute Maximum Rating (Ta = 25°C unless otherwise noted)

Parameter	Symbol	Limit	Unit
Drain-Source Voltage	V _{DS}	30	V
Gate-Source Voltage	V _{GS}	±20	V
Continuous Drain Current	I _D	5.8	A
Pulsed Drain Current	I _{DM}	20	A
Continuous Source Current (Diode Conduction) ^{a,b}	I _S	2.5	A
Maximum Power Dissipation	T _a = 25°C	0.75	W
	T _a = 75°C	0.48	
Operating Junction Temperature	T _J	+150	°C
Operating Junction and Storage Temperature Range	T _J , T _{STG}	-55 to +150	°C

Thermal Performance

Parameter	Symbol	Limit	Unit
Junction to Foot Thermal Resistance	R<θ _{JF}	75	°C/W
Junction to Ambient Thermal Resistance (PCB mounted)	R<θ _{JA}	140	°C/W

Notes:

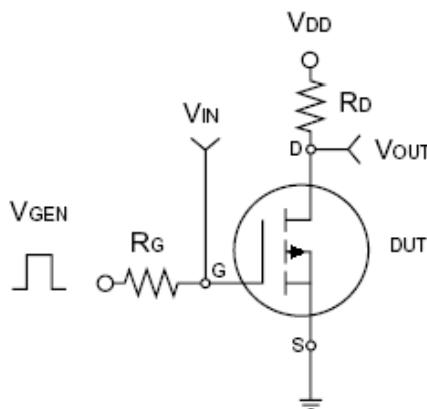
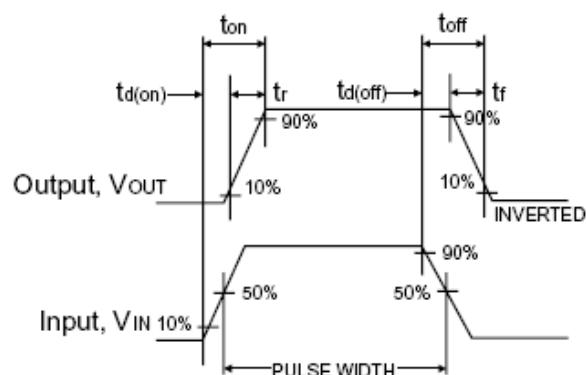
- a. Pulse width limited by the Maximum junction temperature
- b. Surface Mounted on FR4 Board, t ≤ 10 sec.

Electrical Specifications ($T_a = 25^\circ\text{C}$ unless otherwise noted)

Parameter	Conditions	Symbol	Min	Typ	Max	Unit
Static						
Drain-Source Breakdown Voltage	$V_{GS} = 0\text{V}, I_D = 250\mu\text{A}$	BV_{DSS}	30	--	--	V
Gate Threshold Voltage	$V_{DS} = V_{GS}, I_D = 250\mu\text{A}$	$V_{GS(\text{TH})}$	1	1.4	3	V
Gate Body Leakage	$V_{GS} = \pm 20\text{V}, V_{DS} = 0\text{V}$	I_{GSS}	--	--	± 100	μA
Zero Gate Voltage Drain Current	$V_{DS} = 24\text{V}, V_{GS} = 0\text{V}$	I_{DSS}	--	--	1.0	μA
On-State Drain Current	$V_{DS} = 5\text{V}, V_{GS} = 4.5\text{V}$	$I_{D(\text{ON})}$	20	--	--	A
Drain-Source On-State Resistance	$V_{GS} = 10\text{V}, I_D = 5.8\text{A}$	$R_{DS(\text{ON})}$	--	23	30	$\text{m}\Omega$
	$V_{GS} = 4.5\text{V}, I_D = 5\text{A}$		--	35	43	
Forward Transconductance	$V_{DS} = 5\text{V}, I_D = 5\text{A}$	g_{fs}	--	25	--	S
Diode Forward Voltage	$I_S = 1.0\text{A}, V_{GS} = 0\text{V}$	V_{SD}	--	0.76	1	V
Dynamic^b						
Total Gate Charge	$V_{DS} = 15\text{V}, I_D = 5.8\text{A}, V_{GS} = 10\text{V}$	Q_g	--	4.52	--	nC
Gate-Source Charge		Q_{gs}	--	1.24	--	
Gate-Drain Charge		Q_{gd}	--	1.68	--	
Input Capacitance	$V_{DS} = 15\text{V}, V_{GS} = 0\text{V}, f = 1.0\text{MHz}$	C_{iss}	--	400.96	--	pF
Output Capacitance		C_{oss}	--	100.47	--	
Reverse Transfer Capacitance		C_{rss}	--	71.82	--	
Switching^c						
Turn-On Delay Time	$V_{DD} = 15\text{V}, R_L = 2.2\Omega, I_D = 1\text{A}, V_{GEN} = 10\text{V}, R_G = 6\Omega$	$t_{d(on)}$	--	7.42	--	nS
Turn-On Rise Time		t_r	--	3.41	--	
Turn-Off Delay Time		$t_{d(off)}$	--	20.4	--	
Turn-Off Fall Time		t_f	--	3.01	--	

Notes:

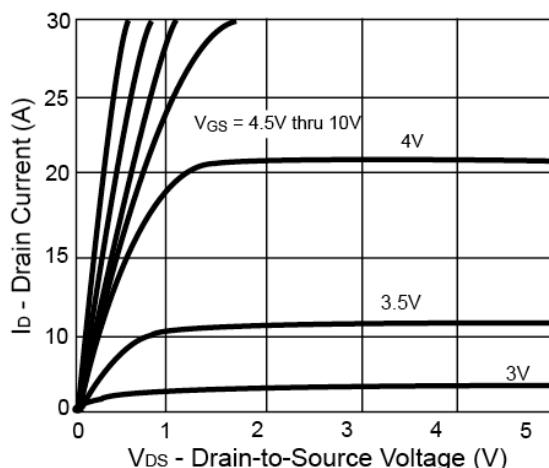
- a. pulse test: PW $\leq 300\mu\text{s}$, duty cycle $\leq 2\%$
- b. For DESIGN AID ONLY, not subject to production testing.
- c. Switching time is essentially independent of operating temperature.


Switching Test Circuit

Switching Waveforms

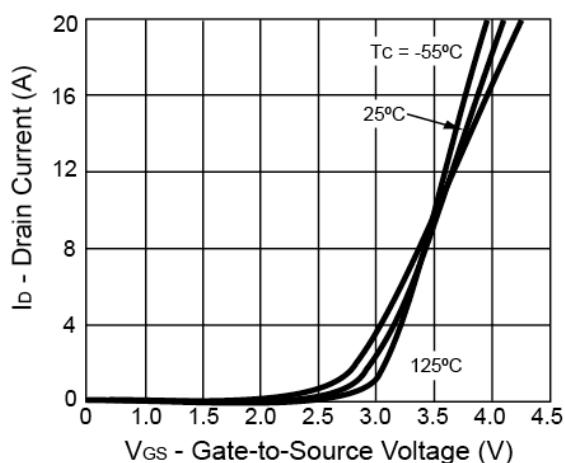


Electrical Characteristics Curve ($T_a = 25^\circ\text{C}$, unless otherwise noted)

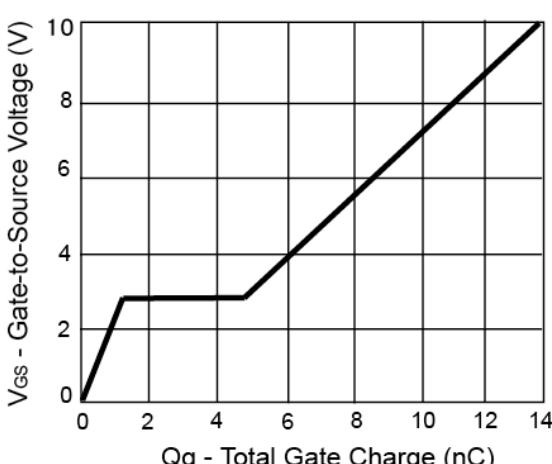
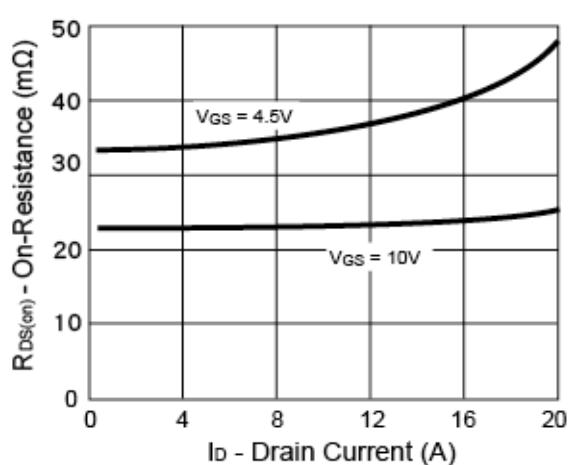
Output Characteristics



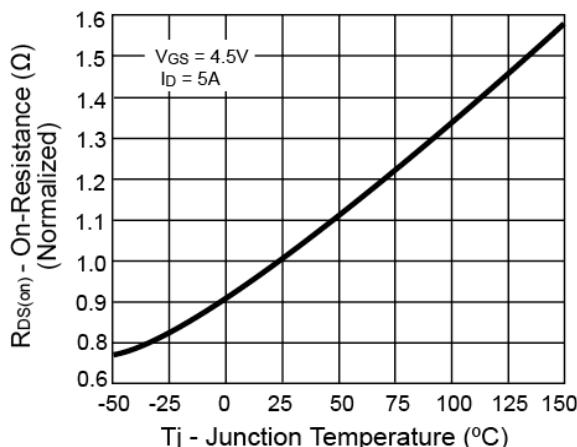
Transfer Characteristics



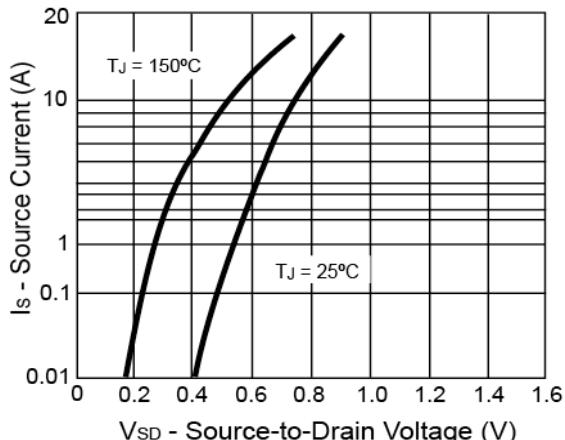
On-Resistance vs. Drain Current



On-Resistance vs. Junction Temperature



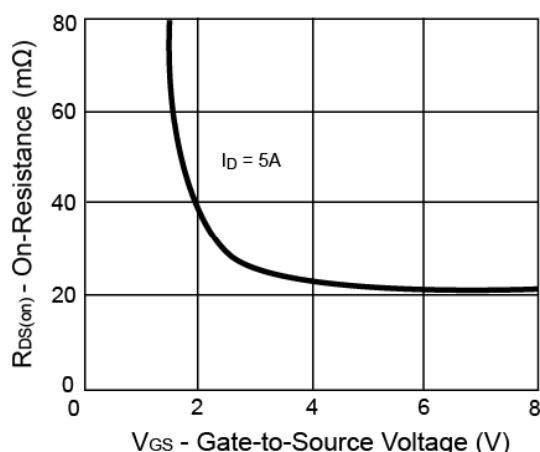
Source-Drain Diode Forward Voltage



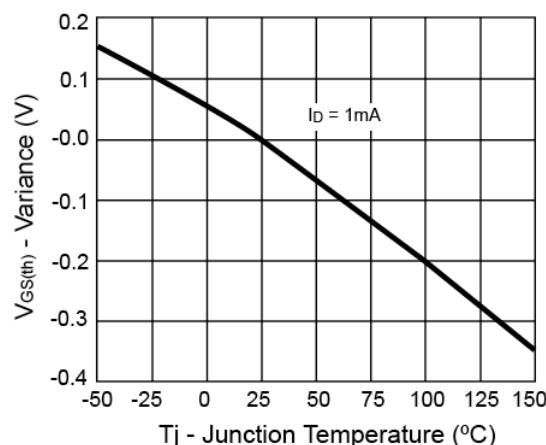


Electrical Characteristics Curve ($T_A = 25^\circ\text{C}$, unless otherwise noted)

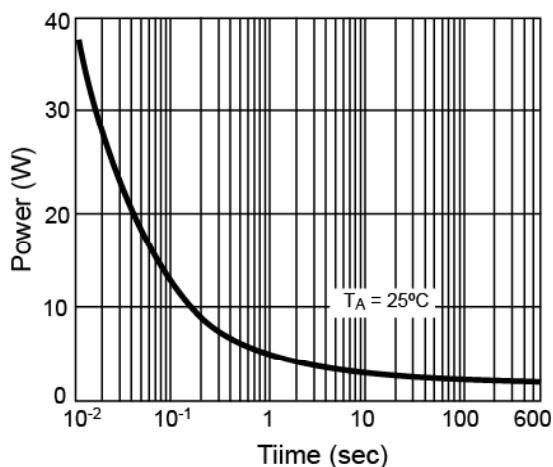
On-Resistance vs. Gate-Source Voltage



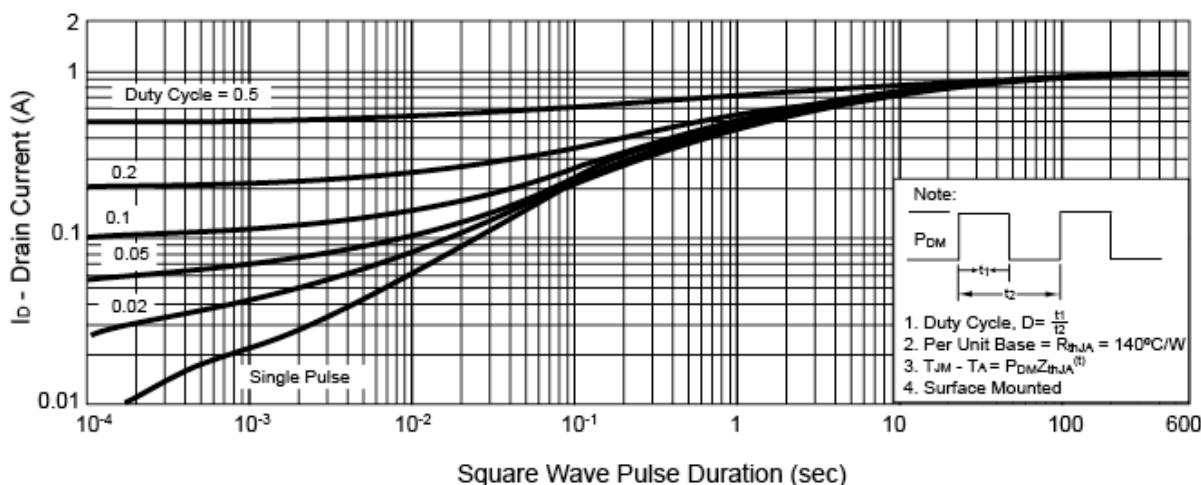
Threshold Voltage



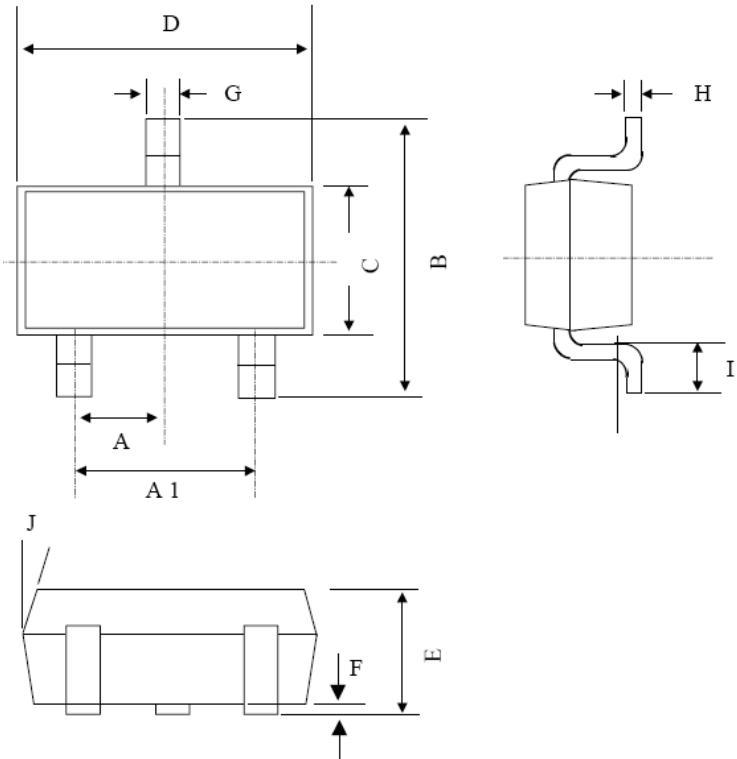
Single Pulse Power



Normalized Thermal Transient Impedance, Junction-to-Ambient

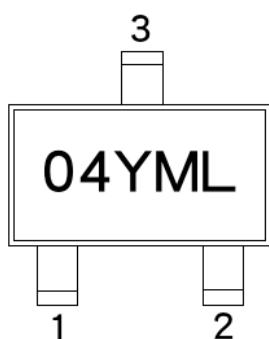


SOT-23 Mechanical Drawing



SOT-23 DIMENSION				
DIM	MILLIMETERS		INCHES	
	MIN	MAX	MIN	MAX
A	0.95 BSC		0.037 BSC	
A1	1.9 BSC		0.074 BSC	
B	2.60	3.00	0.102	0.118
C	1.40	1.70	0.055	0.067
D	2.80	3.10	0.110	0.122
E	1.00	1.30	0.039	0.051
F	0.00	0.10	0.000	0.004
G	0.35	0.50	0.014	0.020
H	0.10	0.20	0.004	0.008
I	0.30	0.60	0.012	0.024
J	5°	10°	5°	10°

Marking Diagram



04 = Device Code

Y = Year Code

M = Month Code for Halogen Free Product

O =Jan P =Feb Q =Mar R =Apr

S =May T =Jun U =Jul V =Aug

W =Sep X =Oct Y =Nov Z =Dec

L = Lot Code



TSM3404

30V N-Channel MOSFET



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