



QB-78K0KC2L-TB

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QB-R5F112GE-TB

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QB-V850EIG3-TB

QB-V850EIH4H-TB

QB-V850ESHG3-TB

QB-V850ESIE2-TB

QB-V850ESJG2-TB

QB-V850ESJG3L-TB

QB-V850ESJG3LUSB-TB

QB-V850ESJG3U-TB

QB-V850ESJ3-TB

QB-V850ESJ3E-TB

RTE510Y470TGB00000R

RTE5117GC0TGB00000R

Overview

Documentation

Design Support



Target Devices

* The status indicated in the bracket () refers to the tools and software products.



78K Family

Overview

As a CPU board usable in connection with the on-chip debugging emulator E1 or MINICUBE2, this product achieves simple and easy evaluation of the target MCU.

Features

- Built-in 78K0/KC2-L (μ PD78F0588GA) MCU
- Operatable at high speed with the 10 MHz system clock (oscillator equipped) (when supplied with 2.7 to 5.5V)
- Use SSP-T7-FL(Seiko Instruments Inc.) for sub clock.
- Equipped with universal area (2.54mm pitch)
- Both flash memory programming and on-chip debugging supported (TOOLD/TOOL1 pins)
- Lead-free (Pb-free)

Connection with The E1 Emulator

The CPU board is provided with a 16-pin/2.54mm pitch connector for the connection with MINICUBE2. Connecting it to the E1 emulator, whose user interface cable has a 14-pin/2.54mm pitch connector, requires the optional adapter QB-F14T16-01 (See [here](#) for the details).

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