

## High voltage fast-switching NPN power transistor

### **Features**

- High voltage capability
- Very high switching speed

### **Application**

■ Electronic ballast for fluorescent lighting

### **Description**

The device is manufactured using high voltage multi epitaxial planar technology for high switching speeds and high voltage capability. It uses a cellular emitter structure with planar edge termination to enhance switching speeds while maintaining the wide RBSOA. The ST83003 is expressly designed for a new solution to be used in compact fluorescent lamps, where it is coupled with the ST93003, its complementary PNP transistor.

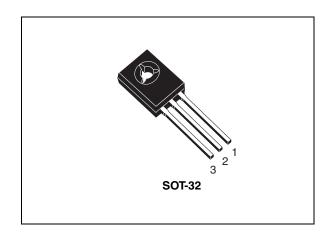


Figure 1. Internal schematic diagram

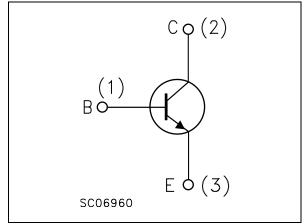


Table 1. Device summary

Order code	Marking	Package	Packaging	
ST83003	83003	SOT-32	Bag	

Contents ST83003

## **Contents**

1	Electrical ratings 3
2	Electrical characteristics 4
	2.1 Electrical characteristics (curves) 5
3	Test circuits
4	Package mechanical data 8
5	Revision history

ST83003 Electrical ratings

# 1 Electrical ratings

Table 2. Absolute maximum ratings

Symbol	Parameter	Value	Unit
$V_{CES}$	Collector-emitter voltage (V <sub>BE</sub> = 0)	700	٧
V <sub>CEO</sub>	Collector-emitter voltage (I <sub>B</sub> = 0)	400	٧
V <sub>EBO</sub>	Emitter-base voltage $(I_C = 0, I_B = 0.75 \text{ A}, t_p < 10 \mu\text{s})$	V <sub>(BR)EBO</sub>	<b>V</b>
I <sub>C</sub>	Collector current	1.5	Α
I <sub>CM</sub>	Collector peak current (t <sub>p</sub> < 5 ms)	3	Α
I <sub>B</sub>	Base current	0.75	Α
I <sub>BM</sub>	Base peak current (t <sub>p</sub> < 5 ms)	1.5	Α
P <sub>TOT</sub>	Total dissipation at T <sub>c</sub> = 25 °C	40	W
T <sub>STG</sub>	Storage temperature	-65 to 150	°C
TJ	Max. operating junction temperature	150	°C

Table 3. Thermal data

Symbol	Parameter	Value	Unit
R <sub>thJC</sub>	Thermal resistance junction-case max	3.1	°C/W

Electrical characteristics ST83003

## 2 Electrical characteristics

 $T_{case} = 25$  °C unless otherwise specified

Table 4. On/off states

Symbol	Parameter	Test conditions	Value			Unit
Symbol			Min.	Тур.	Max.	Jilit
I <sub>CES</sub>	Collector cut-off current (V <sub>BE</sub> = 0)	V <sub>CE</sub> = 700 V V <sub>CE</sub> = 700 V, T <sub>C</sub> = 125 °C			1 5	mA mA
V <sub>(BR)EBO</sub>	Emitter-base breakdown voltage (I <sub>C</sub> = 0)	I <sub>E</sub> = 10 mA	12		18	V
V <sub>CEO(sus)</sub> <sup>(1)</sup>	Collector-emitter sustaining voltage (I <sub>B</sub> = 0)	I <sub>C</sub> = 10 mA	400			V
V <sub>CE(sat)</sub> (1)	Collector-emitter saturation voltage	$I_C = 0.5 \text{ A}, I_B = 0.1 \text{ A}$ $I_C = 0.35 \text{ A}, I_B = 50 \text{ mA}$			0.5 1	V V
V <sub>BE(sat)</sub> (1)	Base-emitter saturation voltage	I <sub>C</sub> = 0.5 A, I <sub>B</sub> = 0.1 A			1	V
h <sub>FE</sub> <sup>(1)</sup>	DC current gain	$I_C = 10 \text{ mA}, V_{CE} = 5 \text{ V}$ $I_C = 0.35 \text{ A}, V_{CE} = 5 \text{ V}$ $I_C = 1 \text{ A}, V_{CE} = 5 \text{ V}$	10 16 4	25	32	
t <sub>r</sub> t <sub>s</sub> t <sub>f</sub>	Resistive load Rise time Storage time Fall time	$I_{C}$ = 0.35 A, $V_{CC}$ = 125 V, $I_{B1}$ = 70 mA, $I_{B2}$ = -70 mA $I_{P} \ge$ 25 $\mu$ s see <i>Figure 14</i>	1.5	100 2.2 0.2	2.9	ns µs µs
t <sub>s</sub>	Inductive load Storage time Fall time	$I_{C} = 0.5 \text{ A}, I_{B1} = 0.1 \text{ A},$ $V_{BE(off)} = -5 \text{ V},$ $L = 10 \text{ mH}, V_{clamp} = 300 \text{ V}$ see <i>Figure 13</i>		450 90		ns ns

<sup>1.</sup> Pulse test: pulse duration  $\leq$  300  $\mu$ s, duty cycle  $\leq$  2 %

#### **Electrical characteristics (curves)** 2.1

Figure 2. Safe operating area

GC84410  $I_{C}(A)$ Area of permissible operation driving turn—or provided R<sub>BE</sub>=100 Ohm and tr≤ 100ns 10<sup>1</sup> I<sub>C</sub> MAX PULSED PULSE OPERATION \* 10<sup>0</sup> IC MAX D.C. OPERATION  $10^{-1}$ For single non repetitive pulse 4 6 8 10<sup>1</sup> 4 6 8 10<sup>2</sup>

Figure 3. **Derating** 

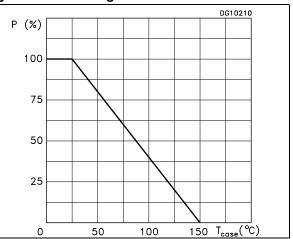


Figure 4. DC current gain  $(V_{CE} = 5 V)$ 

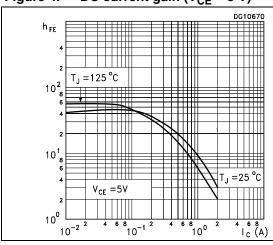


Figure 5. DC current gain  $(V_{CE} = 1 V)$ 

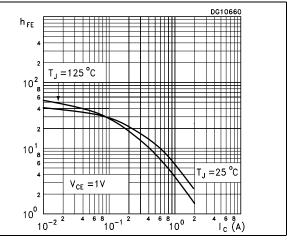
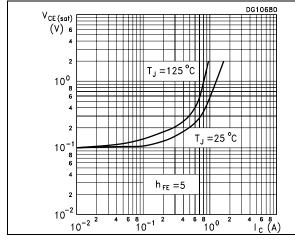
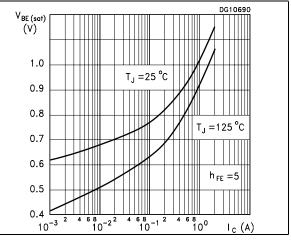


Figure 6. Collector emitter saturation voltage Figure 7. Base emitter saturation voltage

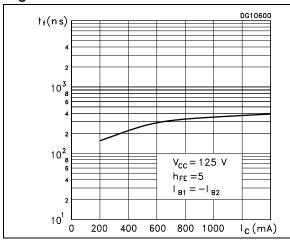




Electrical characteristics ST83003

Figure 8. Resistive load fall time

Figure 9. Resistive load storage time



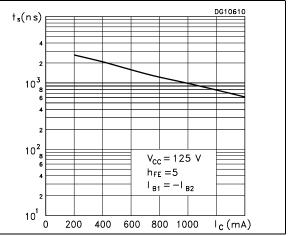
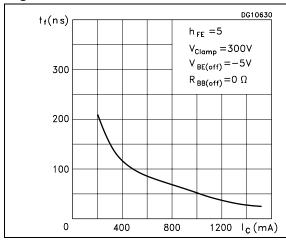


Figure 10. Inductive load fall time

Figure 11. Inductive load storage time



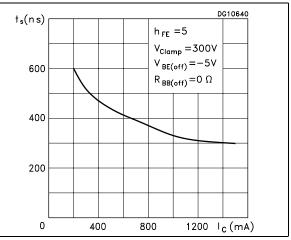
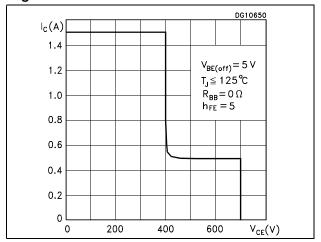


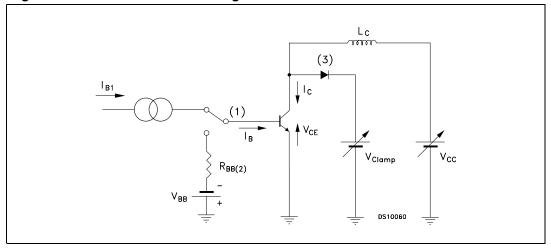
Figure 12. Reverse biased SOA



ST83003 Test circuits

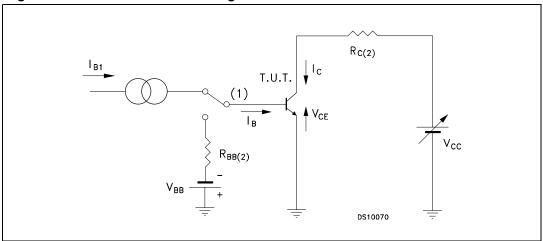
## 3 Test circuits

Figure 13. Inductive load switching



- 1. Fast electronic switch
- 2. Non-inductive resistor
- 3. Fast recovery rectifier

Figure 14. Resistive load switching



- 1. Fast electronic switch
- 2. Non-inductive resistor

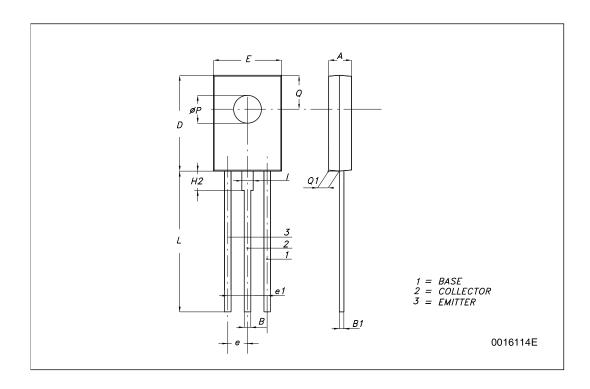
# 4 Package mechanical data

In order to meet environmental requirements, ST offers these devices in different grades of ECOPACK<sup>®</sup> packages, depending on their level of environmental compliance. ECOPACK<sup>®</sup> specifications, grade definitions and product status are available at: <a href="https://www.st.com">www.st.com</a>. ECOPACK<sup>®</sup> is an ST trademark.

8/11 Doc ID 9085 Rev 4

SOT-32 (7	TO-126)	MECHANICAL	DATA
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DIM		mm.		
DIM.	MIN.	TYP	MAX.	
А	2.4		2.9	
В	0.64		0.88	
B1	0.39		0.63	
D	10.5		11.05	
Е	7.4		7.8	
е	2.04	2.29	2.54	
e1	4.07	4.58	5.08	
L	15.3		16	
Р	2.9		3.2	
Q		3.8		
Q1	1		1.52	
H2		2.15		
I		1.27		



Revision history ST83003

# 5 Revision history

Table 5. Document revision history

Date	Revision	Changes
16-Oct-2002	1	Initial release
08-Jul-2008	2	Mechanical data has been updated
08-Sep-2009	3	Updated packaging information Table 1 on page 1.
03-Dec-2010	4	Added Table 3: Thermal data on page 3.

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