# High Performance Schottky Rectifier, $2 \times 3.5$ A 



| PRODUCT SUMMARY |  |
| :---: | :---: |
| Package | D-PAK (TO-252AA) |
| $\mathrm{I}_{\mathrm{F}(\mathrm{AV})}$ | $2 \times 3.5 \mathrm{~A}$ |
| $\mathrm{~V}_{\mathrm{R}}$ | 100 V |
| $\mathrm{~V}_{\mathrm{F}}$ at $\mathrm{I}_{\mathrm{F}}$ | See Electrical table |
| $\mathrm{I}_{\mathrm{RM}}$ | 4.9 mA at $125^{\circ} \mathrm{C}$ |
| $\mathrm{T}_{\mathrm{J}}$ max. | $150^{\circ} \mathrm{C}$ |
| Diode variation | Common cathode |
| $\mathrm{E}_{\mathrm{AS}}$ | 5 mJ |

## FEATURES

- Popular D-PAK outline
- Small foot print, surface mountable
- Low forward voltage drop
- High frequency operation
- Guard ring for enhanced ruggedness and long term reliability
- Meets MSL level 1, per J-STD-020, LF maximum peak of $260{ }^{\circ} \mathrm{C}$
- Material categorization: for definitions of compliance please see www.vishay.com/doc?99912


## DESCRIPTION

The VS-6CWQ10FNPbF surface mount, center tap, Schottky rectifier series has been designed for applications requiring low forward drop and small foot prints on PC board. Typical applications are in disk drives, switching power supplies, converters, freewheeling diodes, battery charging, and reverse battery protection.

| MAJOR RATINGS AND CHARACTERISTICS |  |  |  |
| :--- | :--- | :---: | :---: |
| SYMBOL | CHARACTERISTICS | VALUES | UNITS |
| $\mathrm{I}_{\mathrm{F}(\mathrm{AV})}$ | Rectangular waveform | 7 | A |
| $\mathrm{~V}_{\text {RRM }}$ |  | 100 | V |
| $\mathrm{I}_{\mathrm{FSM}}$ | $\mathrm{t}_{\mathrm{p}}=5 \mu \mathrm{~s}$ sine | 440 | A |
| $\mathrm{~V}_{\mathrm{F}}$ | $3 \mathrm{~A}_{\mathrm{pk}}, \mathrm{T}_{J}=125^{\circ} \mathrm{C}($ per leg $)$ | 0.63 | V |
| $\mathrm{~T}_{J}$ | Range | -40 to +150 | ${ }^{\circ} \mathrm{C}$ |


| VOLTAGE RATINGS |  |  |  |
| :--- | :---: | :---: | :---: |
| PARAMETER | SYMBOL | VS-6CWQ10FNPbF | UNITS |
| Maximum DC reverse voltage | $\mathrm{V}_{\mathrm{R}}$ | 100 | V |
| Maximum working peak reverse voltage | $\mathrm{V}_{\mathrm{RWM}}$ |  | V |


| ABSOLUTE MAXIMUM RATINGS |  |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: |
| PARAMETER | SYMBOL | TEST CONDITIONS |  | VALUES | UNITS |
| Maximum average <br> forward current <br> See fig. 5 per leg |  | $50 \%$ duty cycle at $\mathrm{T}_{\mathrm{C}}=135^{\circ} \mathrm{C}$, rectangular waveform |  | 3.5 7 | A |
| Maximum peak one cycle non-repetitive surge current per leg See fig. 7 | $I_{\text {FSM }}$ | $5 \mu \mathrm{~s}$ sine or $3 \mu \mathrm{~s}$ rect. pulse 10 ms sine or 6 ms rect. pulse | Following any rated load condition and with rated $\mathrm{V}_{\text {RRM }}$ applied | 440 70 |  |
| Non-repetitive avalanche energy per leg | $\mathrm{E}_{\text {AS }}$ | $\mathrm{T}_{J}=25^{\circ} \mathrm{C}, \mathrm{I}_{\mathrm{AS}}=1 \mathrm{~A}, \mathrm{~L}=10 \mathrm{mH}$ |  | 5.0 | mJ |
| Repetitive avalanche current per leg | $\mathrm{I}_{\text {AR }}$ | Current decaying linearly to zero in $1 \mu \mathrm{~s}$ Frequency limited by $\mathrm{T}_{J}$ maximum $\mathrm{V}_{\mathrm{A}}=1.5 \times \mathrm{V}_{\mathrm{R}}$ typical |  | 0.5 | A |

VS-6CWQ10FNPbF
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| ELECTRICAL SPECIFICAT | ONS |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: |
| PARAMETER | SYMBOL | TEST CONDITIONS |  | VALUES | UNITS |
| Maximum forward voltage drop per leg See fig. 1 | $V_{\text {FM }}{ }^{(1)}$ | 3 A | $\mathrm{T}_{J}=25^{\circ} \mathrm{C}$ | 0.81 | V |
|  |  | 6 A |  | 0.96 |  |
|  |  | 3 A | $\mathrm{T}_{J}=125^{\circ} \mathrm{C}$ | 0.63 |  |
|  |  | 6 A |  | 0.74 |  |
| Maximum reverse leakage current per leg See fig. 2 | $\mathrm{I}_{\text {RM }}{ }^{(1)}$ | $\mathrm{T}_{J}=25^{\circ} \mathrm{C}$ | $\mathrm{V}_{\mathrm{R}}=$ Rated $\mathrm{V}_{\mathrm{R}}$ | 1 | mA |
|  |  | $\mathrm{T}_{J}=125^{\circ} \mathrm{C}$ |  | 4.9 |  |
| Threshold voltage | $\mathrm{V}_{\mathrm{F} \text { (TO) }}$ | $\mathrm{T}_{J}=\mathrm{T}_{J}$ maximum |  | 0.48 | V |
| Forward slope resistance | $r_{\text {t }}$ |  |  | 30.89 | $\mathrm{m} \Omega$ |
| Typical junction capacitance per leg | $\mathrm{C}_{\text {T }}$ | $\mathrm{V}_{\mathrm{R}}=5 \mathrm{~V}$ DC, , | e 100 kHz to 1 MHz ), $25^{\circ} \mathrm{C}$ | 92 | pF |
| Typical series inductance per leg | Ls | Measured lead | m from package body | 5.0 | nH |
| Maximum voltage rate of change | dV/dt | Rated VR |  | 10000 | V/ $/ \mathrm{s}$ |

Note
${ }^{(1)}$ Pulse width $<300 \mu \mathrm{~s}$, duty cycle $<2 \%$

| PARAMETER | SYMBOL | TEST CONDITIONS | VALUES | UNITS |
| :---: | :---: | :---: | :---: | :---: |
| Maximum junction and storage temperature range | $\mathrm{T}_{\mathrm{J}}{ }^{(1)}, \mathrm{T}_{\text {Stg }}$ |  | -40 to +150 | ${ }^{\circ} \mathrm{C}$ |
| Maximum thermal resistance, junction to case | $\mathrm{R}_{\text {thJc }}$ | DC operation See fig. 4 | 4.70 | ${ }^{\circ} \mathrm{C} / \mathrm{W}$ |
|  |  |  | 2.35 |  |
| Approximate weight |  |  | 0.3 | g |
|  |  |  | 0.01 | oz. |
| Marking device |  | Case style D-PAK (similar to TO-252AA) | 6CWQ10FN |  |

## Note

(1) $\frac{d P_{\text {tot }}}{d T_{J}}<\frac{1}{R_{\text {thJA }}}$ thermal runaway condition for a diode on its own heatsink

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Fig. 1 - Maximum Forward Voltage Drop Characteristics (Per Leg)


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage (Per Leg)


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage (Per Leg)


Fig. 4 - Maximum Thermal Impedance $Z_{\text {thJC }}$ Characteristics (Per Leg)


Fig. 5 - Maximum Allowable Case Temperature vs. Average Forward Current (Per Leg)


Fig. 6 - Forward Power Loss Characteristics (Per Leg)


Fig. 7 - Maximum Non-Repetitive Surge Current (Per Leg)

## Note

(1) Formula used: $T_{C}=T_{J}-\left(P d+\mathrm{Pd}_{\mathrm{REV}}\right) \times \mathrm{R}_{\text {thJC }}$;
$\mathrm{Pd}=$ forward power loss $=\mathrm{I}_{\mathrm{F}(\mathrm{AV})} \times \mathrm{V}_{\mathrm{FM}}$ at $\left(\mathrm{I}_{\mathrm{F}(\mathrm{AV})} / \mathrm{D}\right)$ (see fig. 6);
$\mathrm{Pd}_{\mathrm{REV}}=$ inverse power loss $=\mathrm{V}_{\mathrm{R} 1} \times \mathrm{I}_{\mathrm{R}}(1-\mathrm{D}) ; \mathrm{I}_{\mathrm{R}}$ at $\mathrm{V}_{\mathrm{R} 1}=80 \%$ rated $\mathrm{V}_{\mathrm{R}}$
$\qquad$

## ORDERING INFORMATION TABLE



1 - Vishay Semiconductors product
2 - Current rating (7 A)
3 - Center tap configuration
4 - Package identifier:
W = D-PAK
5 - Schottky "Q" series
6 - Voltage rating ( $10=100 \mathrm{~V}$ )
7 - FN = TO-252AA (D-PAK)
8 - - None $=$ tube ( 50 pieces)

- TR = tape and reel
-TRL = tape and reel (left oriented)
- TRR = tape and reel (right oriented)
$9 \quad-\quad \mathrm{PbF}=$ lead $(\mathrm{Pb})$-free

| LINKS TO RELATED DOCUMENTS |  |
| :--- | :--- |
| Dimensions | $\underline{w w w . v i s h a y . c o m / d o c ? 95016 ~}$ |
| Part marking information | $\underline{w w w . v i s h a y . c o m / d o c ? 95059 ~}$ |
| Packaging information | $\underline{w w w . v i s h a y . c o m / d o c ? 95033 ~}$ |

## D-PAK (TO-252AA)

DIMENSIONS in millimeters and inches


| SYMBOL | MILLIMETERS |  | INCHES |  | NOTES | SYMBOL | MILLIMETERS |  | INCHES |  | NOTES |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | MIN. | MAX. | MIN. | MAX. |  |  | MIN. | MAX. | MIN. | MAX. |  |
| A | 2.18 | 2.39 | 0.086 | 0.094 |  | e | 2.29 BSC |  | 0.090 BSC |  |  |
| A1 | - | 0.13 | - | 0.005 |  | H | 9.40 | 10.41 | 0.370 | 0.410 |  |
| b | 0.64 | 0.89 | 0.025 | 0.035 |  | L | 1.40 | 1.78 | 0.055 | 0.070 |  |
| b2 | 0.76 | 1.14 | 0.030 | 0.045 |  | L1 | 2.74 BSC |  | 0.108 REF. |  |  |
| b3 | 4.95 | 5.46 | 0.195 | 0.215 | 3 | L2 | 0.51 BSC |  | 0.020 BSC |  |  |
| c | 0.46 | 0.61 | 0.018 | 0.024 |  | L3 | 0.89 | 1.27 | 0.035 | 0.050 | 3 |
| c2 | 0.46 | 0.89 | 0.018 | 0.035 |  | L4 | - | 1.02 | - | 0.040 |  |
| D | 5.97 | 6.22 | 0.235 | 0.245 | 5 | L5 | 1.14 | 1.52 | 0.045 | 0.060 | 2 |
| D1 | 5.21 | - | 0.205 | - | 3 | $\varnothing$ | $0^{\circ}$ | $10^{\circ}$ | $0^{\circ}$ | $10^{\circ}$ |  |
| E | 6.35 | 6.73 | 0.250 | 0.265 | 5 | 01 | $0^{\circ}$ | $15^{\circ}$ | $0^{\circ}$ | $15^{\circ}$ |  |
| E1 | 4.32 | - | 0.170 | - | 3 | $\varnothing 2$ | $25^{\circ}$ | $35^{\circ}$ | $25^{\circ}$ | $35^{\circ}$ |  |

## Notes

${ }^{(1)}$ Dimensioning and tolerancing as per ASME Y14.5M-1994
(2) Lead dimension uncontrolled in L5
(3) Dimension D1, E1, L3 and b3 establish a minimum mounting surface for thermal pad
${ }^{(4)}$ Section C-C dimension apply to the flat section of the lead between 0.13 and $0.25 \mathrm{~mm}\left(0.005\right.$ and $\left.0.10^{\prime \prime}\right)$ from the lead tip
(5) Dimension D, and E do not include mold flash. Mold flash shall not exceed $0.127 \mathrm{~mm}\left(0.005^{\prime \prime}\right)$ per side. These dimensions are measured at the outermost extremes of the plastic body
(6) Dimension b1 and c1 applied to base metal only
(7) Datum $A$ and $B$ to be determined at datum plane H
(8) Outline conforms to JEDEC outline TO-252AA

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