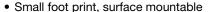


High Performance Schottky Rectifier, 1.0 A



PRODUCT SUMMARY			
Package	SMB		
I _{F(AV)}	1.0 A		
V_{R}	30 V		
V _F at I _F	0.420 V		
I _{RM} max.	15 mA at 125 °C		
T_J max.	150 °C		
Diode variation	Single die		
E _{AS}	3.0 mJ		

FEATURES







COMPLIANT

vory low for ward vortage drop

- High frequency operation
 Guard, ring, for, enhanced, ruggedne
- Guard ring for enhanced ruggedness and long term reliability
- Meets MSL level 1, per J-STD-020, LF maximum peak of 260 °C
- · Designed and qualified for industrial level
- Material categorization: for definitions of compliance please see <u>www.vishay.com/doc?99912</u>

DESCRIPTION

The VS-10BQ030PbF surface mount Schottky rectifier has been designed for applications requiring low forward drop and small foot prints on PC boards. Typical applications are in disk drives, switching power supplies, converters, freewheeling diodes, battery charging, and reverse battery protection.

MAJOR RATINGS AND CHARACTERISTICS					
SYMBOL	CHARACTERISTICS	CHARACTERISTICS VALUES			
I _{F(AV)}	Rectangular waveform	1.0	Α		
V _{RRM}		30	V		
I _{FSM}	t _p = 5 ms sine	430	Α		
V _F	1.0 A _{pk} , T _J = 125 °C	0.30	V		
T _J	Range	-55 to +150	°C		

VOLTAGE RATINGS				
PARAMETER	SYMBOL	VS-10BQ030PbF	UNITS	
Maximum DC reverse voltage	V_{R}	30	V	
Maximum working peak reverse voltage	V_{RWM}	30	V	

ABSOLUTE MAXIMUM RATINGS					
PARAMETER	SYMBOL	TEST CONDITIONS		VALUES	UNITS
Maximum average forward current	I _{F(AV)}	I _{F(AV)} 50 % duty cycle at T _L = 106 °C, rectangular waveform		1.0	Α
Maximum peak one cycle non-repetitive surge current	l=	5 µs sine or 3 µs rect. pulse	Following any rated load condition and with rated V _{RRM} applied	430	A
See fig. 6	IFSM	10 ms sine or 6 ms rect. pulse		90	
Non-repetitive avalanche energy	E _{AS}	$T_J = 25 ^{\circ}\text{C}, I_{AS} = 1 \text{A}, L = 6 \text{mH}$		3.0	mJ
Repetitive avalanche current	I _{AR}	Current decaying linearly to zero in 1 μ s Frequency limited by T _J maximum V _A = 1.5 x V _R typical		1.0	А



ELECTRICAL SPECIFICATIONS					
PARAMETER	SYMBOL	TEST CONDITIONS		VALUES	UNITS
Maximum forward voltage drop	V _{FM} ⁽¹⁾	1 A	T _J = 25 °C	0.420	V
		2 A		0.470	
		1 A	- T _J = 125 °C	0.300	
		2 A		0.370	
Maximum reverse leakage current	I _{RM} ⁽¹⁾	T _J = 25 °C	V _R = Rated V _R	0.5	mA
		T _J = 100 °C		5.0	
		T _J = 125 °C		15	
Maximum junction capacitance	C _T	$V_R = 5 V_{DC}$ (test signal range 100 kHz to 1 MHz), 25 °C		200	pF
Typical series inductance	L _S	Measured lead to lead 5 mm from package body 2.0		nH	
Maximum voltage rate of change	dV/dt	Rated V _R 10 000 V/ _k		V/µs	

Note

 $^{^{(1)}}$ Pulse width < 300 μ s, duty cycle < 2 %

THERMAL - MECHANICAL SPECIFICATIONS				
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS
Maximum junction and storage temperature range	T _J ⁽¹⁾ , T _{Stg}		-55 to +150	°C
Maximum thermal resistance, junction to lead	R _{thJL} ⁽²⁾	DC operation	25	°C/W
Maximum thermal resistance, junction to ambient	R _{thJA}		80	C/VV
Approximate weight			0.10	g
Approximate weight			0.003	OZ.
Marking device		Case style SMB (similar DO-214AA)	V-	1E

Notes

 $\frac{dP_{tot}}{T} < \frac{R_{thJA}}{R_{thJA}}$ thermal runaway condition for a diode on its own heatsink

(2) Mounted 1" square PCB

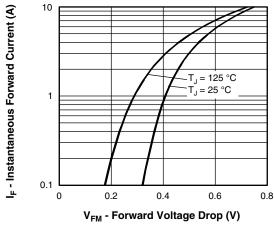


Fig. 1 - Maximum Forward Voltage Drop Characteristics

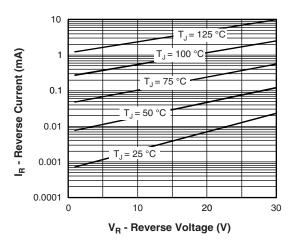


Fig. 2 - Typical Peak Reverse Current vs. Reverse Voltage

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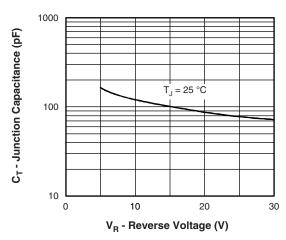


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

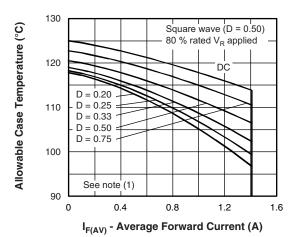
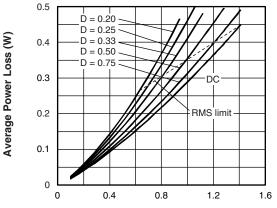
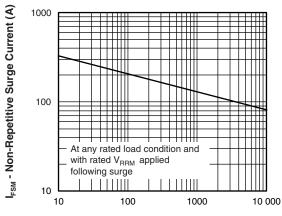


Fig. 4 - Maximum Average Forward Current vs. Allowable Lead Temperature



I_{F(AV)} - Average Forward Current (A)

Fig. 5 - Maximum Average Forward Dissipation vs. Average Forward Current



t_p - Square Wave Pulse Duration (μs)

Fig. 6 - Maximum Peak Surge Forward Current vs.
Pulse Duration

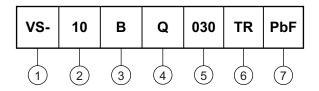
Note

 $\begin{array}{ll} \text{(1)} & \text{Formula used: } T_C = T_J - (\text{Pd} + \text{Pd}_{\text{REV}}) \times \text{R}_{\text{thJC}}; \\ \text{Pd} = \text{Forward power loss} = I_{\text{F(AV)}} \times \text{V}_{\text{FM}} \text{ at } (I_{\text{F(AV)}}/D) \text{ (see fig. 6)}; \\ \text{Pd}_{\text{REV}} = \text{Inverse power loss} = \text{V}_{\text{R1}} \times \text{I}_{\text{R}} \text{ (1 - D)}; I_{\text{R}} \text{ at } \text{V}_{\text{R1}} = 80 \text{ \% rated V}_{\text{R}} \\ \end{array}$



ORDERING INFORMATION TABLE

Device code



- 1 Vishay Semiconductors product
- 2 Current rating
- 3 B = single lead diode
- 4 Q = Schottky "Q" series
- Voltage rating (030 = 30 V)
- 6 • None = box (1000 pieces)
 - TR = tape and reel (3000 pieces)
- 7 PbF = lead (Pb)-free

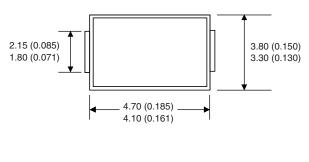
LINKS TO RELATED DOCUMENTS				
Dimensions				
Part marking information		www.vishay.com/doc?95029		
De el cenir en informaction	Tape and reel	www.vishay.com/doc?95034		
Packaging information	Bulk	www.vishay.com/doc?95397		

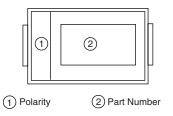


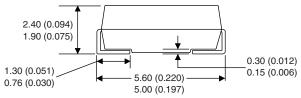
Vishay High Power Products

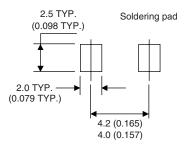
SMB

DIMENSIONS in millimeters (inches)







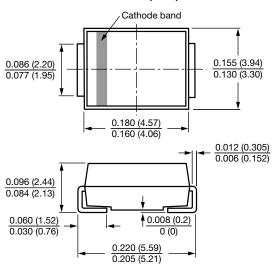




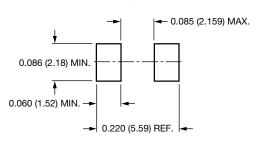
SMB

DIMENSIONS in inches (millimeters)

DO-214AA (SMB)



Mounting Pad Layout





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Vishay

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Revision: 02-Oct-12 Document Number: 91000

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CUS06(TE85L,Q,M) D1FH3-5063 MBR0530L-TP MBR10100CT-BP MBR30H100MFST1G MMBD301M3T5G PMAD1103-LF
PMAD1108-LF RB160M-50TR RB520S-30 RB551V-30 DD350N18K DZ435N40K DZ600N16K BAS16E6433HTMA1 BAS 3010S02LRH E6327 BAT 54-02LRH E6327 IDL02G65C5XUMA1 NSR05F40QNXT5G JANS1N6640 SB07-03C-TB-H SB1003M3-TL-W
SBAT54CWT1G SBM30-03-TR-E SK32A-LTP SK33A-TP SK34A-TP SK34B-TP SMD1200PL-TP ACDBN160-HF SS3003CH-TL-E
STPS30S45CW PDS3100Q-7 GA01SHT18 CRS10I30A(TE85L,QM MBR1240MFST1G MBRB30H30CT-1G BAS28E6433HTMA1 BAS
70-02L E6327 HSB123JTR-E JANTX1N5712-1 VS-STPS40L45CW-N3 DD350N12K SB007-03C-TB-E SB10015M-TL-E SB1003M3-TL-E SK110-LTP SK154-TP SK32A-TP SK33B-TP