AEC-Q101 Qualified

2.5V Drive Nch MOS FET

RTR040N03FRA

Structure

Silicon N-channel MOS FET

Features

- 1) Low On-resistance.
- 2) Built-in G-S Protection Diode.
- 3) Small Surface Mount Package (TSMT3).

Application

Power switching, DC / DC converter.

Packaging specifications

	Package	Taping
Туре	Code	TL
	Basic ordering unit (pieces)	3000
RTR040N0	0	

•Absolute maximum ratings (Ta=25°C)

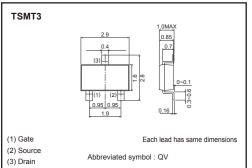
Parameter		Symbol	Limits	Unit
Drain-source voltage		VDSS	30	V
Gate-source voltage		V _{GSS}	12	V
Drain current	Continuous	ID	±4.0	А
	Pulsed	I _{DP} *1	±16	А
Source current	Continuous	ls	0.8	А
(Body diode)	Pulsed	I _{SP} *1	16	А
Total power dissipation		P _D *2	1.0	W
Channel temperature		Tch	150	°C
Range of Storage temperature		Tstg	-55 to +150	°C

*1 Pw≤10μs, Duty cycle≤1% ∗2 Mounted on a ceramic board

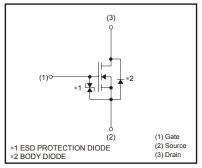
•Thermal resistance

Parameter	Symbol	Limits	Unit
Channel to ambient	Rth (ch-a)*	125	°C / W
* Mounted on a ceramic board			

•External dimensions (Unit : mm)



•Equivalent circuit





Transistors

•Electrical characteristics (Ta=25°C)

Parameter	Symbol	Min.	Тур.	Max.	Unit	Conditions
Gate-source leakage	Igss	-	-	10	μA	V _{GS} =12V, V _{DS} =0V
Drain-source breakdown voltage	V(BR) DSS	30	_	_	V	I _D =1mA, V _{GS} =0V
Zero gate voltage drain current	IDSS	-	_	1	μA	V _{DS} =30V, V _{GS} =0V
Gate threshold voltage	VGS (th)	0.5	_	1.5	V	V _{DS} =10V, I _D =1mA
o		-	34	48	mΩ	I _D =4.0A, V _{GS} =4.5V
Static drain-source on-state resistance	$R_{DS(on)}^*$	-	36	50	mΩ	I _D =4.0A, V _{GS} =4.0V
resistance		-	47	66	mΩ	I _D =4.0A, V _{GS} =2.5V
Forward transfer admittance	Y _{fs} *	4.0	_	_	S	V _{DS} =10V, I _D =4.0A
Input capacitance	Ciss	-	475	_	pF	V _{DS} =10V
Output capacitance	Coss	-	120	_	pF	V _{GS} =0V
Reverse transfer capacitance	Crss	-	70	_	pF	f=1MHz
Turn-on delay time	td (on) *	-	10	-	ns	ID=2.0A
Rise time	tr *	-	18	_	ns	$V_{DD} = 15V$
Turn-off delay time	td (off) *	-	37	_	ns	Vgs=4.5V Ri=7.5Ω
Fall time	t _f *	-	19	_	ns	R _G =10Ω
Total gate charge	Qg *	-	5.9	8.3	nC	V _{DD} ≒15V V _{GS} =4.5V
Gate-source charge	Q _{gs} *	_	1.0	-	nC	I _D =4.0A
Gate-drain charge	Q _{gd} *	-	2.0	-	nC	R∟=3.75Ω Rg=10Ω

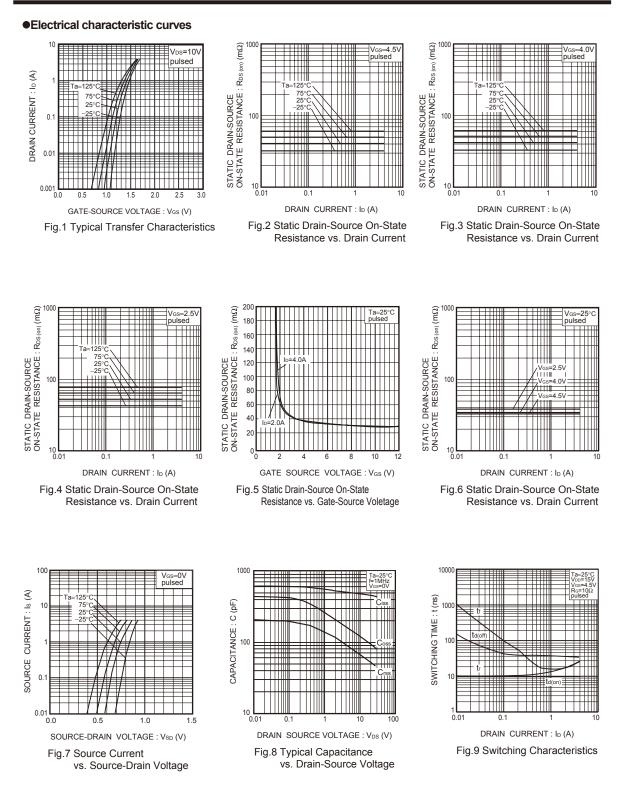
*Pulsed

•Body diode characteristics (Source-drain) (Ta=25°C)

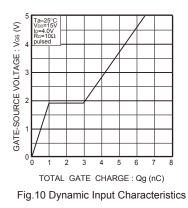
Parameter	Symbol	Min.	Тур.	Max.	Unit	Conditions
Forward voltage	Vsd	—	_	1.2	V	I _S =0.8A, V _{GS} =0V



Transistors



Transistors



Measurement circuits

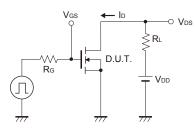


Fig.11 Switching Time Test Circuit

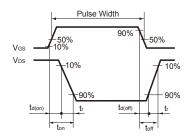


Fig.12 Switching Time Waveforms

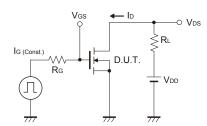


Fig.13 Gate Charge Test Circuit

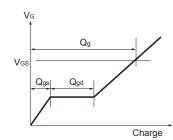


Fig.14 Gate Charge Waveform

ROHM

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(Note1) Medical Equipment Classification of the Spec	cific Applications
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JAPAN	USA	EU	CHINA
CLASSI	CLASSⅢ	CLASS II b	CLASSⅢ
CLASSIV	CLASSI	CLASSⅢ	CLASSI

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 - [b] Use of our Products outdoors or in places where the Products are exposed to direct sunlight or dust
 - [c] Use of our Products in places where the Products are exposed to sea wind or corrosive gases, including Cl₂, H₂S, NH₃, SO₂, and NO₂
 - [d] Use of our Products in places where the Products are exposed to static electricity or electromagnetic waves
 - [e] Use of our Products in proximity to heat-producing components, plastic cords, or other flammable items
 - [f] Sealing or coating our Products with resin or other coating materials
 - [g] Use of our Products without cleaning residue of flux (even if you use no-clean type fluxes, cleaning residue of flux is recommended); or Washing our Products by using water or water-soluble cleaning agents for cleaning residue after soldering
 - [h] Use of the Products in places subject to dew condensation
- 4. The Products are not subject to radiation-proof design.
- 5. Please verify and confirm characteristics of the final or mounted products in using the Products.
- 6. In particular, if a transient load (a large amount of load applied in a short period of time, such as pulse. is applied, confirmation of performance characteristics after on-board mounting is strongly recommended. Avoid applying power exceeding normal rated power; exceeding the power rating under steady-state loading condition may negatively affect product performance and reliability.
- 7. De-rate Power Dissipation (Pd) depending on Ambient temperature (Ta). When used in sealed area, confirm the actual ambient temperature.
- 8. Confirm that operation temperature is within the specified range described in the product specification.
- 9. ROHM shall not be in any way responsible or liable for failure induced under deviant condition from what is defined in this document.

Precaution for Mounting / Circuit board design

- 1. When a highly active halogenous (chlorine, bromine, etc.) flux is used, the residue of flux may negatively affect product performance and reliability.
- 2. In principle, the reflow soldering method must be used on a surface-mount products, the flow soldering method must be used on a through hole mount products. If the flow soldering method is preferred on a surface-mount products, please consult with the ROHM representative in advance.

For details, please refer to ROHM Mounting specification

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- 1. If change is made to the constant of an external circuit, please allow a sufficient margin considering variations of the characteristics of the Products and external components, including transient characteristics, as well as static characteristics.
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Precaution for Electrostatic

This Product is electrostatic sensitive product, which may be damaged due to electrostatic discharge. Please take proper caution in your manufacturing process and storage so that voltage exceeding the Products maximum rating will not be applied to Products. Please take special care under dry condition (e.g. Grounding of human body / equipment / solder iron, isolation from charged objects, setting of lonizer, friction prevention and temperature / humidity control).

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 - [b] the temperature or humidity exceeds those recommended by ROHM
 - [c] the Products are exposed to direct sunshine or condensation
 - [d] the Products are exposed to high Electrostatic
- 2. Even under ROHM recommended storage condition, solderability of products out of recommended storage time period may be degraded. It is strongly recommended to confirm solderability before using Products of which storage time is exceeding the recommended storage time period.
- 3. Store / transport cartons in the correct direction, which is indicated on a carton with a symbol. Otherwise bent leads may occur due to excessive stress applied when dropping of a carton.
- 4. Use Products within the specified time after opening a humidity barrier bag. Baking is required before using Products of which storage time is exceeding the recommended storage time period.

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QR code printed on ROHM Products label is for ROHM's internal use only.

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RTR040N03FRA - Web Page

Distribution Inventory

Part Number	RTR040N03FRA
Package	TSMT3
Unit Quantity	3000
Minimum Package Quantity	3000
Packing Type	Taping
Constitution Materials List	inquiry
RoHS	Yes

X-ON Electronics

Largest Supplier of Electrical and Electronic Components

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