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oducts	Overview Documentation D	esign Support
ficrocontrollers (MCU) and		
icroprocessors (MPU) SuperH Family	Product Spec Vorder Information	Buy 🛛 Package
SH7780 Series		
SH7724	Product Spec	
R8A77240D500BG		l CPU (Bit, Clock, DMA, External Bus etc) 🛛 🛛 Timers 💟 Interfaces 🖓 Display Functions
	Operating Conditions	
		Basic Information
	Product Status	Mass Production
	Family	SuperH
	Series	SH7780
	Group	SH7724
		Memory
	Programmemory	0 KB ROMess
	RAM	18 KB
	Cache Memory Remark	32K (Instruction), 32K (Data), 256K (L2 Cache), 4-w ay
		CPU (Bit, Clock, DMA, External Bus etc)
	CPU	SH-4A (32-bit)
	Max. Frequency	500 MHz
	RL	YES
	Real-Time Clock (RTC)	YES
	Power-On Reset	YES
	Memory Management Unit	YES
	Floating Point Unit	YES
	DMA Remarks	DMAC x 6ch x 2, 2D-DMAC x 8ch, SPU2 (DMAC x 3ch), EtherMAC(E-DMAC x 1ch)
	External Address/Data Bus	YES
	External Interrupt Pins	9
	VO Ports	180
		Timers
	16-bit Timers	4 ch
	32-bit Timers	7 ch
	Watchdog Timers	1 ch
	PWMOutput	4
		Interfaces
	Ethernet	YES, 10Mpps/100Mpps
	USB Ports	2
	USB Host	YES
	USB Peripheral	YES
	USB High-Speed Support	YES
		Low-speed Support (Host only)
	USB Remarks	Low-speed Support (Host only) 10 ch
	USB Remarks CSIs	10 ch
	USB Remarks	

Serial Interface Remarks	MSIOF (SCI:2ch/SPI:2ch), SCIF (SCI:3ch/UART:3ch), SCIFA (SCI:3ch/UART:3ch), IrDA (UART:1ch), FSI (SCI:2ch), I2C:2ch
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Display Functions	
High-resolution display controller	LODC :~SXGA, VOU : 720x240

Operating Conditions		
Operating Voltage	2.7 to 3.6 V	
Pow er Supply	VccQ=VccQ_SDC=2.7-3.6V, VccQ1=VccQ_LCD=VccQ_VIO=VccQ_MVC=VccQ_SR=1.65-1.95V/2.7- 3.6V, AVcc=DV33=AV33=3.0-3.6V, VDD=VDD_FLL=VDD_FLL=1.25-1.36V, DV12=AV12=UV12= 1.15-1.30V, VccQ_DDR=1.7-1.9V	
Operating Temperature	-40 to 85 (Ambient Temperature)	

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Order Information

Orderable Part No. 😧	Order	Date of PLP Termination	Production Status 😮	Packing
R8A77240D500BG#U0	Buy	Mar. 2025	Mass Production	Tray

Z : Product participating in the Product Longevity Program. More information on the Product Longevity Program

Buy : Product can be purchased online.

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Package	Chat OFFLINE				
Package					
Pins	449				
Pin pitch	0.8 mm				
Package Type	FBGA				
Body Size	21x21 mm ²				
Package drawing	🔀 prbg0449ga_a				
Mount pad drawing	☑ fig0013e				
Mounting height	1.9 mm				
Mass	1.4 g				
Renesas code	PRBG0449GA-A (old: BP-449V)				
JETA code	P-FBGA449-21x21-0.80				

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