

TOSHIBA Photocoupler GaAlAs Ired & Photo IC

# TLP2530, TLP2531

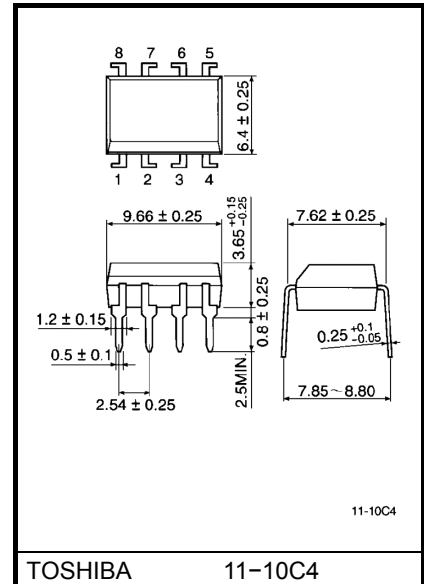
- Digital Logic Isolation
- Line Receiver
- Power Supply Control
- Switching Power Supply
- Transistor Inverter

The TOSHIBA TLP2530 and TLP2531 dual photocouplers consist of a pair of GaAlAs light emitting diode and integrated photodetector. This unit is 8-lead DIP.

Separate connection for the photodiode bias and output transistor collectors improve the speed up to a hundred times that of a conventional phototransistor coupler by reducing the base-collector capacitance.

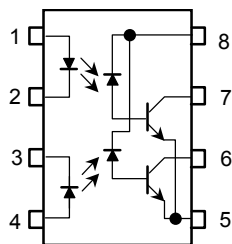
- TTL compatible
- Switching speed:  $t_{pHL}=0.3\mu s$ ,  $t_{pLH}=0.3\mu s$  (typ.)  
(@ $R_L=1.9k\Omega$ )
- Guaranteed performance over temp: 0~70°C
- Isolation voltage: 2500 Vrms (min)
- UL recognized: UL1577, file no. E67349

Unit: mm



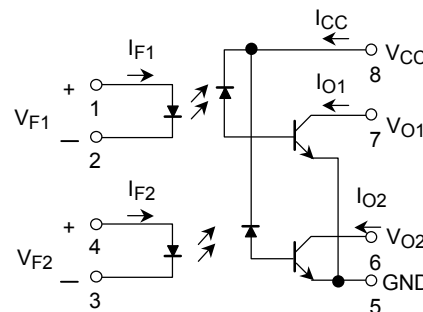
Weight: 0.54 g (typ.)

## Pin Configuration (top view)



- 1. : Anode.1
- 2. : Cathode.1
- 3. : Cathode.2
- 4. : Anode.2
- 5. : Gnd
- 6. :  $V_{O2}$ (output 2)
- 7. :  $V_{O1}$ (output 1)
- 8. :  $V_{CC}$

## Schematic



Start of commercial production  
1986/03

**Absolute Maximum Ratings**

Characteristic		Symbol	Rating	Unit
LED	Forward current (each channel) (Note 1)	$I_F$	25	mA
	Pulse forward current (each channel) (Note 2)	$I_{FP}$	50	mA
	Total pulse forward current (each channel) (Note 3)	$I_{FPT}$	1	A
	Reverse voltage (each channel)	$V_R$	5	V
	Diode power dissipation (each channel) (Note 4)	$P_D$	45	mW
Detector	Output current(each channel)	$I_O$	8	mA
	Peak output current (each channel)	$I_{OP}$	16	mA
	Supply voltage	$V_{CC}$	-0.5 to 15	V
	Output voltage(each channel)	$V_O$	-0.5 to 15	V
	Output power dissipation (each channel) (Note 5)	$P_O$	35	mW
Operating temperature range		$T_{opr}$	-55 to 100	°C
Storage temperature range		$T_{stg}$	-55 to 125	°C
Lead solder temperature(10s)**		$T_{sol}$	260	°C
Isolation voltage (AC, 1 minute, R.H. ≤ 60%) (Note 7)		$BV_S$	2500	Vrms

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook (“Handling Precautions”/“Derating Concept and Methods”) and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

(Note 1) Derate 0.8mA above 70°C.

(Note 2) 50% duty cycle, 1ms pulse width. Derate 1.6mA / °C above 70°C.

(Note 3) Pulse width 1µs, 300pps.

(Note 4) Derate 0.9mW / °C above 70°C.

(Note 5) Derate 1mW / °C above 70°C.

\*\*2mm below seating plane.

**Recommended Operating Conditions**

Characteristic	Symbol	Min	Typ.	Max	Unit
Supply voltage	$V_{CC}$	0	—	12	V
Forward current, each channel	$I_F$	—	16	25	mA
Operating temperature	$T_{opr}$	-25	—	85	°C

Note: Recommended operating conditions are given as a design guideline to obtain expected performance of the device. Additionally, each item is an independent guideline respectively. In developing designs using this product, please confirm specified characteristics shown in this document.

## Electrical Characteristics Over Recommended Temperature (Ta = 0°C~70°C, unless otherwise noted)

Characteristic		Symbol	Test Condition	Min	Typ.**	Max	Unit
Current transfer ratio (each channel)	TLP2530	CTR	I <sub>F</sub> = 16mA, V <sub>O</sub> = 0.4V V <sub>CC</sub> = 4.5V, Ta = 25°C (Note 6)	7	30	—	%
	TLP2531			19	30	—	
	TLP2530	CTR	I <sub>F</sub> = 16mA, V <sub>O</sub> = 0.5V V <sub>CC</sub> = 4.5V (Note 6)	5	—	—	%
	TLP2531			15	—	—	
Logic low output voltage (each channel)	TLP2530	V <sub>OL</sub>	I <sub>F</sub> = 16mA, I <sub>O</sub> = 1.1mA V <sub>CC</sub> = 4.5V	—	0.1	0.4	V
	TLP2531			I <sub>F</sub> = 16mA, I <sub>O</sub> = 2.4mA V <sub>CC</sub> = 4.5V	—	0.1	0.4
Logic high output current (each channel)		I <sub>OH</sub>	I <sub>F</sub> = 0mA, V <sub>O</sub> = V <sub>CC</sub> = 5.5V Ta = 25°C	—	3	500	nA
			I <sub>F</sub> = 0mA, V <sub>O</sub> = V <sub>CC</sub> = 15V	—	—	50	μA
Logic low supply current		I <sub>CCL</sub>	I <sub>F1</sub> = I <sub>F2</sub> = 16mA V <sub>O1</sub> = V <sub>O2</sub> = Open V <sub>CC</sub> = 15V	—	160	—	μA
Logic high supply current		I <sub>CCH</sub>	I <sub>F1</sub> = I <sub>F2</sub> = 0mA V <sub>O1</sub> = V <sub>O2</sub> = Open V <sub>CC</sub> = 15V	—	0.05	4	μA
Input forward voltage (each channel)		V <sub>F</sub>	I <sub>F</sub> = 16mA, Ta = 25°C	—	1.65	1.7	V
Temperature coefficient of forward voltage (each channel)		ΔV <sub>F</sub> / ΔTa	I <sub>F</sub> = 16mA	—	-2	—	mV/°C
Input reverse breakdown voltage (each channel)		BV <sub>R</sub>	I <sub>R</sub> = 10μA, Ta = 25°C	5	—	—	V
Input capacitance (each channel)		C <sub>IN</sub>	f = 1MHz, V <sub>F</sub> = 0	—	60	—	pF
Input-output insulation leakage current		I <sub>I-O</sub>	Relative humidity = 45% t = 5s, V <sub>I-O</sub> = 3000V <sub>dc</sub> Ta = 25°C (Note 7)	—	—	1.0	μA
Resistance (input-output)		R <sub>I-O</sub>	V <sub>I-O</sub> = 500V <sub>dc</sub> (Note 7)	—	10 <sup>12</sup>	—	Ω
Capacitance (input-output)		C <sub>I-O</sub>	f = 1MHz (Note 7)	—	0.6	—	pF
Input-input leakage current		I <sub>I-I</sub>	Relative humidity = 45% t = 5s, V <sub>I-I</sub> = 500V (Note 8)	—	0.005	—	μA
Resistance (input-input)		R <sub>I-I</sub>	V <sub>I-I</sub> = 500V <sub>dc</sub> (Note 8)	—	10 <sup>11</sup>	—	Ω
Capacitance (input-input)		C <sub>I-I</sub>	f = 1MHz (Note 8)	—	0.25	—	pF

\*\*All typicals at Ta = 25°C.

(Note 6) DC current transfer ratio is defined as the ratio of output collector current, I<sub>O</sub>, to the forward LED input current, I<sub>F</sub>, times 100%.

(Note 7) Device considered a two-terminal device: Pins 1, 2, 3 and 4 shorted together and pins 5, 6, 7 and 8 shorted together.

(Note 8) Measured between pins 1 and 2 shorted together, and pins 3 and 4 shorted together.

## Switching Characteristics (unless otherwise specified, $T_a = 25^\circ\text{C}$ , $V_{CC} = 5\text{V}$ , $I_F = 16\text{mA}$ )

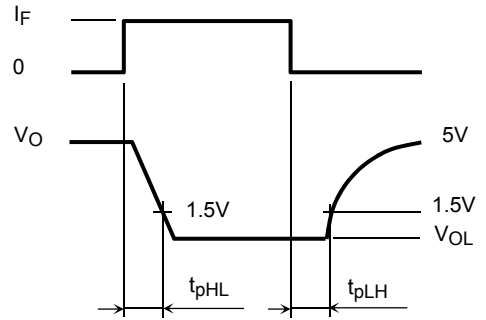
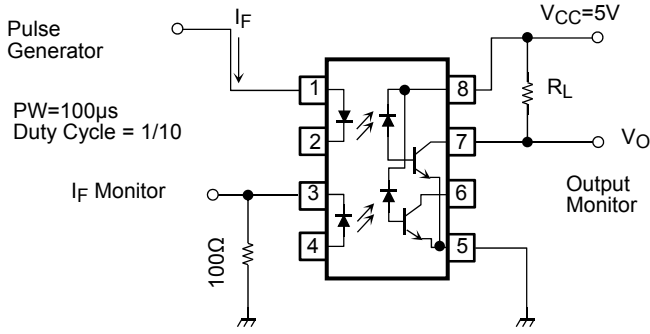
Characteristic		Symbol	Test Circuit	Test Condition	Min.	Typ.	Max.	Unit
Propagation delay time to logic low at output (each channel)	TLP2530	$t_{pHL}$	1	$R_L = 4.1\text{k}\Omega$	—	0.3	1.5	$\mu\text{s}$
	TLP2531			$R_L = 1.9\text{k}\Omega$	—	0.2	0.8	
Propagation delay time to logic high at output (each channel)	TLP2530	$t_{pLH}$	1	$R_L = 4.1\text{k}\Omega$	—	0.5	1.5	$\mu\text{s}$
	TLP2531			$R_L = 1.9\text{k}\Omega$	—	0.3	0.8	
Common mode transient immunity at logic high level output (each channel, Note 9)	TLP2530	$CM_H$	2	$I_F = 0\text{mA}$ , $V_{CM} = 400\text{V}_{p-p}$ $R_L = 4.1\text{k}\Omega$	—	1500	—	$\text{V} / \mu\text{s}$
	TLP2531			$I_F = 0\text{mA}$ , $V_{CM} = 400\text{V}_{p-p}$ $R_L = 1.9\text{k}\Omega$	—	1500	—	
Common mode transient immunity at logic low level output (each channel, Note 9)	TLP2530	$CM_L$	2	$V_{CM} = 400\text{V}_{p-p}$ $R_L = 4.1\text{k}\Omega$ , $I_F = 16\text{mA}$	—	-1500	—	$\text{V} / \mu\text{s}$
	TLP2531			$V_{CM} = 400\text{V}_{p-p}$ $R_L = 1.9\text{k}\Omega$ , $I_F = 16\text{mA}$	—	-1500	—	
Bandwidth (each channel, Note 10)		BW	3	$R_L = 100\Omega$	—	2	—	MHz

(Note 9) Common mode transient immunity in logic high level is the maximum tolerable (positive)  $dV_{cm} / dt$  on the leading edge of the common mode pulse,  $V_{cm}$ , to assure that the output will remain in a logic high state (i.e.,  $V_O > 2.0\text{V}$ ).

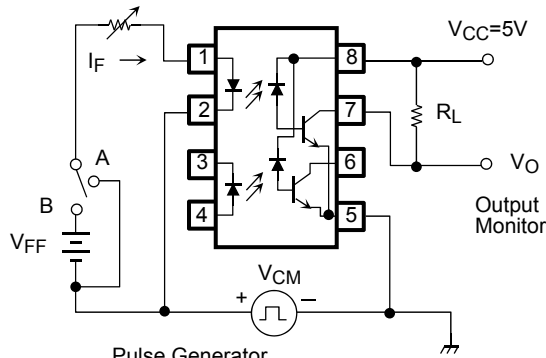
Common mode transient immunity in logic low level is the maximum tolerable (negative)  $dV_{cm} / dt$  on the trailing edge of the common mode pulse signal,  $V_{cm}$ , to assure that the output will remain in logic low state (i.e.,  $V_O < 0.8\text{V}$ ).

(Note 10) The frequency at which the ac output voltage is 3dB below the low frequency asymptote.

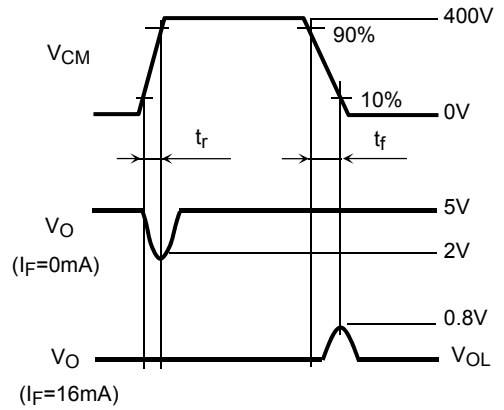
**Test Circuit 1: Switching Time,  $t_{pHL}$ ,  $t_{pLH}$**



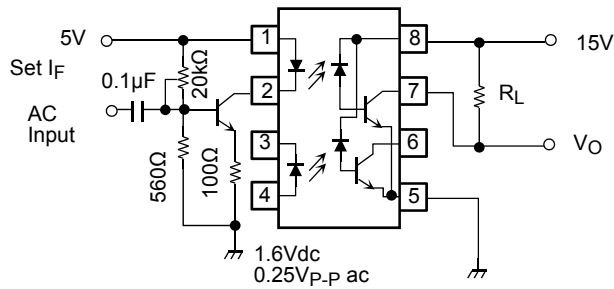
**Test Circuit 2: Transient Immunity and Typical Waveform**

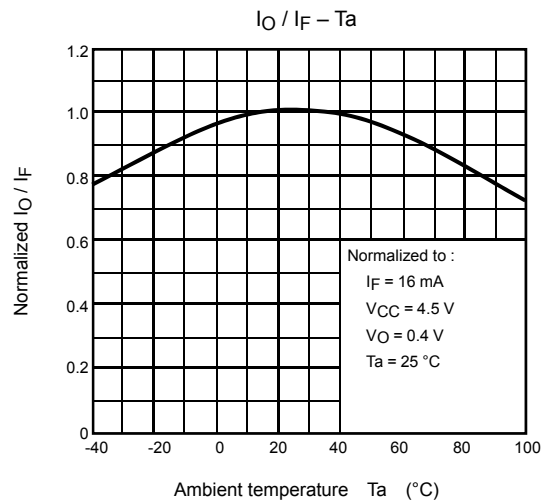
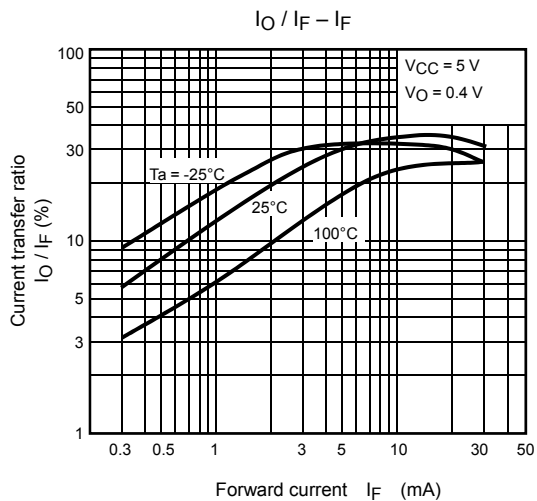
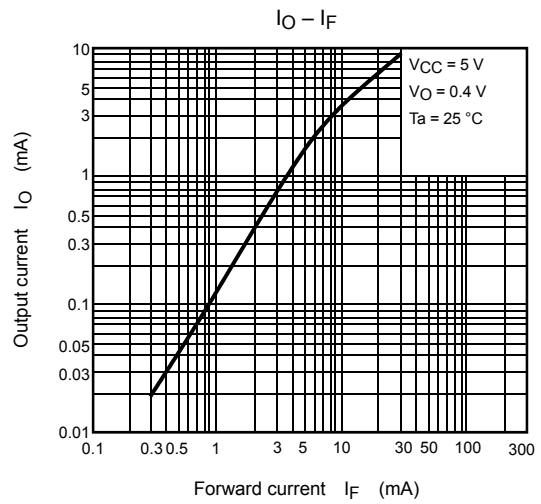
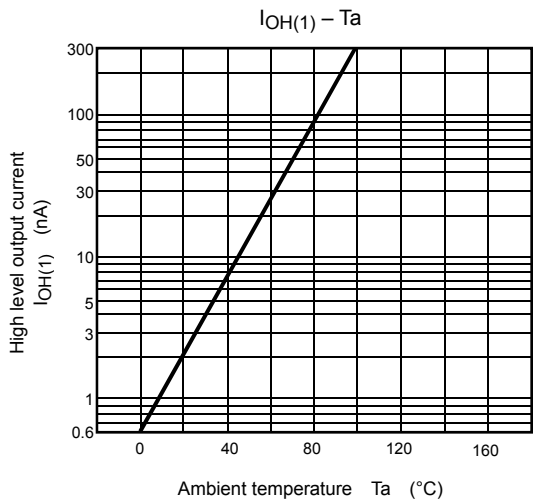
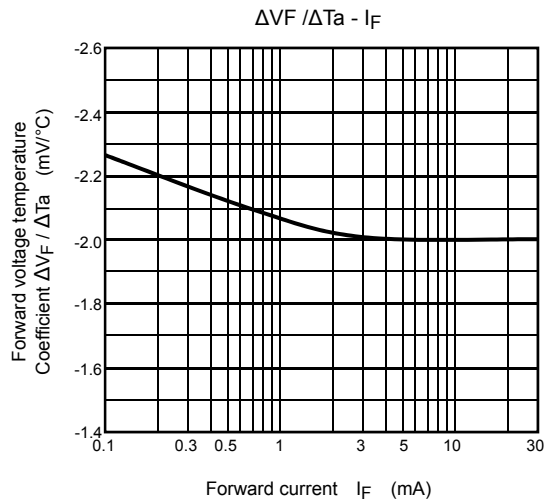
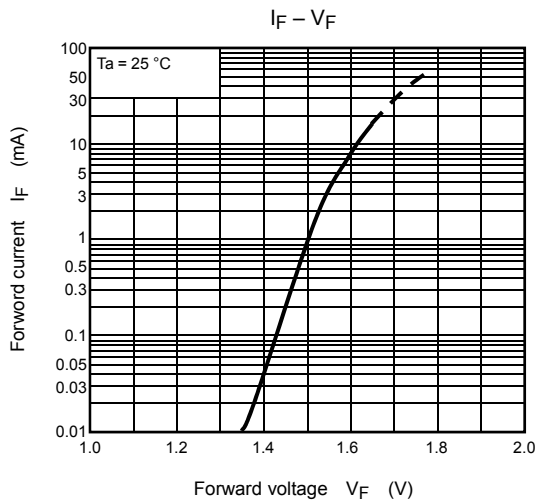


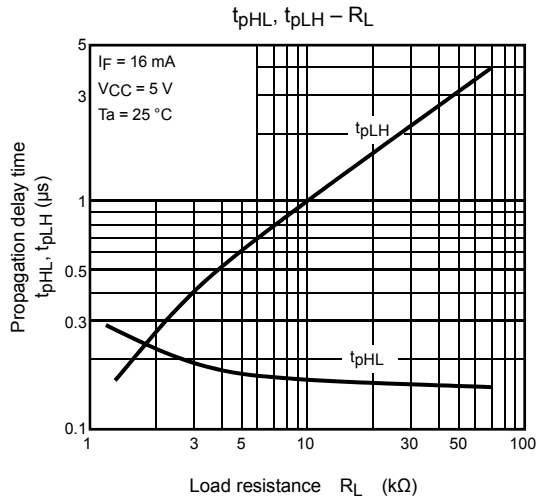
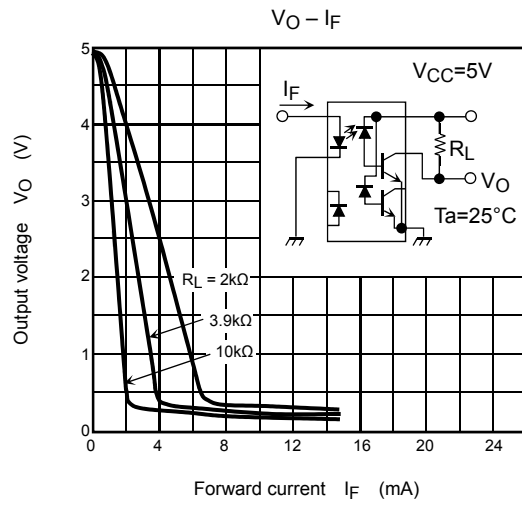
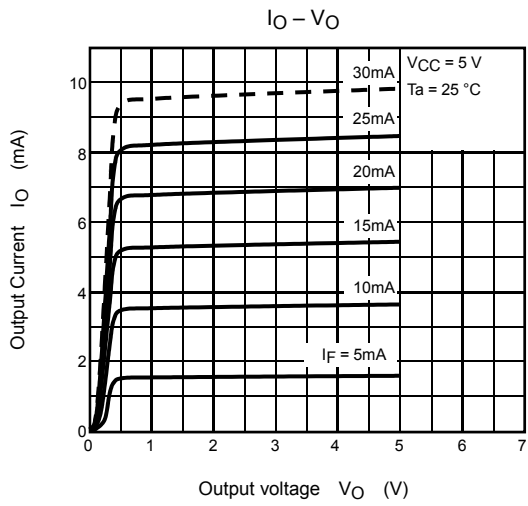
Pulse Generator  
 $Z_0 = 50\Omega$   
 $CM_H = \frac{320(V)}{t_r(\mu s)}$ ,  $CM_L = \frac{320(V)}{t_f(\mu s)}$



**Test Circuit 3: Frequency Response**







**RESTRICTIONS ON PRODUCT USE**

- Toshiba Corporation, and its subsidiaries and affiliates (collectively "TOSHIBA"), reserve the right to make changes to the information in this document, and related hardware, software and systems (collectively "Product") without notice.
- This document and any information herein may not be reproduced without prior written permission from TOSHIBA. Even with TOSHIBA's written permission, reproduction is permissible only if reproduction is without alteration/omission.
- Though TOSHIBA works continually to improve Product's quality and reliability, Product can malfunction or fail. Customers are responsible for complying with safety standards and for providing adequate designs and safeguards for their hardware, software and systems which minimize risk and avoid situations in which a malfunction or failure of Product could cause loss of human life, bodily injury or damage to property, including data loss or corruption. Before creating and producing designs and using, customers must also refer to and comply with (a) the latest versions of all relevant TOSHIBA information, including without limitation, this document, the specifications, the data sheets and application notes for Product and the precautions and conditions set forth in the "TOSHIBA Semiconductor Reliability Handbook" and (b) the instructions for the application that Product will be used with or for. Customers are solely responsible for all aspects of their own product design or applications, including but not limited to (a) determining the appropriateness of the use of this Product in such design or applications; (b) evaluating and determining the applicability of any information contained in this document, or in charts, diagrams, programs, algorithms, sample application circuits, or any other referenced documents; and (c) validating all operating parameters for such designs and applications. **TOSHIBA ASSUMES NO LIABILITY FOR CUSTOMERS' PRODUCT DESIGN OR APPLICATIONS.**
- Product is intended for use in general electronics applications (e.g., computers, personal equipment, office equipment, measuring equipment, industrial robots and home electronics appliances) or for specific applications as expressly stated in this document. Product is neither intended nor warranted for use in equipment or systems that require extraordinarily high levels of quality and/or reliability and/or a malfunction or failure of which may cause loss of human life, bodily injury, serious property damage or serious public impact ("Unintended Use"). Unintended Use includes, without limitation, equipment used in nuclear facilities, equipment used in the aerospace industry, medical equipment, equipment used for automobiles, trains, ships and other transportation, traffic signaling equipment, equipment used to control combustions or explosions, safety devices, elevators and escalators, devices related to electric power, and equipment used in finance-related fields. Do not use Product for Unintended Use unless specifically permitted in this document.
- Do not disassemble, analyze, reverse-engineer, alter, modify, translate or copy Product, whether in whole or in part.
- Product shall not be used for or incorporated into any products or systems whose manufacture, use, or sale is prohibited under any applicable laws or regulations.
- The information contained herein is presented only as guidance for Product use. No responsibility is assumed by TOSHIBA for any infringement of patents or any other intellectual property rights of third parties that may result from the use of Product. No license to any intellectual property right is granted by this document, whether express or implied, by estoppel or otherwise.
- **ABSENT A WRITTEN SIGNED AGREEMENT, EXCEPT AS PROVIDED IN THE RELEVANT TERMS AND CONDITIONS OF SALE FOR PRODUCT, AND TO THE MAXIMUM EXTENT ALLOWABLE BY LAW, TOSHIBA (1) ASSUMES NO LIABILITY WHATSOEVER, INCLUDING WITHOUT LIMITATION, INDIRECT, CONSEQUENTIAL, SPECIAL, OR INCIDENTAL DAMAGES OR LOSS, INCLUDING WITHOUT LIMITATION, LOSS OF PROFITS, LOSS OF OPPORTUNITIES, BUSINESS INTERRUPTION AND LOSS OF DATA, AND (2) DISCLAIMS ANY AND ALL EXPRESS OR IMPLIED WARRANTIES AND CONDITIONS RELATED TO SALE, USE OF PRODUCT, OR INFORMATION, INCLUDING WARRANTIES OR CONDITIONS OF MERCHANTABILITY, FITNESS FOR A PARTICULAR PURPOSE, ACCURACY OF INFORMATION, OR NONINFRINGEMENT.**
- GaAs (Gallium Arsenide) is used in Product. GaAs is harmful to humans if consumed or absorbed, whether in the form of dust or vapor. Handle with care and do not break, cut, crush, grind, dissolve chemically or otherwise expose GaAs in Product.
- Do not use or otherwise make available Product or related software or technology for any military purposes, including without limitation, for the design, development, use, stockpiling or manufacturing of nuclear, chemical, or biological weapons or missile technology products (mass destruction weapons). Product and related software and technology may be controlled under the Japanese Foreign Exchange and Foreign Trade Law and the U.S. Export Administration Regulations. Export and re-export of Product or related software or technology are strictly prohibited except in compliance with all applicable export laws and regulations.
- Please contact your TOSHIBA sales representative for details as to environmental matters such as the RoHS compatibility of Product. Please use Product in compliance with all applicable laws and regulations that regulate the inclusion or use of controlled substances, including without limitation, the EU RoHS Directive. TOSHIBA assumes no liability for damages or losses occurring as a result of noncompliance with applicable laws and regulations.



## X-ON Electronics

Largest Supplier of Electrical and Electronic Components

*Click to view similar products for [High Speed Optocouplers](#) category:*

*Click to view products by [Toshiba](#) manufacturer:*

Other Similar products are found below :

[PS8502L2-AX](#) [ACNW261L-000E](#) [ACPL-344JT-000E](#) [ACPL-K49T-500E](#) [ACPL-K74T-000E](#) [ACPL-K75T-000E](#) [ACPL-W21L-560E](#) [ACPL-K44T-500E](#) [TLP187\(TPL,E\(T](#) [TLP2601\(TP1,F\)](#) [610737H](#) [6N137A-X001](#) [6N137A-X017T](#) [6N139-X007T](#) [HCPL2630M](#) [HCPL2731SM](#) [TLP555\(F\)](#) [HCPL2630SM](#) [PS2841-4A-F3-AX](#) [PS9817A-1-F3-AX](#) [PS9821-2-F3-AX](#) [ORPC-817D](#) [ORPC-817M/C](#) [ORPC-817M/B](#) [PT17-51C/L129\(BIN2\)](#) [TLP521-4GBSM](#) [UMW817C](#) [6N137S1\(TA\)](#) [TLP521GB](#) [TLP521GB-S](#) [PS2501](#) [PS2501-S](#) [TLP785GB](#) [TLP785GB-S](#) [LTV-214-G](#) [TLP2766A\(E](#) [TLP2766A\(LF4,E](#) [LCR-0202](#) [EL814S1\(TA\)-V](#) [PC817X4NSZ2B](#) [CYPC817](#) [OR-MOC3023](#) [TLP267J\(TPL,E\(T](#) [TLP109\(TPL,E\(O](#) [EL2514S1\(TU\)\(CLW\)-G](#) [EL816S2\(C\)\(TU\)-F](#) [TLP281-4](#) [MOC3023M](#) [ACPL-K49T-060E](#) [ACPL-K75T-500E](#)