2N5460 2N5461 2N5462

SILICON P-CHANNEL JFETS



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The CENTRAL SEMICONDUCTOR 2N5460, 2N5461, and 2N5462 are silicon P-Channel JFETs designed for low level amplifier applications.



MARKING: FULL PART NUMBER

MAXIMUM RATINGS: (TA=25°C)	SYMBOL		UNITS
Drain-Gate Voltage	V_{DG}	40	V
Reverse Gate-Source Voltage	V_{GSR}	40	V
Continuous Gate Current	I_{G}	10	mA
Power Dissipation	P_{D}	310	mW
Operating and Storage Junction Temperature	T _J , T _{stq}	-65 to +150	°C

ELECTRICAL CHARACTERISTICS: (TA=25°C unless otherwise noted)

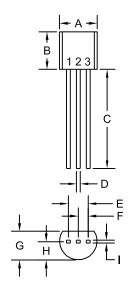
		2N:	<u>5460</u>	2N:	<u>5461</u>	<u>2N5</u>	<u> 462</u>	
SYMBOL	TEST CONDITIONS	MIN	MAX	MIN	MAX	MIN	MAX	UNITS
I _{GSS}	V _{GS} =20V	-	5.0	-	5.0	-	5.0	nA
I _{GSS}	V _{GS} =20V, T _A =100°C	-	1.0	-	1.0	-	1.0	μΑ
IDSS	V _{DS} =15V, f=1.0kHz	1.0	5.0	2.0	9.0	4.0	16	mA
BV_{GSS}	I _G =10μA	40	-	40	-	40	-	V
V_{GS}	V_{DS} =15V, I_D =0.1mA	0.5	4.0	-	-	-	-	V
V_{GS}	V_{DS} =15V, I_D =0.2mA	-	-	8.0	4.5	-	-	V
V_{GS}	V_{DS} =15V, I_D =0.4mA	-	-	-	-	1.5	6.0	V
V _{GS(OFF)}	V_{DS} =15V, I_{D} =1.0 μ A	0.75	6.0	1.0	7.5	1.8	9.0	V
y _{fs}	V_{DS} =15V, V_{GS} =0, f=1.0kHz	1.0K	4.0K	1.5K	5.0K	2.0K	6.0K	μS
$ y_{os} $	V_{DS} =15V, V_{GS} =0, f=1.0kHz	-	75	-	75	-	75	μS
C _{rss}	V_{DS} =15V, V_{GS} =0, f=1.0MHz	-	2.0	-	2.0	-	2.0	pF
C _{iss}	V_{DS} =15V, V_{GS} =0, f=1.0MHz	-	7.0	-	7.0	-	7.0	pF
e_N	V_{DS} =15V, V_{GS} =0, f=100Hz, BW=1.0Hz	-	115	-	115	-	115	nV/√Hz
NF	V_{DS} =15V, V_{GS} =0, f=100Hz,							
	R_G =1.0M Ω , BW=1.0Hz	-	2.5	-	2.5	-	2.5	dB

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TO-92 CASE - MECHANICAL OUTLINE



DIMENSIONS						
	INCHES		MILLIMETERS			
SYMBOL	MIN	MAX	MIN	MAX		
A (DIA)	0.175	0.205	4.45	5.21		
В	0.170	0.210	4.32	5.33		
С	0.500	_	12.70	-		
D	0.016	0.022	0.41	0.56		
Е	0.100		2.54			
F	0.050		1.27			
G	0.125	0.165	3.18	4.19		
Н	0.080	0.105	2.03	2.67		
	0.015		0.38			

TO-92 (REV: R1)

LEAD CODE:

- 1) Drain 2) Source
- 3) Gate

R1

MARKING:

FULL PART NUMBER



OUTSTANDING SUPPORT AND SUPERIOR SERVICES

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Central's operations team provides the highest level of support to insure product is delivered on-time.

- Supply management (Customer portals)
- · Inventory bonding
- · Consolidated shipping options

- Custom bar coding for shipments
- · Custom product packing

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Central's applications engineering team is ready to discuss your design challenges. Just ask.

- Free quick ship samples (2nd day air)
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- SPICE models
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- · Environmental regulation compliance
- · Customer specific screening
- · Up-screening capabilities

- Special wafer diffusions
- · PbSn plating options
- · Package details
- · Application notes
- · Application and design sample kits
- Custom product and package development

REQUESTING PRODUCT PLATING

- 1. If requesting Tin/Lead plated devices, add the suffix "TIN/LEAD" to the part number when ordering (example: 2N2222A TIN/LEAD).
- 2. If requesting Lead (Pb) Free plated devices, add the suffix "PBFREE" to the part number when ordering (example: 2N2222A PBFREE).

CONTACT US

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