DISCRETE SEMICONDUCTORS

DATA SHEET

BFR93ANPN 6 GHz wideband transistor

Product specification Supersedes data of September 1995



NPN 6 GHz wideband transistor

BFR93A

FEATURES

• High power gain

• Low noise figure

• Very low intermodulation distortion.

DESCRIPTION

NPN wideband transistor in a plastic SOT23 package.

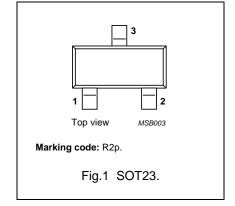
PNP complement: BFT93.

APPLICATIONS

RF wideband amplifiers and oscillators.

PINNING

PIN	DESCRIPTION	
1	base	
2	emitter	
3	collector	



QUICK REFERENCE DATA

SYMBOL	PARAMETER	CONDITIONS	TYP.	MAX.	UNIT
V _{CBO}	collector-base voltage	open emitter	_	15	V
V _{CEO}	collector-emitter voltage	open base	_	12	V
I _C	collector current (DC)		_	35	mA
P _{tot}	total power dissipation	T _s ≤ 95 °C	_	300	mW
C _{re}	feedback capacitance	I _C = 0; V _{CE} = 5 V; f = 1 MHz	0.6	_	pF
f _T	transition frequency	$I_C = 30 \text{ mA}; V_{CE} = 5 \text{ V}; f = 500 \text{ MHz}$	6	_	GHz
G _{UM}	maximum unilateral power gain	I_C = 30 mA; V_{CE} = 8 V; f = 1 GHz; T_{amb} = 25 °C	13	_	dB
		$I_C = 30 \text{ mA}; V_{CE} = 8 \text{ V}; f = 2 \text{ GHz}; T_{amb} = 25 ^{\circ}\text{C}$	7	_	dB
F	noise figure	I_C = 5 mA; V_{CE} = 8 V; f = 1 GHz; Γ_s = Γ_{opt} ; Γ_{amb} = 25 °C	1.9	_	dB
Vo	output voltage	$d_{im} = -60 \text{ dB}; I_C = 30 \text{ mA}; V_{CE} = 8 \text{ V};$ $R_L = 75 \Omega; T_{amb} = 25 \text{ °C};$ $f_p + f_q - f_r = 793.25 \text{ MHz}$	425	_	mV

LIMITING VALUES

In accordance with the Absolute Maximum Rating System (IEC 134).

SYMBOL	PARAMETER	CONDITIONS	MIN.	MAX.	UNIT
V_{CBO}	collector-base voltage	open emitter	_	15	V
V_{CEO}	collector-emitter voltage	open base	_	12	V
V _{EBO}	emitter-base voltage	open collector	_	2	V
I _C	collector current (DC)		_	35	mA
P _{tot}	total power dissipation	T _s ≤ 95 °C; note 1	_	300	mW
T _{stg}	storage temperature		-65	+150	°C
T _j	junction temperature		_	+175	°C

Note

1. T_{S} is the temperature at the soldering point of the collector pin.

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THERMAL CHARACTERISTICS

SYMBOL	PARAMETER	CONDITIONS	VALUE	UNIT
R _{th j-s}	thermal resistance from junction to soldering point	T _s ≤ 95 °C; note 1	260	K/W

Note

1. T_s is the temperature at the soldering point of the collector pin.

CHARACTERISTICS

 $T_i = 25$ °C unless otherwise specified.

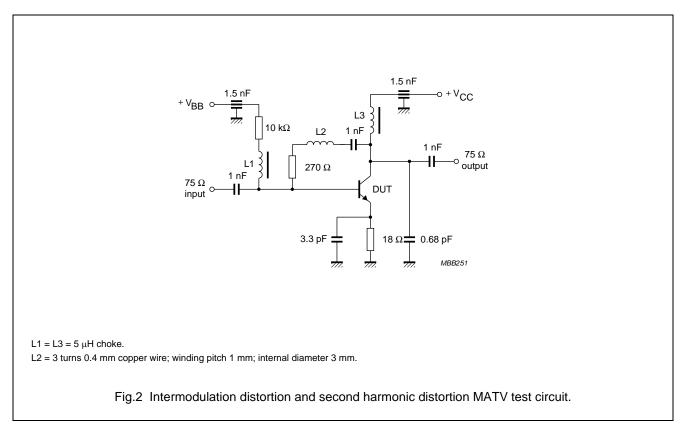
SYMBOL	PARAMETER	CONDITIONS	MIN.	TYP.	MAX.	UNIT
I _{CBO}	collector cut-off current	I _E = 0; V _{CB} = 5 V	_	_	50	nA
h _{FE}	DC current gain	$I_C = 30 \text{ mA}; V_{CE} = 5 \text{ V}$	40	90	-	
C _c	collector capacitance	I _E = i _e = 0; V _{CB} = 5 V; f = 1 MHz	_	0.7	_	pF
C _e	emitter capacitance	$I_C = I_c = 0$; $V_{EB} = 0.5 \text{ V}$; $f = 1 \text{ MHz}$	_	1.9	_	pF
C _{re}	feedback capacitance	$I_C = I_c = 0$; $V_{CE} = 5$ V; $f = 1$ MHz; $T_{amb} = 25$ °C	-	0.6	_	pF
f _T	transition frequency	I _C = 30 mA; V _{CE} = 5 V; f = 500 MHz	4.5	6	_	GHz
G _{UM}	maximum unilateral power gain (note 1)	$I_C = 30 \text{ mA}; V_{CE} = 8 \text{ V}; f = 1 \text{ GHz};$ $T_{amb} = 25 \text{ °C}$	_	13	_	dB
		$I_C = 30 \text{ mA}; V_{CE} = 8 \text{ V}; f = 2 \text{ GHz};$ $T_{amb} = 25 ^{\circ}\text{C}$	_	7	_	dB
F	noise figure (note 2)	$I_C = 5 \text{ mA}; V_{CE} = 8 \text{ V}; f = 1 \text{ GHz};$ $\Gamma_S = \Gamma_{\text{opt}}; T_{\text{amb}} = 25 ^{\circ}\text{C}$	_	1.9	_	dB
		$I_C = 5 \text{ mA}; V_{CE} = 8 \text{ V}; f = 2 \text{ GHz};$ $\Gamma_S = \Gamma_{opt}; T_{amb} = 25 \text{ °C}$	_	3	_	dB
Vo	output voltage	notes 2 and 3	_	425	_	mV
d ₂	second order intermodulation distortion	notes 2 and 4	-	-50	_	dB

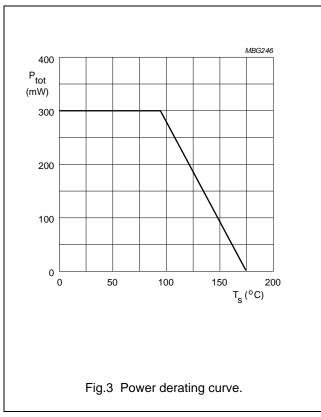
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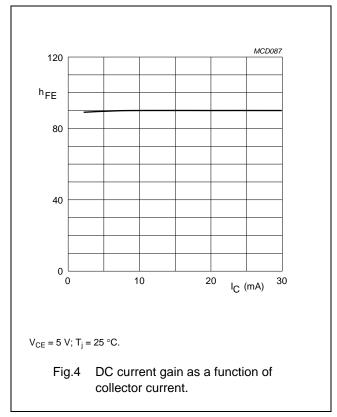
- 1. G_{UM} is the maximum unilateral power gain, assuming S_{12} is zero and $G_{UM} = 10 \log \frac{\left|S_{21}\right|^2}{(1-\left|S_{11}\right|^2)(1-\left|S_{22}\right|^2)} dB$.
- 2. Measured on the same die in a SOT37 package (BFR91A).
- 3. $\begin{aligned} &d_{im} = -60 \text{ dB (DIN 45004B)}; \ I_C = 30 \text{ mA; V}_{CE} = 8 \text{ V; R}_L = 75 \ \Omega; \ T_{amb} = 25 \ ^{\circ}\text{C}; \\ &V_p = V_O \text{ at } d_{im} = -60 \text{ dB; f}_p = 795.25 \text{ MHz;} \\ &V_q = V_O 6 \text{ dB at f}_q = 803.25 \text{ MHz;} \\ &V_r = V_O 6 \text{ dB at f}_r = 805.25 \text{ MHz;} \\ &\text{measured at f}_p + f_q f_r = 793.25 \text{ MHz.} \end{aligned}$
- 4. I_C = 30 mA; V_{CE} = 8 V; R_L = 75 Ω ; T_{amb} = 25 °C; V_p = 200 mV at f_p = 250 MHz; V_q = 200 mV at f_q = 560 MHz; measured at f_p + f_q = 810 MHz.

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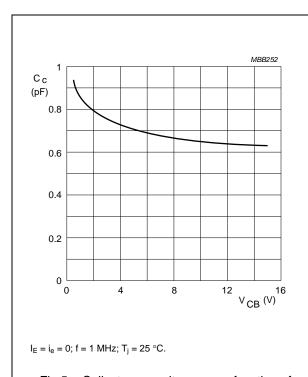
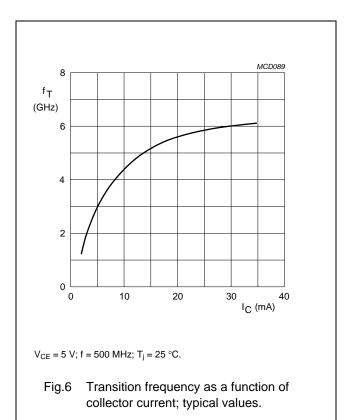
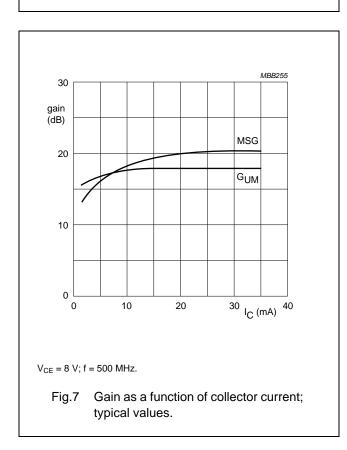
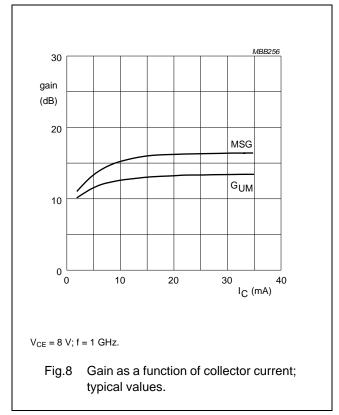


Fig.5 Collector capacitance as a function of collector-base voltage; typical values.

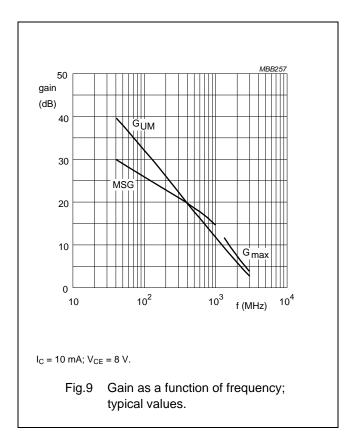


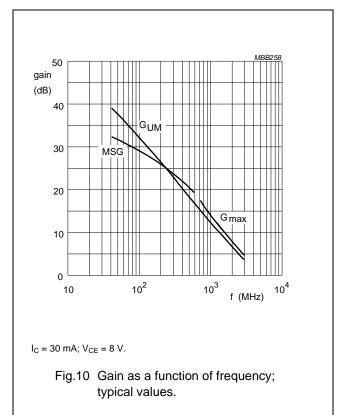


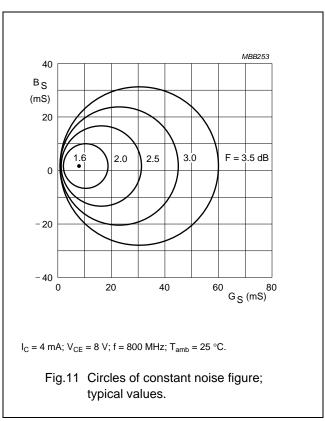


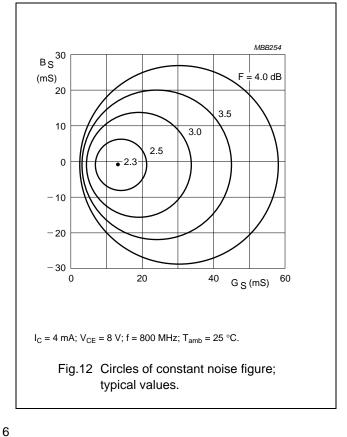
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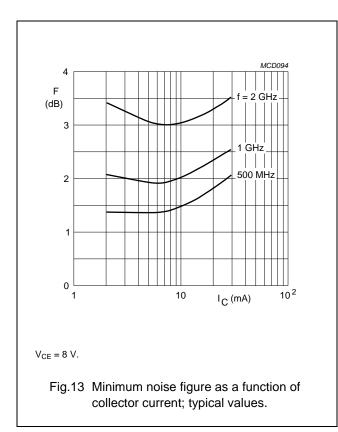


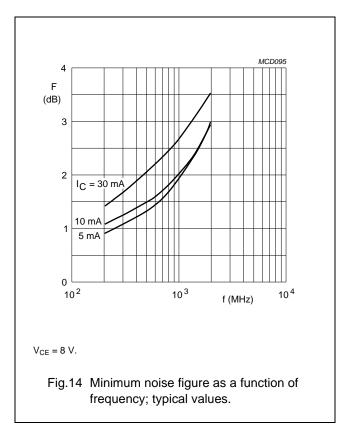


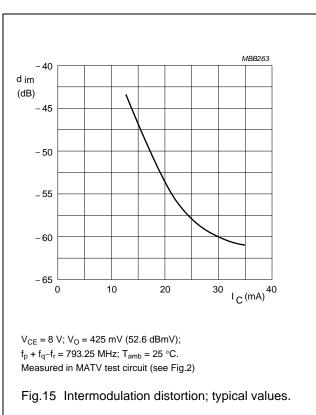


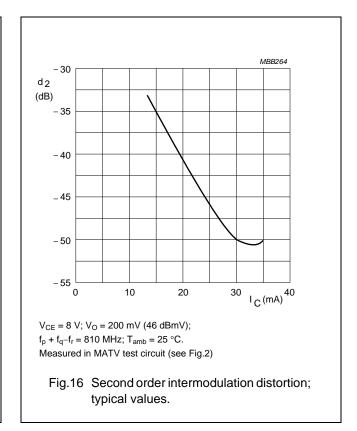
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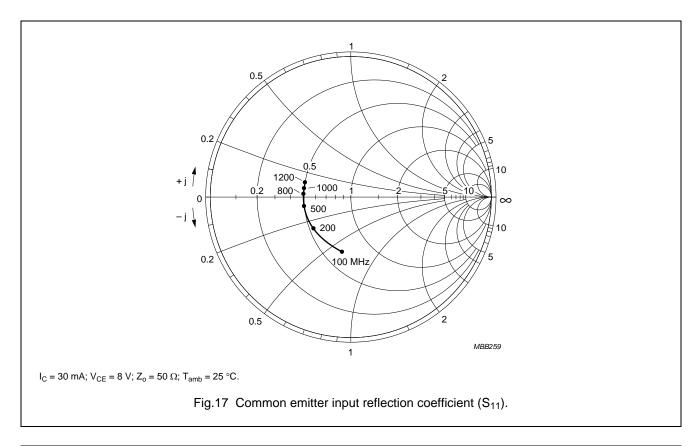


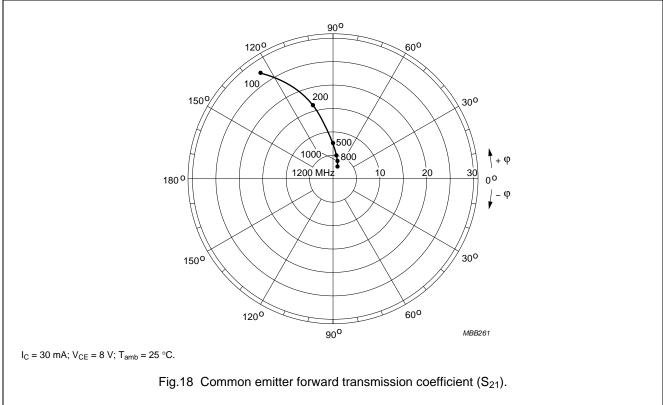




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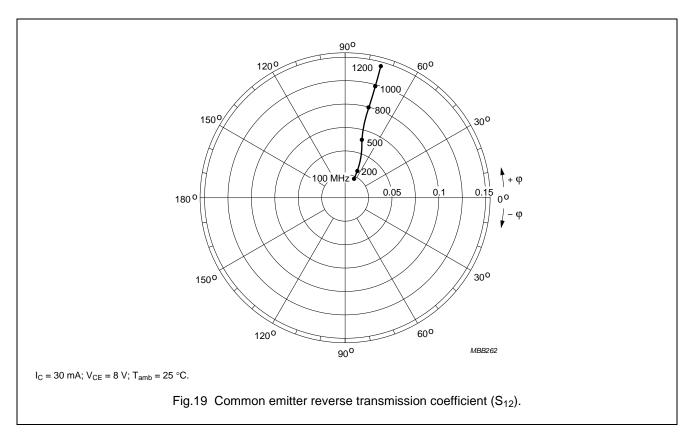
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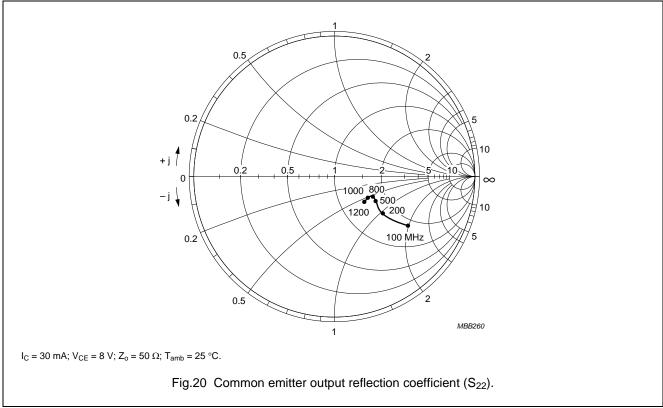




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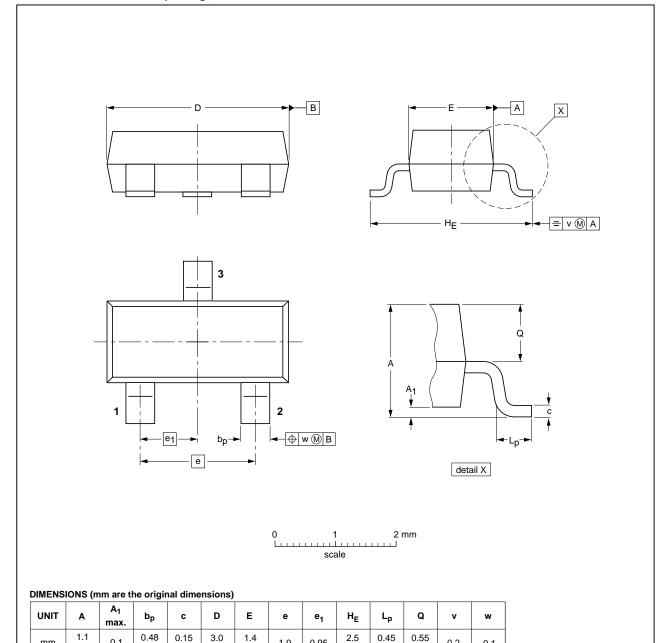
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PACKAGE OUTLINE

Plastic surface-mounted package; 3 leads

SOT23



OUTLINE	NE REFERENCES			EUROPEAN	ISSUE DATE	
VERSION	IEC	JEDEC	JEITA		PROJECTION	ISSUE DATE
SOT23		TO-236AB				-04-11-04 06-03-16

0.2

0.1

0.95

1.9

1997 Oct 29 10

0.1

0.38

0.9

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DATA SHEET STATUS

DOCUMENT STATUS ⁽¹⁾	PRODUCT STATUS ⁽²⁾	DEFINITION
Objective data sheet	Development	This document contains data from the objective specification for product development.
Preliminary data sheet	Qualification	This document contains data from the preliminary specification.
Product data sheet	Production	This document contains the product specification.

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