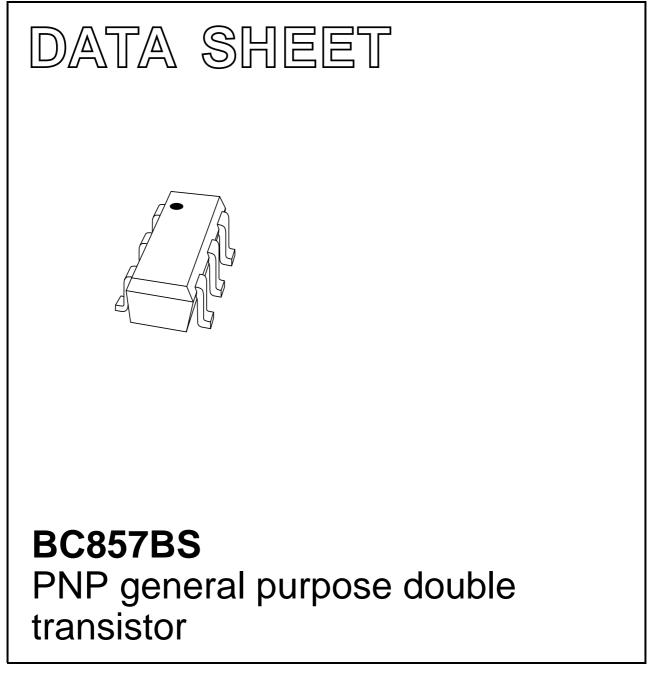
DISCRETE SEMICONDUCTORS



Product data sheet Supersedes data of 1997 Jul 09 1999 Apr 26



FEATURES

- Low collector capacitance
- · Low collector-emitter saturation voltage
- Closely matched current gain
- · Reduces number of components and boardspace
- No mutual interference between the transistors.

APPLICATIONS

• General purpose switching and amplification.

DESCRIPTION

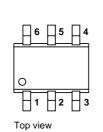
PNP double transistor in an SC-88; SOT363 plastic package. NPN complement: BC847BS.

MARKING

TYPE NUMBER	MARKING CODE		
BC857BS	3Ft		

PINNING

PIN	DESCRIPTION		
1, 4	emitter	TR1; TR2	
2, 5	base	TR1; TR2	
6, 3	collector	TR1; TR2	



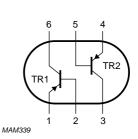


Fig.1 Simplified outline (SC-88; SOT363) and symbol.

LIMITING VALUES

In accordance with the Absolute Maximum Rating System (IEC 134).

SYMBOL	PARAMETER	CONDITIONS	MIN.	MAX.	UNIT	
Per transis	Per transistor					
V _{CBO}	collector-base voltage	open emitter	-	-50	V	
V _{CEO}	collector-emitter voltage	open base	—	-45	V	
V _{EBO}	emitter-base voltage	open collector	—	-5	V	
I _C	collector current (DC)		—	-100	mA	
I _{CM}	peak collector current		—	-200	mA	
I _{BM}	peak base current		—	-200	mA	
P _{tot}	total power dissipation	$T_{amb} \le 25 \ ^{\circ}C$	—	200	mW	
T _{stg}	storage temperature		-65	+150	°C	
Tj	junction temperature		—	150	°C	
T _{amb}	operating ambient temperature		-65	+150	°C	
Per device	•					
P _{tot}	total power dissipation	Tamb \leq 25 °C; note 1	-	300	mW	

Note

1. Device mounted on an FR4 printed-circuit board.

Product data sheet

BC857BS

BC857BS

THERMAL CHARACTERISTICS

SYMBOL	PARAMETER	CONDITIONS	VALUE	UNIT	
Per device					
R _{th j-a} thermal resistance from junction to ambient note 1 416 K/					

Note

1. Device mounted on an FR4 printed-circuit board.

CHARACTERISTICS

 T_{amb} = 25 °C unless otherwise specified.

SYMBOL	PARAMETER	CONDITIONS	MIN.	TYP.	MAX.	UNIT
Per transist	tor	•		•	•	
I _{CBO}	collector cut-off current	$I_E = 0; V_{CB} = -30 V$	_	-	-15	nA
		$I_E = 0; V_{CB} = -30 \text{ V}; T_j = 150 \text{ °C}$	_	-	-5	μA
I _{EBO}	emitter cut-off current	$I_{C} = 0; V_{EB} = -5 V$	-	-	-100	nA
h _{FE}	DC current gain	$I_{C} = -2 \text{ mA}; V_{CE} = -5 \text{ V}$	200	-	450	
V _{CEsat} collector-en voltage	collector-emitter saturation	$I_{C} = -10 \text{ mA}; I_{B} = -0.5 \text{ mA}$	-	-	-100	mV
	voltage	$I_{C} = -100 \text{ mA}; I_{B} = -5 \text{ mA}; \text{ note } 1$	-	-	-400	mV
V _{BEsat}	base-emitter saturation voltage	$I_{C} = -10 \text{ mA}; I_{B} = -0.5 \text{ mA}$	_	-755	-	mV
V _{BE}	base-emitter voltage	$I_{C} = 2 \text{ mA}; V_{CE} = -5 \text{ V}$	-600	-655	-750	mV
C _c	collector capacitance	$I_E = i_e = 0; V_{CB} = -10 V; f = 1 MHz$	-	-	2.2	pF
Ce	emitter capacitance	$I_{C} = i_{c} = 0; V_{EB} = -500 \text{ mV}; f = 1 \text{ MHz}$	_	10	-	pF
f _T	transition frequency	$I_{C} = -10 \text{ mA}; V_{CE} = -5 \text{ V}; \text{ f} = 100 \text{ MHz}$	100	-	-	MHz

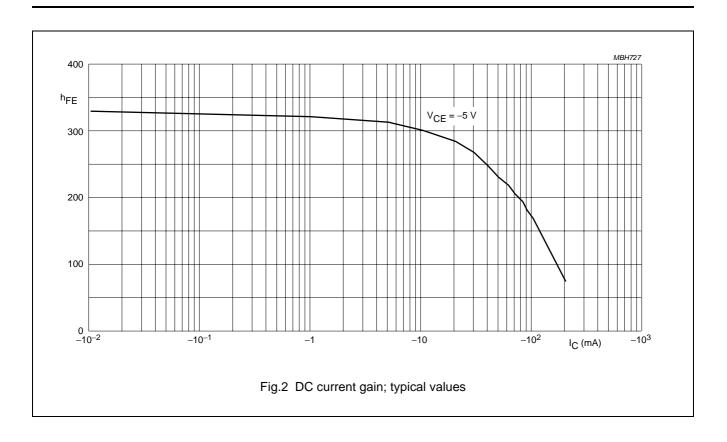
Note

1. Pulse test: $t_p \le 300 \ \mu s; \ \delta \le 0.02.$

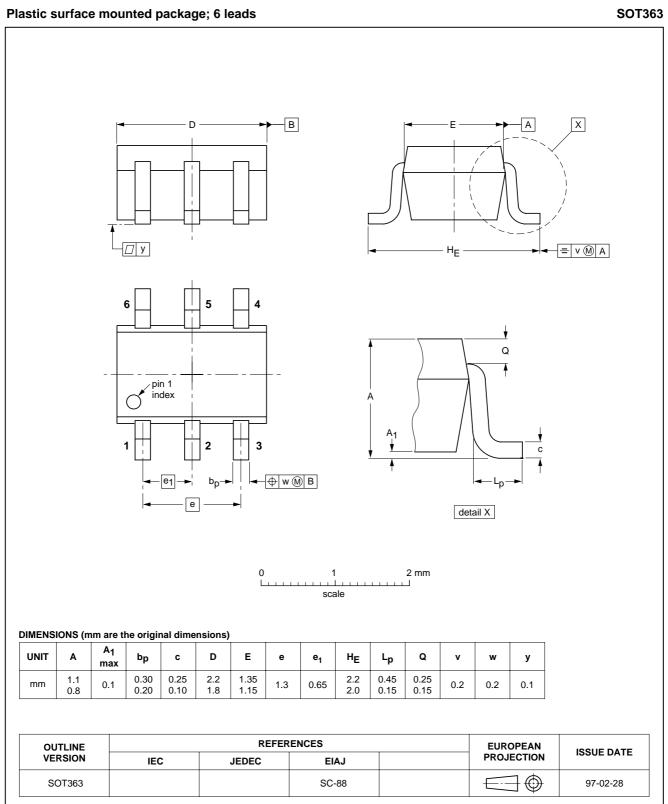
Product data sheet

BC857BS

PNP general purpose double transistor



PACKAGE OUTLINE



BC857BS

BC857BS

DATA SHEET STATUS

DOCUMENT STATUS ⁽¹⁾	PRODUCT STATUS ⁽²⁾	DEFINITION
Objective data sheet	Development	This document contains data from the objective specification for product development.
Preliminary data sheet	Qualification	This document contains data from the preliminary specification.
Product data sheet	Production	This document contains the product specification.

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Printed in The Netherlands

115002/00/02/pp7

Date of release: 1999 Apr 26

Document order number: 9397 750 05804



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