BCP54; BCX54; BC54PA 45 V, 1 A NPN medium power transistors Rev. 8 – 21 October 2011 P

#### 1. **Product profile**

### **1.1 General description**

NPN medium power transistor series in Surface-Mounted Device (SMD) plastic packages.

#### **Product overview** Table 1.

Type number <sup>[1]</sup>	Package	PNP complement		
	NXP	JEITA	JEDEC	
BCP54	SOT223	SC-73	-	BCP51
BCX54	SOT89	SC-62	TO-243	BCX51
BC54PA	SOT1061	-	-	BC51PA

[1] Valid for all available selection groups.

### 1.2 Features and benefits

- High current
- Three current gain selections
- High power dissipation capability
- Exposed heatsink for excellent thermal and electrical conductivity (SOT89, SOT1061)
- Leadless very small SMD plastic package with medium power capability (SOT1061)
- AEC-Q101 gualified

### 1.3 Applications

- Linear voltage regulators
- Low-side switches
- Battery-driven devices
- Power management
- MOSFET drivers
- Amplifiers

#### 1.4 Quick reference data

#### Table 2. Quick reference data

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
$V_{CEO}$	collector-emitter voltage	open base	-	-	45	V
I <sub>C</sub>	collector current		-	-	1	А
I <sub>CM</sub>	peak collector current	single pulse; $t_p \le 1 \text{ ms}$	-	-	2	А
h <sub>FE</sub>	DC current gain	$V_{CE}$ = 2 V; $I_{C}$ = 150 mA	<u>[1]</u> 63	-	250	
	h <sub>FE</sub> selection -10	$V_{CE} = 2 \text{ V}; I_{C} = 150 \text{ mA}$	<u>[1]</u> 63	-	160	
	h <sub>FE</sub> selection -16	$V_{CE} = 2 \text{ V}; I_{C} = 150 \text{ mA}$	[ <u>1]</u> 100	-	250	

[1] Pulse test:  $t_p \le 300 \ \mu s$ ;  $\delta = 0.02$ .



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### 2. Pinning information

Pin	Description	Simplified outline	Graphic symbo
SOT223			
1	base		
2	collector		2,4
3	emitter		1
4	collector		۲) ع
SOT89			sym016
1	emitter		
2	collector		2 J
3	base		31 sym042
SOT1061			
1	base		_
2	emitter	3	3
3	collector		1

### 3. Ordering information

Table 4.         Ordering information							
Type number <sup>[1]</sup>	Package	'ackage					
	Name	Description	Version				
BCP54	SC-73	plastic surface-mounted package with increased heatsink; 4 leads	SOT223				
BCX54	SC-62	plastic surface-mounted package; exposed die pad for good heat transfer; 3 leads	SOT89				
BC54PA	HUSON3	plastic thermal enhanced ultra thin small outline package; no leads; 3 terminals; body $2 \times 2 \times 0.65$ mm	SOT1061				

[1] Valid for all available selection groups.

BCP54_BCX54_BC54PA
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### 4. Marking

Table 5.   Marking codes	
Type number	Marking code
BCP54	BCP54
BCP54-10	BCP54/10
BCP54-16	BCP54/16
BCX54	BA
BCX54-10	BC
BCX54-16	BD
BC54PA	AT
BC54-10PA	BF
BC54-16PA	BG

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### 5. Limiting values

Symbol	Parameter	Conditions	Min	Max	Unit
V <sub>CBO</sub>	collector-base voltage	open emitter	-	45	V
V <sub>CEO</sub>	collector-emitter voltage	open base	-	45	V
V <sub>EBO</sub>	emitter-base voltage	open collector	-	5	V
I <sub>C</sub>	collector current		-	1	А
I <sub>CM</sub>	peak collector current	single pulse; t <sub>p</sub> ≤ 1 ms	-	2	А
I <sub>B</sub>	base current		-	0.3	А
I <sub>BM</sub>	peak base current	single pulse; t <sub>p</sub> ≤ 1 ms	-	0.3	А
P <sub>tot</sub>	total power dissipation	$T_{amb} \le 25 \ ^{\circ}C$			
BCP54		<u>[1]</u> _	0.65	W	
			[2]	1.00	W
			[3]	1.35	W
	BCX54		<u>[1]</u> _	0.50	W
			[2]	0.95	W
			<u>[3]</u>	1.35	W
	BC54PA		<u>[1]</u> _	0.42	W
			[2]	0.83	W
			<u>[3]</u>	1.10	W
			<u>[4]</u>	0.81	W
			<u>[5]</u>	1.65	W
Tj	junction temperature		-	150	°C
T <sub>amb</sub>	ambient temperature		-55	+150	°C
T <sub>stg</sub>	storage temperature		-65	+150	°C

[1] Device mounted on an FR4 Printed-Circuit Board (PCB), single-sided copper, tin-plated and standard footprint.

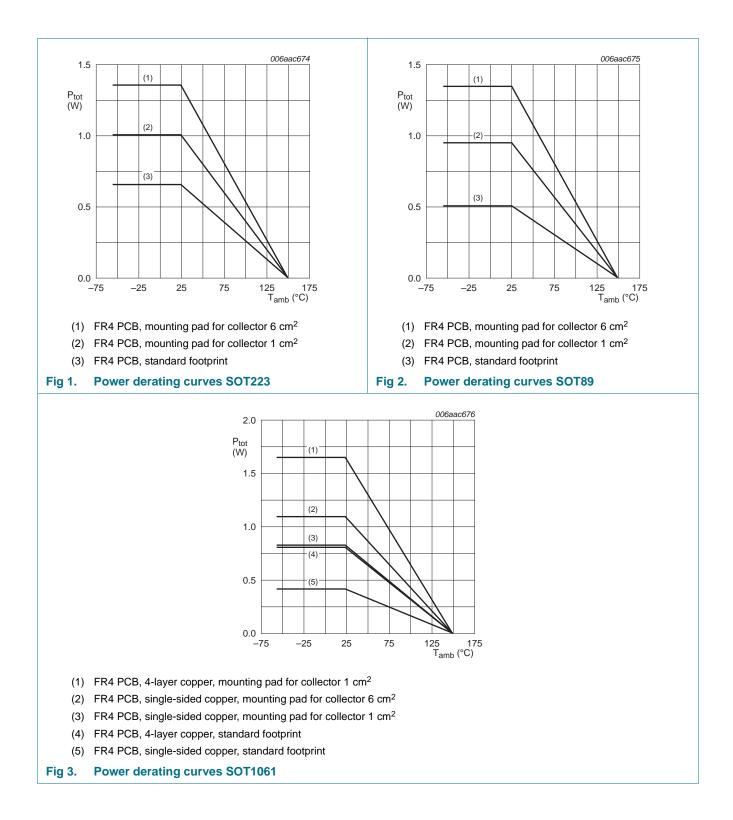
[3] Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for collector 6 cm<sup>2</sup>.

[4] Device mounted on an FR4 PCB, 4-layer copper, tin-plated and standard footprint.

[5] Device mounted on an FR4 PCB, 4-layer copper, tin-plated, mounting pad for collector 1 cm<sup>2</sup>.

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### 6. Thermal characteristics

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
R <sub>th(j-a)</sub>	thermal resistance from	in free air		.76		•
	junction to ambient					
	BCP54		<u>[1]</u> -	-	192	K/W
			[2] _	-	125	K/W
			[3]	-	93	K/W
	BCX54		[1] -	-	250	K/W
			[2] _	-	132	K/W
			[3]	-	93	K/W
	BC54PA		<u>[1]</u> _	-	298	K/W
			[2] _	-	151	K/W
			[3]	-	114	K/W
			[4] _	-	154	K/W
			[5] _	-	76	K/W
R <sub>th(j-sp)</sub>	thermal resistance from junction to solder point					
	BCP54		-	-	16	K/W
	BCX54		-	-	16	K/W
	BC54PA		-	-	20	K/W

[1] Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint.

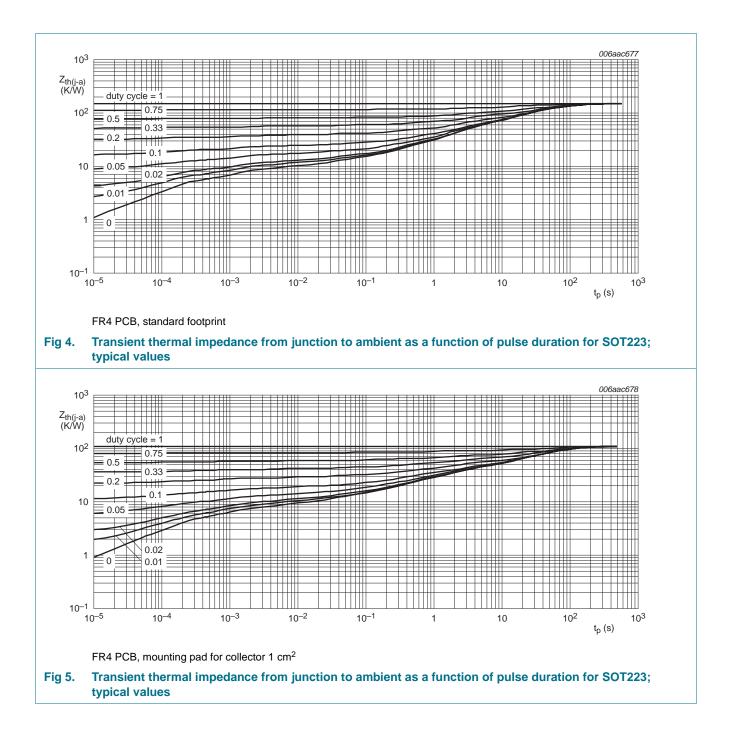
[2] Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for collector 1 cm<sup>2</sup>.

[3] Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for collector 6 cm<sup>2</sup>.

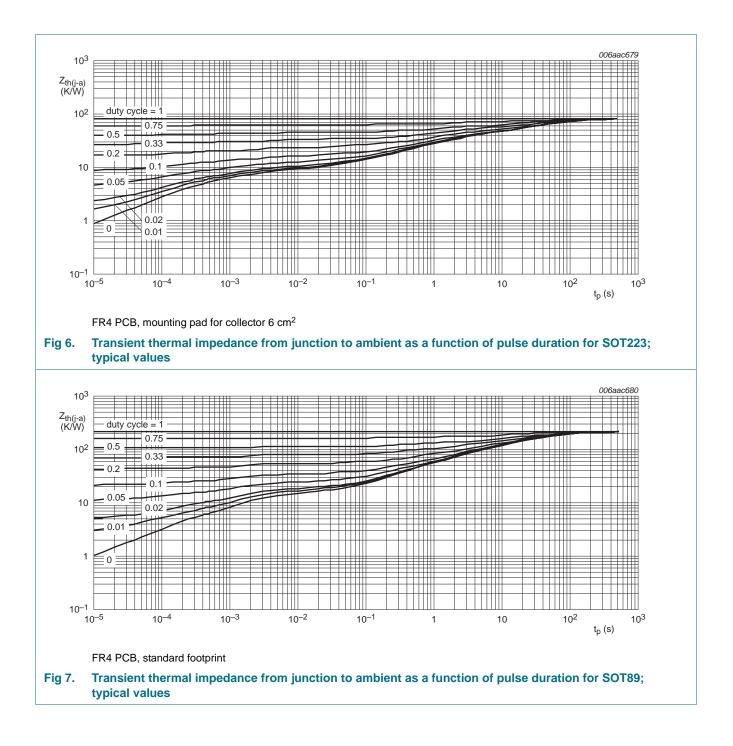
[4] Device mounted on an FR4 PCB, 4-layer copper, tin-plated and standard footprint.

[5] Device mounted on an FR4 PCB, 4-layer copper, tin-plated, mounting pad for collector 1 cm<sup>2</sup>.

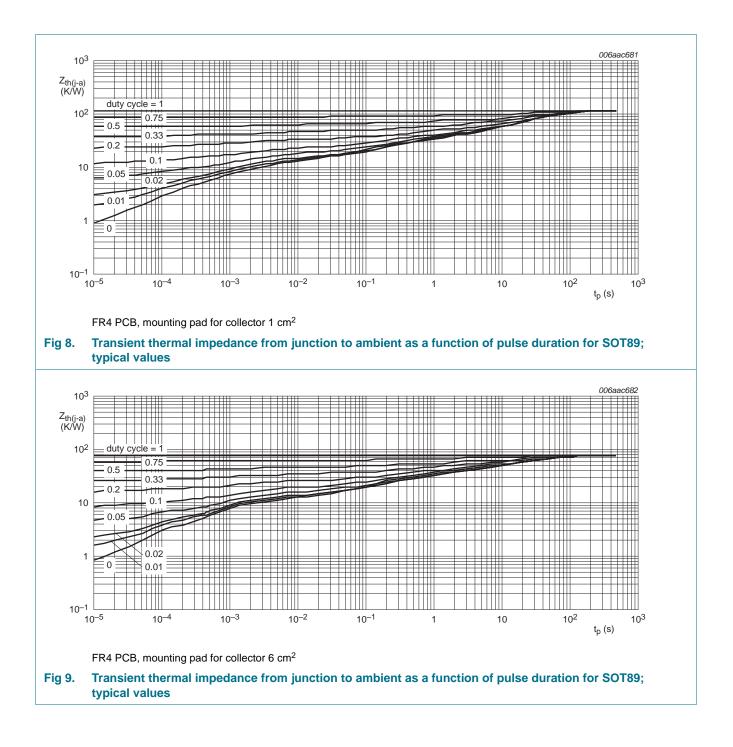
## BCP54; BCX54; BC54PA



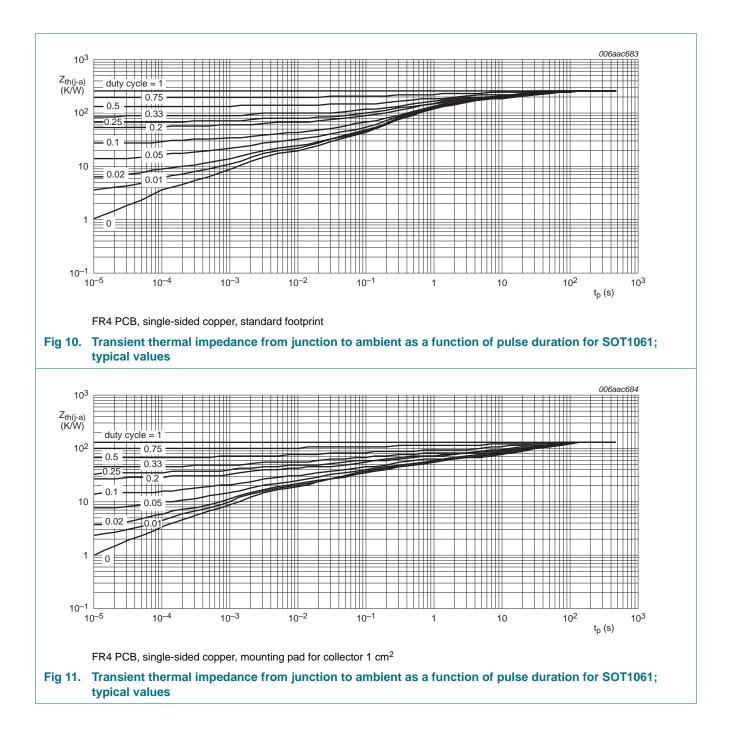
## BCP54; BCX54; BC54PA



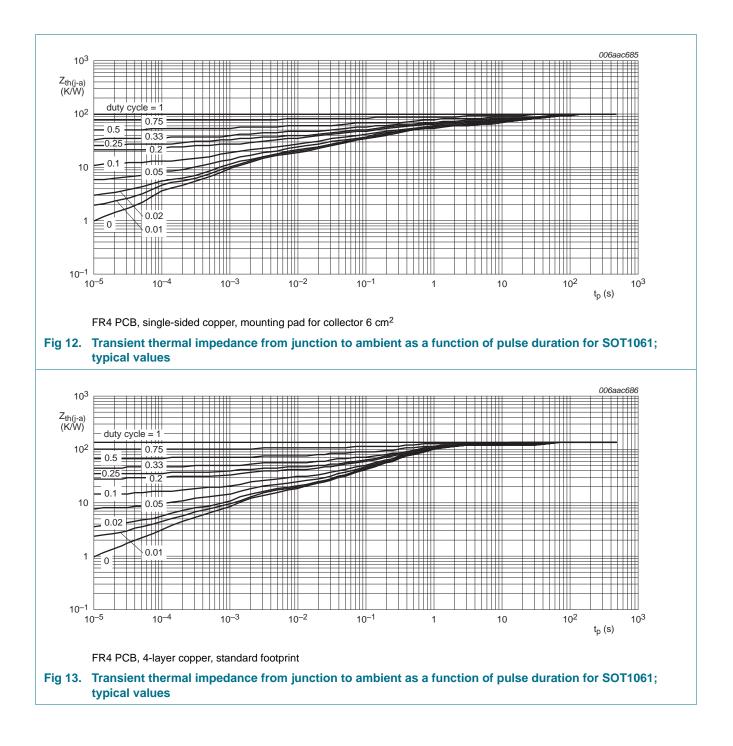
## BCP54; BCX54; BC54PA



## BCP54; BCX54; BC54PA

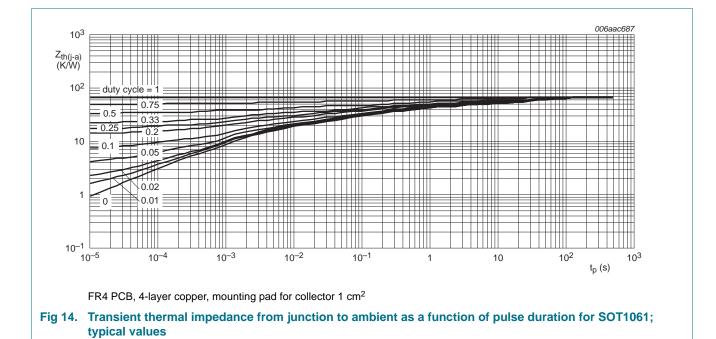


## BCP54; BCX54; BC54PA



## BCP54; BCX54; BC54PA

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### 7. Characteristics

#### Table 8.Characteristics

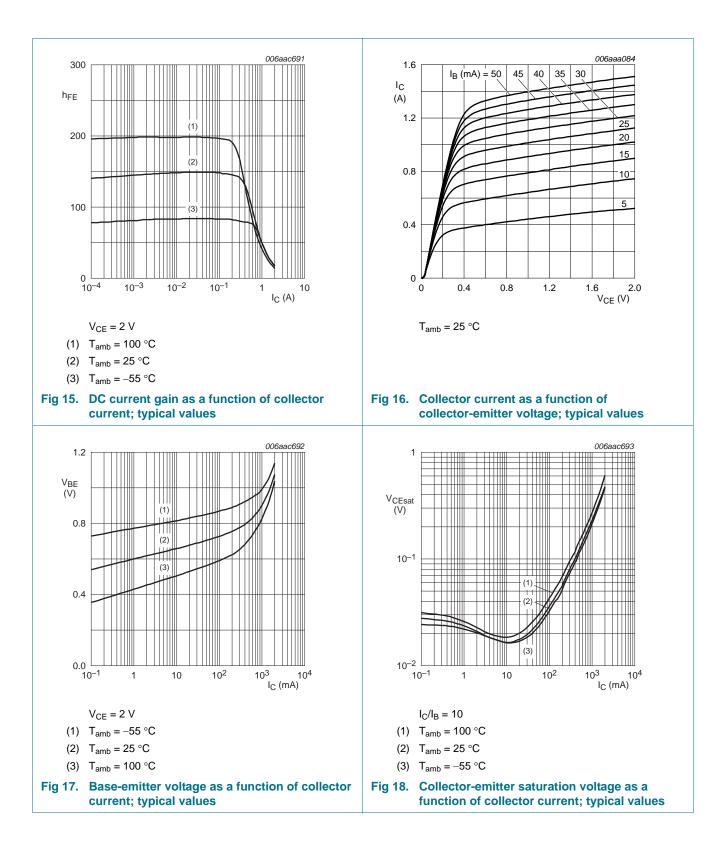
 $T_{amb} = 25 \ ^{\circ}C$  unless otherwise specified.

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
I <sub>CBO</sub>	collector-base cut-off	$V_{CB} = 30 \text{ V}; I_E = 0 \text{ A}$		-	-	100	nA
current		$V_{CB} = 30 \text{ V}; I_E = 0 \text{ A};$ $T_j = 150 ^{\circ}\text{C}$		-	-	10	μΑ
I <sub>EBO</sub>	emitter-base cut-off current	$V_{EB} = 5 \text{ V}; \text{ I}_{C} = 0 \text{ A}$		-	-	100	nA
h <sub>FE</sub>	DC current gain	$V_{CE} = 2 V$					
	$I_{\rm C} = 5  \rm{mA}$	[1]	63	-	-		
	I <sub>C</sub> = 150 mA	[1]	63	-	250		
	I <sub>C</sub> = 500 mA	[1]	40	-	-		
	DC current gain	$V_{CE} = 2 V$					
	h <sub>FE</sub> selection -10	I <sub>C</sub> = 150 mA	[1]	63	-	160	
	h <sub>FE</sub> selection -16	I <sub>C</sub> = 150 mA	[1]	100	-	250	
V <sub>CEsat</sub>	collector-emitter saturation voltage	$I_{\rm C}$ = 500 mA; $I_{\rm B}$ = 50 mA	<u>[1]</u>	-	-	0.5	V
V <sub>BE</sub>	base-emitter voltage	$V_{CE} = 2 \text{ V}; \text{ I}_{C} = 500 \text{ mA}$	[1]	-	-	1	V
C <sub>c</sub>	collector capacitance	$\label{eq:VCB} \begin{array}{l} V_{CB} = 10 \text{ V};  \text{I}_{E} = \text{i}_{e} = 0 \text{ A}; \\ \text{f} = 1 \text{ MHz} \end{array}$		-	6	-	pF
f <sub>T</sub>	transition frequency	V <sub>CE</sub> = 5 V; I <sub>C</sub> = 50 mA; f = 100 MHz		100	180	-	MHz

[1] Pulse test:  $t_p \le 300 \ \mu s$ ;  $\delta = 0.02$ .

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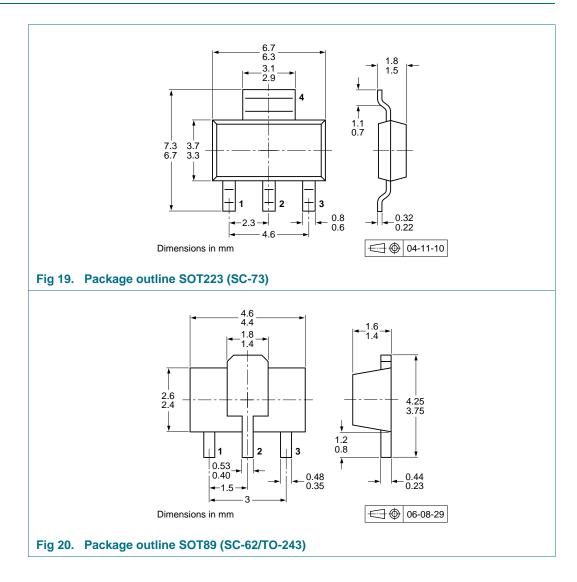
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### 8. Test information

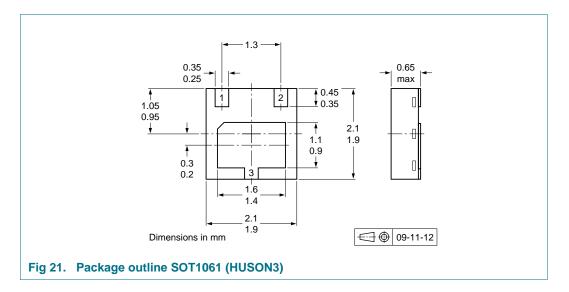
#### 8.1 Quality information

This product has been qualified in accordance with the Automotive Electronics Council (AEC) standard *Q101* - *Stress test qualification for discrete semiconductors*, and is suitable for use in automotive applications.

### 9. Package outline



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### **10. Packing information**

#### Table 9. Packing methods

The indicated -xxx are the last three digits of the 12NC ordering code.[1]

Туре	Package	Description		Packir	ig quant	ity
number <sup>[2]</sup>				1000	3000	4000
BCP54	SOT223	8 mm pitch, 12 mm tape and reel		-115	-	-135
BCX54	SOT89	8 mm pitch, 12 mm tape and reel; T1	[3]	-115	-	-135
		8 mm pitch, 12 mm tape and reel; T3	[4]	-146	-	-
BC54PA	SOT1061	4 mm pitch, 8 mm tape and reel		-	-115	-

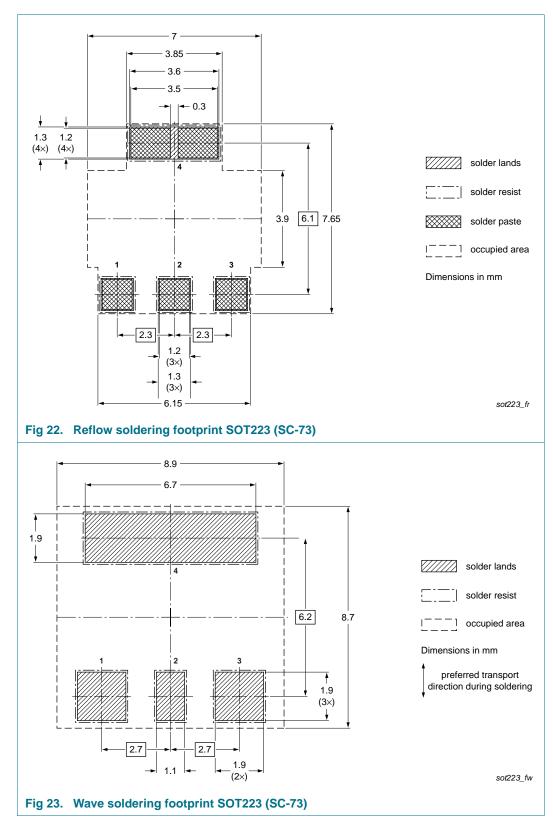
[1] For further information and the availability of packing methods, see <u>Section 14</u>.

[2] Valid for all available selection groups.

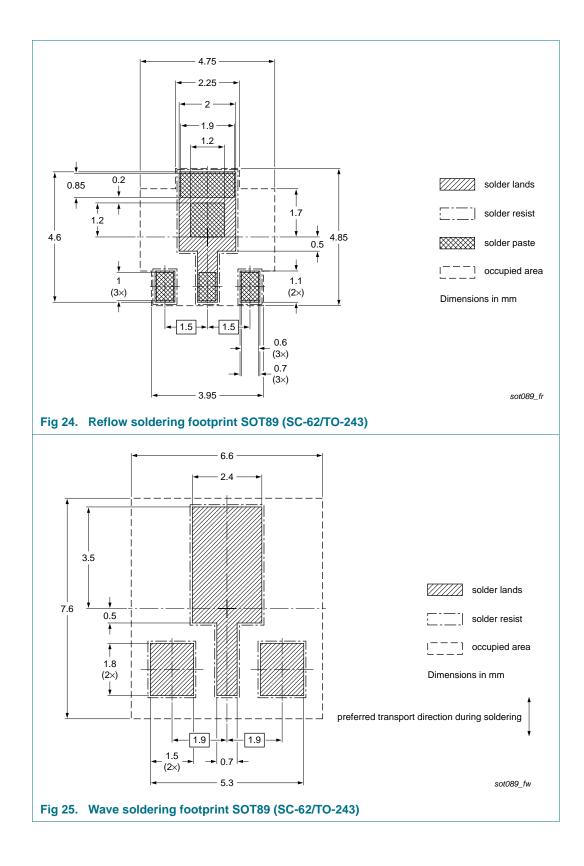
- [3] T1: normal taping
- [4] T3: 90° rotated taping

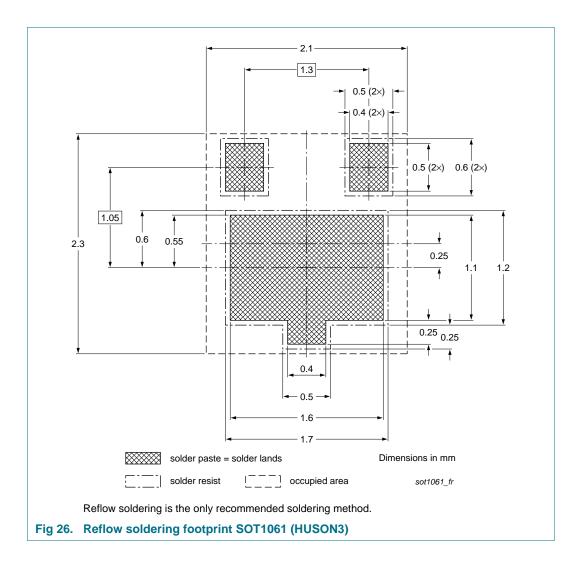
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### 11. Soldering



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### 12. Revision history

#### Table 10.Revision history

Document ID	Release date	Data sheet status	Change notice	Supersedes			
BCP54_BCX54_BC54PA v.8	20111021	Product data sheet	-	BC635_BCP54_BCX54 v.7			
Modifications:	<ul> <li>Type numb</li> </ul>	er removed: BC635					
	<ul> <li>Type numb</li> </ul>	ers added: BC54PA, BC5	4-10PA and BC54-	16PA			
	<u>Section 1 "Product profile"</u> : updated						
	Section 2 "	Pinning information": upda	ated				
	• <u>Table 6</u> and	d <u>7</u> : updated according to	latest measuremen	ts			
	• Figure 1, 2	, <u>4</u> , <u>5</u> , <u>7</u> to <u>9</u> , <u>15</u> , <u>17</u> and <u>1</u>	<u>8</u> : updated				
	• Figure 3, 6	, <u>10</u> to <u>14</u> : added					
	<ul> <li>Section 8 "</li> </ul>	Test information": added					
	<ul> <li><u>Section 10 "Packing information"</u>: updated</li> </ul>						
	Section 11	"Soldering": added					
	Section 13	"Legal information": upda	ted				
BC635_BCP54_BCX54 v.7	20070604	Product data sheet	-	BC635_BCP54_BCX54 v.6			
BC635_BCP54_BCX54 v.6	20050225	Product data sheet	CPCN2004050	BC635_637_639 v.4			
			29	BCP54_55_56 v.5			
				BCX54_55_56 v.4			
BC635_637_639 v.4	20011010	Product specification	-	BC635_637_639 v.3			
BCP54_55_56 v.5	20030206	Product specification	-	BCP54_55_56 v.4			
BCX54_55_56 v.4	20011010	Product specification	-	BCX54_55_56 v.3			

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### 13. Legal information

#### 13.1 Data sheet status

Document status[1][2]	Product status <sup>[3]</sup>	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

[1] Please consult the most recently issued document before initiating or completing a design.

[2] The term 'short data sheet' is explained in section "Definitions".

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