



120V NPN DARLINGTON TRANSISTOR IN SOT223

Features

- BV_{CEO} > 120V
- BV_{CBO} > 140V
- I_C = 1.5A High Continuous Current
- hFE > 2k for High Gain @ 1A
- Lead-Free Finish; RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- Qualified to AEC-Q101 Standards for High Reliability

Mechanical Data

- Case: SOT223
- Case Material: Molded Plastic. "Green" Molding Compound;
 UL Flammability Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Finish Matte Tin Plated Leads, Solderable per MIL-STD-202, Method 208 (3)
- Weight: 0.112 grams (Approximate)

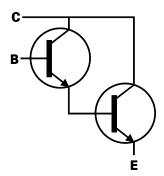
Applications

- Lamp
- Relay
- Solenoid Driving

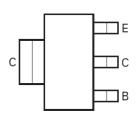




Top View



Device Symbol



Top View Pin-Out

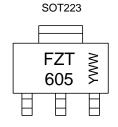
Ordering Information (Note 4)

Product	Marking	Reel size (inches)	Tape width (mm)	Quantity per reel
FZT605TA	FZT605	7	12	1,000
FZT605TC	FZT605	13	12	4,000

Notes:

- 1. EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant. All applicable RoHS exemptions applied.
- See http://www.diodes.com/quality/lead_free.html for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
- 4. For packaging details, go to our website at http"//www.diodes.com/products/packages.html.

Marking Information



FZT 605 = Product Type Marking Code YWW = Date Code Marking Y or \overline{Y} = Last Digit of Year (ex: 5= 2015) WW or $\overline{W}W$ = Week Code (01~53)



Absolute Maximum Ratings (@TA = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit
Collector-Base Voltage	V_{CBO}	140	V
Collector-Emitter Voltage	$V_{\sf CEO}$	120	V
Emitter-Base Voltage	V_{EBO}	14	V
Continuous Collector Current	lc	1.5	Α
Peak Pulse Current	I _{CM}	4	Α

Thermal Characteristics (@TA = +25°C, unless otherwise specified.)

Characteristic		Symbol	Value	Unit	
	(Note 5)		3.0		
Power Dissipation	(Note 6)	D	2.0	W	
Power Dissipation	(Note 7)	P_D	1.6		
	(Note 8)		1.2		
	(Note 5)		41.7		
Thermal Resistance, Junction to Ambient	(Note 6)	$R_{ hetaJA}$	62.5		
Thermal Resistance, Junction to Ambient	(Note 7)		78.1	°C/W	
	(Note 8)		104		
Thermal Resistance Junction to Lead (Note 9)		$R_{ hetaJL}$	12.9		
Operating and Storage Temperature Range	$T_{J,}T_{STG}$	-55 to +150	°C		

ESD Ratings (Note 10)

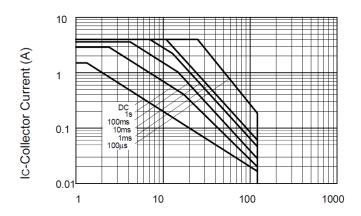
Characteristic	Symbol	Value	Unit	JEDEC Class
Electrostatic Discharge - Human Body Model	ESD HBM	4,000	V	3A
Electrostatic Discharge - Machine Model	ESD MM	400	V	С

Notes:

- 5. For a device mounted with the collector lead on 50mm x 50mm 2oz copper that is on a single-sided 1.6mm FR4 PCB; device is measured under still air conditions whilst operating in a steady-state.
- 6. Same as Note 5, except the device is mounted on 25mm x 25mm 2oz copper.
- 7. Same as Note 5, except the device is mounted on 25mm x 25mm 1oz copper.
- 8. Same as Note 5, except the device is mounted on minimum recommended pad layout.
- 9. Thermal resistance from junction to solder-point (at the end of the collector lead).
- 10. Refer to JEDEC specification JESD22-A114 and JESD22-A115.

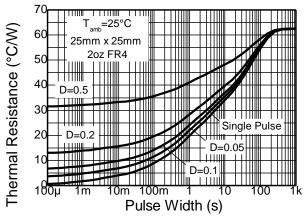


Thermal Characteristics and Derating Information



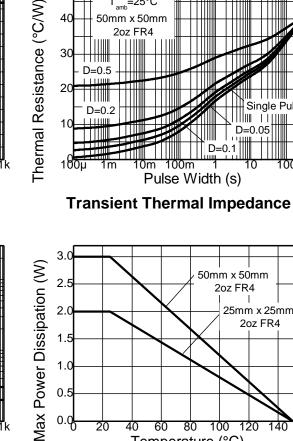
VCE - Collector Emitter Voltage (V)

FZT605 Safe Operating Area



Transient Thermal Impedance

Single Pulse



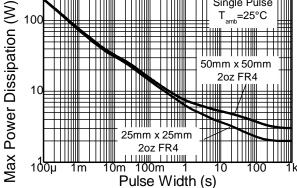
T_{amb}=25°C

50mm x 50mm

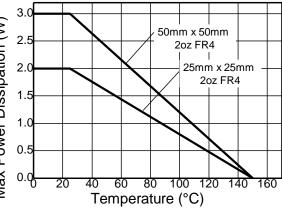
2oz FR4

D=0.5

D=0.2



Pulse Power Dissipation



Derating Curve

Single Pulse

D=0.05

D=0.1





Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

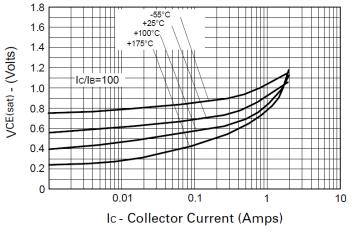
Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition
Collector-Base Breakdown Voltage	BV _{CBO}	140	_	_	V	$I_C = 100\mu A$
Collector-Emitter Breakdown Voltage (Note 11)	BV _{CEO}	120	_	_	V	I _C = 1mA
Emitter-Base Breakdown Voltage	BV _{EBO}	14	_	_	V	I _E = 100μA
Collector-Base Cut-Off Current	I _{CBO}	-	_	100 10	nΑ μΑ	V _{CB} = 120V V _{CB} = 120V, T _A = +120°C
Collector-Emitter Cut-Off Current	I _{CES}	-	_	100	nA	V _{CE} = 120V
Emitter Cut-Off Current	I _{EBO}	-	_	100	nA	V _{EB} = 8V
DC Current Gain (Note 11)	h _{FE}	2,000 5,000 2,000 500	1111	_ _ 100,000 _	ı	$I_{C} = 50 \text{mA}, V_{CE} = 5 \text{V}$ $I_{C} = 500 \text{mA}, V_{CE} = 5 \text{V}$ $I_{C} = 1 \text{A}, V_{CE} = 5 \text{V}$ $I_{C} = 2 \text{A}, V_{CE} = 5 \text{V}$
Collector-Emitter Saturation Voltage (Note 11)	V _{CE(sat)}	_		1 1.5	V	$I_C = 250$ mA, $I_B = 0.25$ mA $I_C = 1$ A, $I_B = 1$ mA
Base-Emitter Saturation Voltage (Note 11)	V _{BE(sat)}	_	_	1.8	V	I _C = 1A, I _B = 1mA
Base-Emitter Turn-On Voltage (Note 11)	V _{BE(on)}	_	_	1.7	V	I _C = 1A, V _{CE} = 5V
Input Capacitance	C _{ibo}	_	90	_	pF	V _{EB} = 0.5V, f = 1MHz
Output Capacitance	C _{obo}	_	15	_	pF	$V_{CB} = 10V$, $f = 1MHz$
Current Gain-Bandwidth Product	f⊤	150	_	_	MHz	V _{CE} = 10V, I _C = 100mA, f=20MHz
Turn-On Time	ton	_	0.5	_	μs	$V_{CC} = 10V, I_C = 500mA$
Turn-Off Time	t _{off}		1.6		μs	$I_{B1} = -I_{B2} = 0.5 \text{mA}$

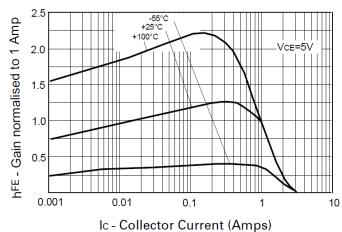
Note: 11. Measured under pulsed conditions. Pulse width \leq 300 μ s. Duty cycle \leq 2%.





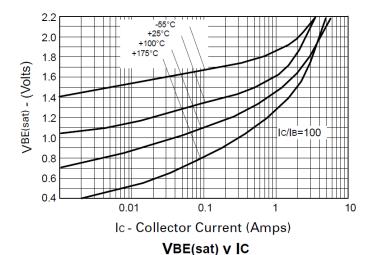
Typical Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

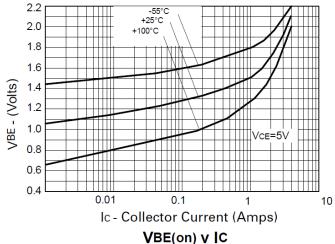




VCE(sat) v IC

hFE v IC

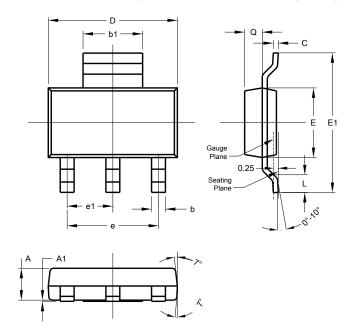






Package Outline Dimensions

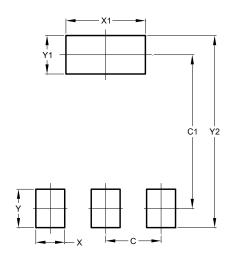
Please see AP02002 at http://www.diodes.com/datasheets/ap02002.pdf for the latest version.



SOT223					
Dim	Min	Max	Тур		
Α	1.55	1.65	1.60		
A1	0.010	0.15	0.05		
b	0.60	0.80	0.70		
b1	2.90	3.10	3.00		
С	0.20	0.30	0.25		
D	6.45	6.55	6.50		
Е	3.45	3.55	3.50		
E1	6.90	7.10	7.00		
е	-	-	4.60		
e1	-	-	2.30		
L	0.85	1.05	0.95		
Q	0.84	0.94	0.89		
All [All Dimensions in mm				

Suggested Pad Layout

Please see AP02001 at http://www.diodes.com/datasheets/ap02001.pdf for the latest version.



Dimensions	Value (in mm)		
С	2.30		
C1	6.40		
Х	1.20		
X1	3.30		
Υ	1.60		
Y1	1.60		
Y2	8.00		

For high voltage applications, the appropriate industry sector guidelines should be considered with regards to creepage and clearance distances between device terminals and PCB tracking.





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