

# PMEG4002EJ

200 mA low  $V_F$  MEGA Schottky barrier rectifier

Rev. 01 — 15 May 2009

Product data sheet

## 1. Product profile

### 1.1 General description

Planar Maximum Efficiency General Application (MEGA) Schottky barrier rectifier with an integrated guard ring for stress protection, encapsulated in a SOD323F (SC-90) small and flat lead Surface-Mounted Device (SMD) plastic package.

### 1.2 Features

- Average forward current:  $I_{F(AV)} \leq 0.2$  A
- Reverse voltage:  $V_R \leq 40$  V
- Low forward voltage
- AEC-Q101 qualified
- Small and flat lead SMD plastic package

### 1.3 Applications

- Low voltage rectification
- High efficiency DC-to-DC conversion
- Switch Mode Power Supply (SMPS)
- Reverse polarity protection
- Ultra high-speed switching
- Low power consumption applications

### 1.4 Quick reference data

**Table 1. Quick reference data**

$T_j = 25^\circ\text{C}$  unless otherwise specified.

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
$I_{F(AV)}$	average forward current	square wave; $\delta = 0.5$ ;	$T_{amb} \leq 130^\circ\text{C}$	-	-	0.2
		$f = 20$ kHz		-	-	A
		$T_{sp} \leq 145^\circ\text{C}$	-	-	0.2	A
$V_R$	reverse voltage		-	-	40	V
$V_F$	forward voltage	$I_F = 0.2$ A	-	520	600	mV
$I_R$	reverse current	$V_R = 40$ V	-	0.7	10	$\mu\text{A}$

[1] Device mounted on a ceramic Printed-Circuit Board (PCB),  $\text{Al}_2\text{O}_3$ , standard footprint.

## 2. Pinning information

Table 2. Pinning

Pin	Description	Simplified outline	Graphic symbol
1	cathode	[1]	
2	anode		 sym001

[1] The marking bar indicates the cathode.

## 3. Ordering information

Table 3. Ordering information

Type number	Package		
	Name	Description	Version
PMEG4002EJ	SC-90	plastic surface-mounted package; 2 leads	SOD323F

## 4. Marking

Table 4. Marking codes

Type number	Marking code
PMEG4002EJ	1N

## 5. Limiting values

Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions	Min	Max	Unit
V <sub>R</sub>	reverse voltage	T <sub>j</sub> = 25 °C	-	40	V
I <sub>F(AV)</sub>	average forward current	square wave; δ = 0.5; f = 20 kHz	[1]	-	A
		T <sub>amb</sub> ≤ 130 °C	[1]	-	0.2
I <sub>FRM</sub>	repetitive peak forward current	T <sub>sp</sub> ≤ 145 °C	-	0.2	A
		t <sub>p</sub> ≤ 1 ms; δ ≤ 0.25	-	2.6	A
I <sub>FSM</sub>	non-repetitive peak forward current	square wave; t <sub>p</sub> = 8 ms	[2]	-	2.75 A
P <sub>tot</sub>	total power dissipation	T <sub>amb</sub> ≤ 25 °C	[3][4]	-	mW
			[3][5]	-	385 mW
			[3][1]	-	695 mW
			[3][1]	-	1045 mW

**Table 5. Limiting values ...continued**

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions	Min	Max	Unit
T <sub>j</sub>	junction temperature		-	150	°C
T <sub>amb</sub>	ambient temperature		-55	+150	°C
T <sub>stg</sub>	storage temperature		-65	+150	°C

[1] Device mounted on a ceramic PCB, Al<sub>2</sub>O<sub>3</sub>, standard footprint.[2] T<sub>j</sub> = 25 °C prior to surge.

[3] Reflow soldering is the only recommended soldering method.

[4] Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint.

[5] Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for cathode 1 cm<sup>2</sup>.

## 6. Thermal characteristics

**Table 6. Thermal characteristics**

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
R <sub>th(j-a)</sub>	thermal resistance from junction to ambient	in free air	[1][2]			
			[3]	-	-	325 K/W
			[4]	-	-	180 K/W
			[5]	-	-	120 K/W
R <sub>th(j-sp)</sub>	thermal resistance from junction to solder point		[6]	-	-	25 K/W

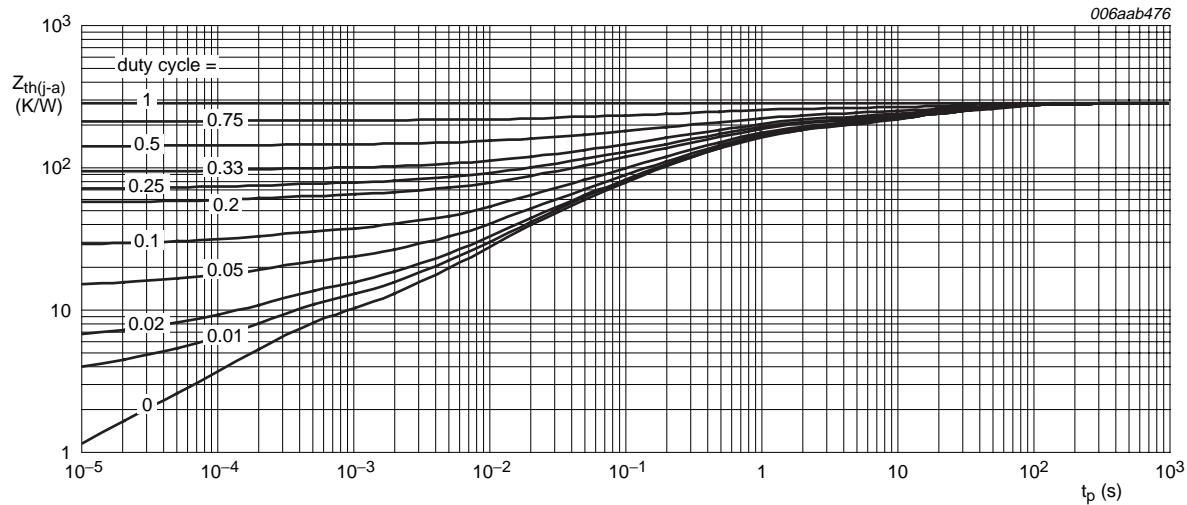
[1] For Schottky barrier diodes thermal runaway has to be considered, as in some applications the reverse power losses P<sub>R</sub> are a significant part of the total power losses.

[2] Reflow soldering is the only recommended soldering method.

[3] Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint.

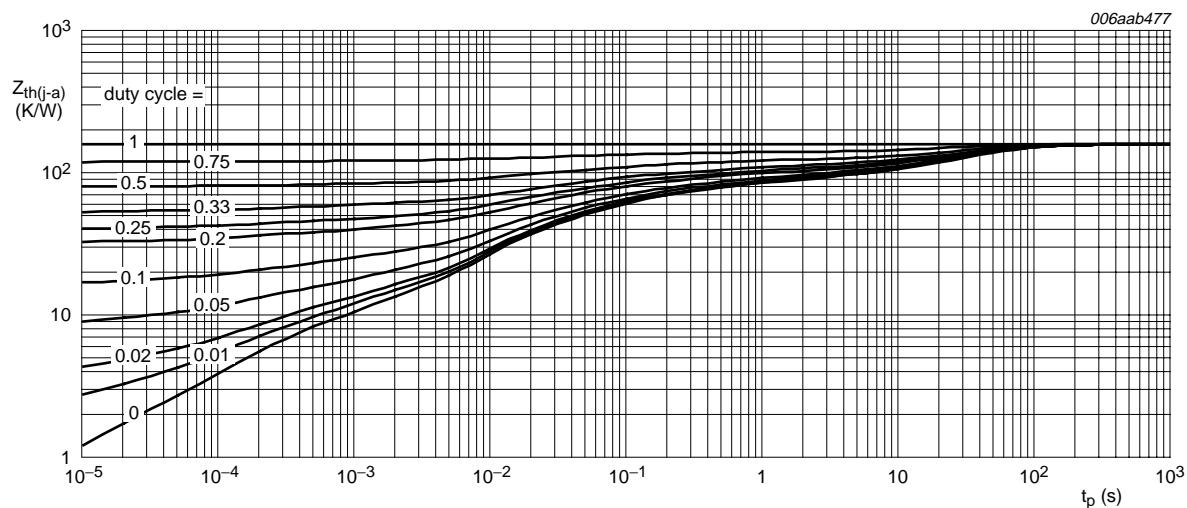
[4] Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for cathode 1 cm<sup>2</sup>.[5] Device mounted on a ceramic PCB, Al<sub>2</sub>O<sub>3</sub>, standard footprint.

[6] Soldering point of cathode tab.



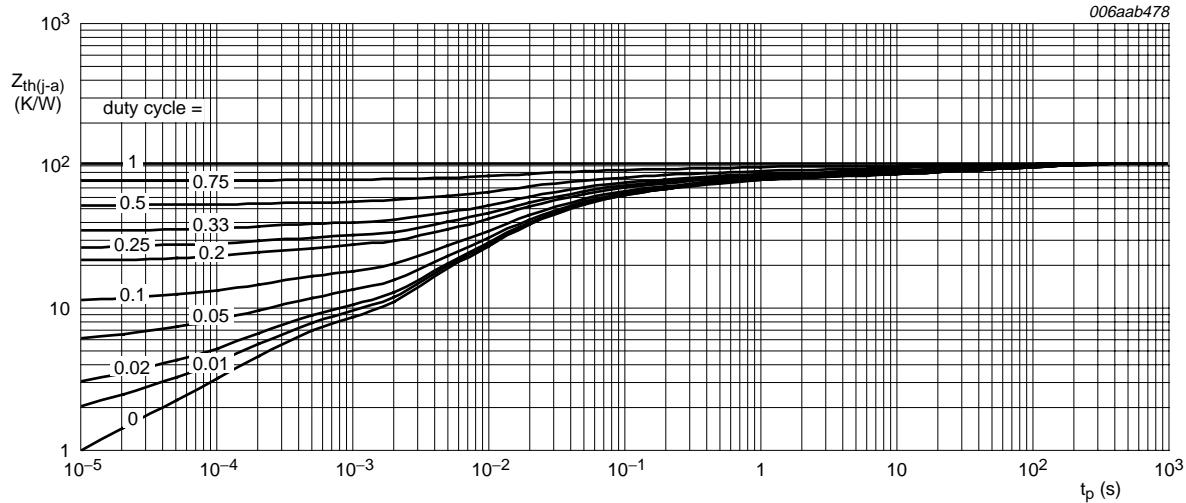
FR4 PCB, standard footprint

**Fig 1. Transient thermal impedance from junction to ambient as a function of pulse duration; typical values**



FR4 PCB, mounting pad for cathode  $1 \text{ cm}^2$

**Fig 2. Transient thermal impedance from junction to ambient as a function of pulse duration; typical values**



Ceramic PCB,  $\text{Al}_2\text{O}_3$ , standard footprint

**Fig 3. Transient thermal impedance from junction to ambient as a function of pulse duration; typical values**

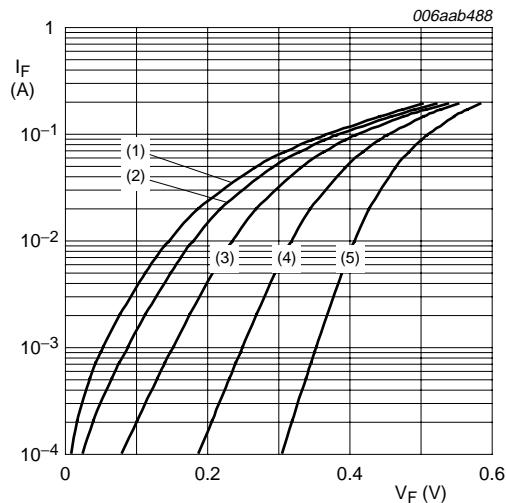
## 7. Characteristics

**Table 7. Characteristics**

$T_j = 25^\circ\text{C}$  unless otherwise specified.

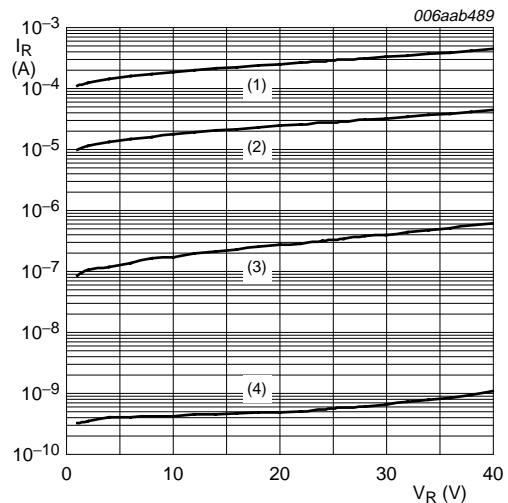
Symbol	Parameter	Conditions	Min	Typ	Max	Unit	
$V_F$	forward voltage	$I_F = 0.1 \text{ mA}$	-	190	220	mV	
		$I_F = 1 \text{ mA}$	-	250	290	mV	
		$I_F = 10 \text{ mA}$	-	320	360	mV	
		$I_F = 100 \text{ mA}$	-	440	500	mV	
		$I_F = 200 \text{ mA}$	-	520	600	mV	
$I_R$	reverse current	$V_R = 10 \text{ V}$	-	0.2	0.3	$\mu\text{A}$	
		$V_R = 25 \text{ V}$	-	0.3	0.5	$\mu\text{A}$	
		$V_R = 40 \text{ V}$	-	0.7	10	$\mu\text{A}$	
$C_d$	diode capacitance	$f = 1 \text{ MHz}$					
		$V_R = 1 \text{ V}$	-	14	-	pF	
		$V_R = 10 \text{ V}$	-	6	-	pF	
$t_{rr}$	reverse recovery time		[1]	-	5	-	ns

[1] When switched from  $I_F = 10 \text{ mA}$  to  $I_R = 10 \text{ mA}$ ;  $R_L = 100 \Omega$ ; measured at  $I_R = 1 \text{ mA}$ .



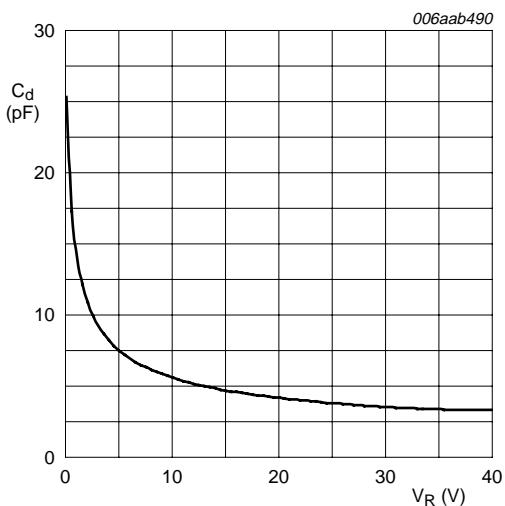
- (1)  $T_j = 150 \text{ } ^\circ\text{C}$
- (2)  $T_j = 125 \text{ } ^\circ\text{C}$
- (3)  $T_j = 85 \text{ } ^\circ\text{C}$
- (4)  $T_j = 25 \text{ } ^\circ\text{C}$
- (5)  $T_j = -40 \text{ } ^\circ\text{C}$

**Fig 4. Forward current as a function of forward voltage; typical values**



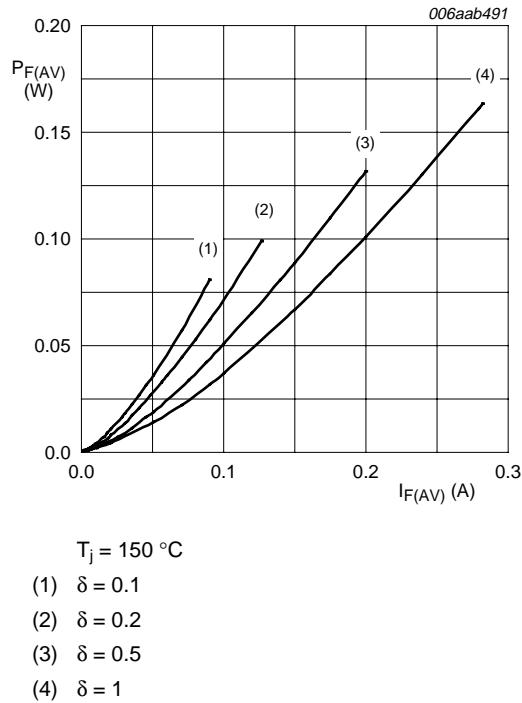
- (1)  $T_j = 125 \text{ } ^\circ\text{C}$
- (2)  $T_j = 85 \text{ } ^\circ\text{C}$
- (3)  $T_j = 25 \text{ } ^\circ\text{C}$
- (4)  $T_j = -40 \text{ } ^\circ\text{C}$

**Fig 5. Reverse current as a function of reverse voltage; typical values**

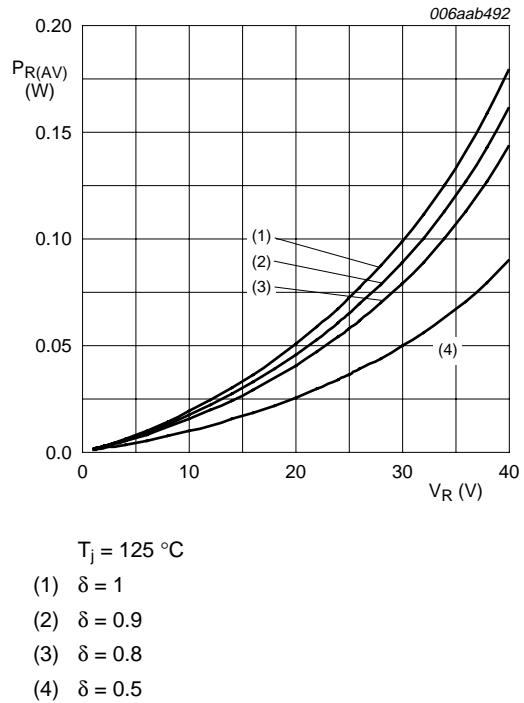


$f = 1 \text{ MHz}; T_{\text{amb}} = 25 \text{ } ^\circ\text{C}$

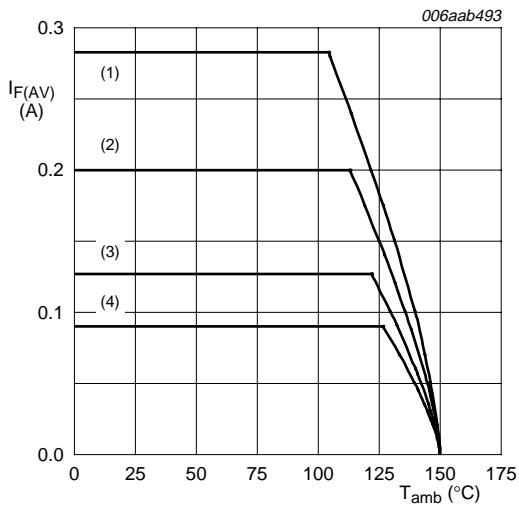
**Fig 6. Diode capacitance as a function of reverse voltage; typical values**



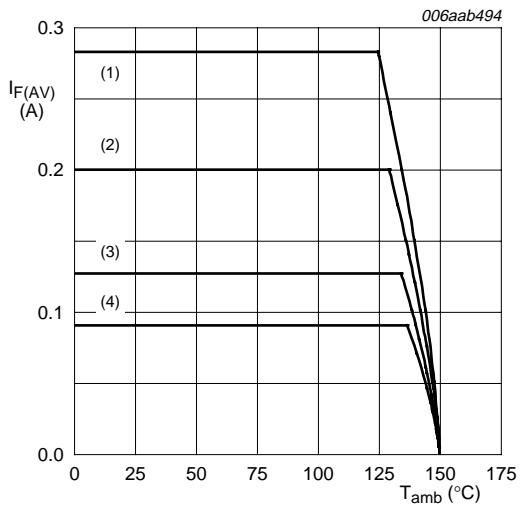
**Fig 7.** Average forward power dissipation as a function of average forward current; typical values



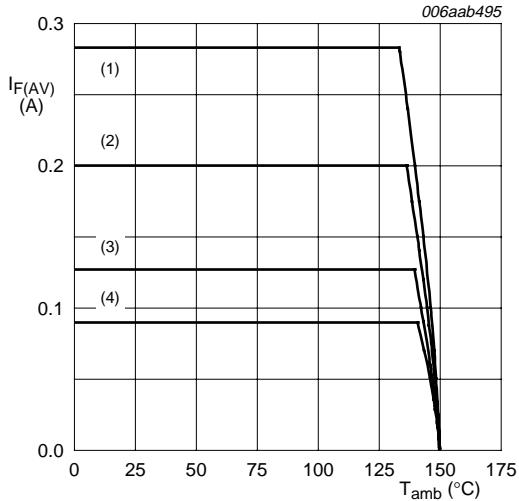
**Fig 8.** Average reverse power dissipation as a function of reverse voltage; typical values



**Fig 9.** Average forward current as a function of ambient temperature; typical values



**Fig 10.** Average forward current as a function of ambient temperature; typical values

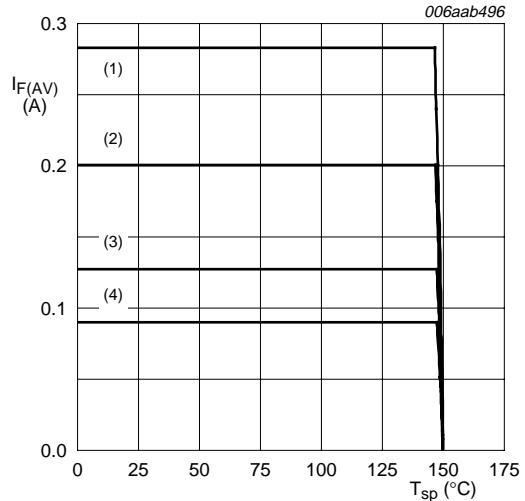


Ceramic PCB,  $\text{Al}_2\text{O}_3$ , standard footprint

$T_j = 150^\circ\text{C}$

- (1)  $\delta = 1$ ; DC
- (2)  $\delta = 0.5$ ;  $f = 20$  kHz
- (3)  $\delta = 0.2$ ;  $f = 20$  kHz
- (4)  $\delta = 0.1$ ;  $f = 20$  kHz

Fig 11. Average forward current as a function of ambient temperature; typical values

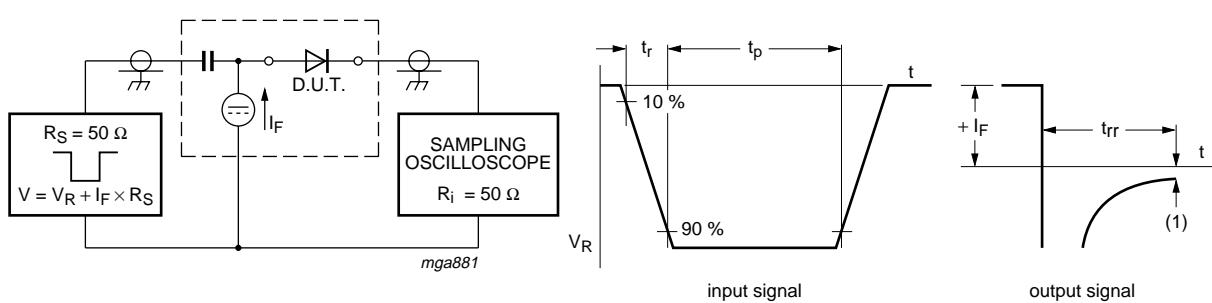


$T_j = 150^\circ\text{C}$

- (1)  $\delta = 1$ ; DC
- (2)  $\delta = 0.5$ ;  $f = 20$  kHz
- (3)  $\delta = 0.2$ ;  $f = 20$  kHz
- (4)  $\delta = 0.1$ ;  $f = 20$  kHz

Fig 12. Average forward current as a function of solder point temperature; typical values

## 8. Test information

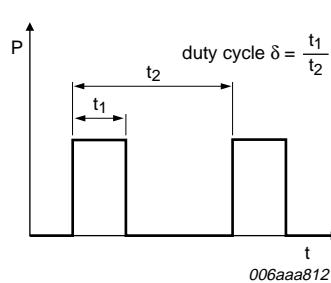


- (1)  $I_R = 1$  mA

Input signal: reverse pulse rise time  $t_r = 0.6$  ns; reverse voltage pulse duration  $t_p = 100$  ns; duty cycle  $\delta = 0.05$

Oscilloscope: rise time  $t_r = 0.35$  ns

Fig 13. Reverse recovery time test circuit and waveforms

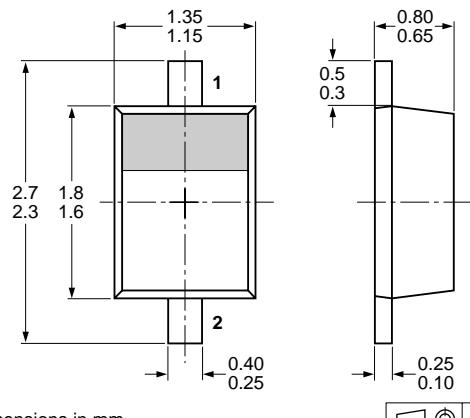
**Fig 14. Duty cycle definition**

The current ratings for the typical waveforms as shown in [Figure 9](#), [10](#), [11](#) and [12](#) are calculated according to the equations:  $I_{F(AV)} = I_M \times \delta$  with  $I_M$  defined as peak current,  $I_{RMS} = I_{F(AV)}$  at DC, and  $I_{RMS} = I_M \times \sqrt{\delta}$  with  $I_{RMS}$  defined as RMS current.

## 8.1 Quality information

This product has been qualified in accordance with the Automotive Electronics Council (AEC) standard *Q101 - Stress test qualification for discrete semiconductors*, and is suitable for use in automotive applications.

## 9. Package outline

**Fig 15. Package outline SOD323F (SC-90)**

## 10. Packing information

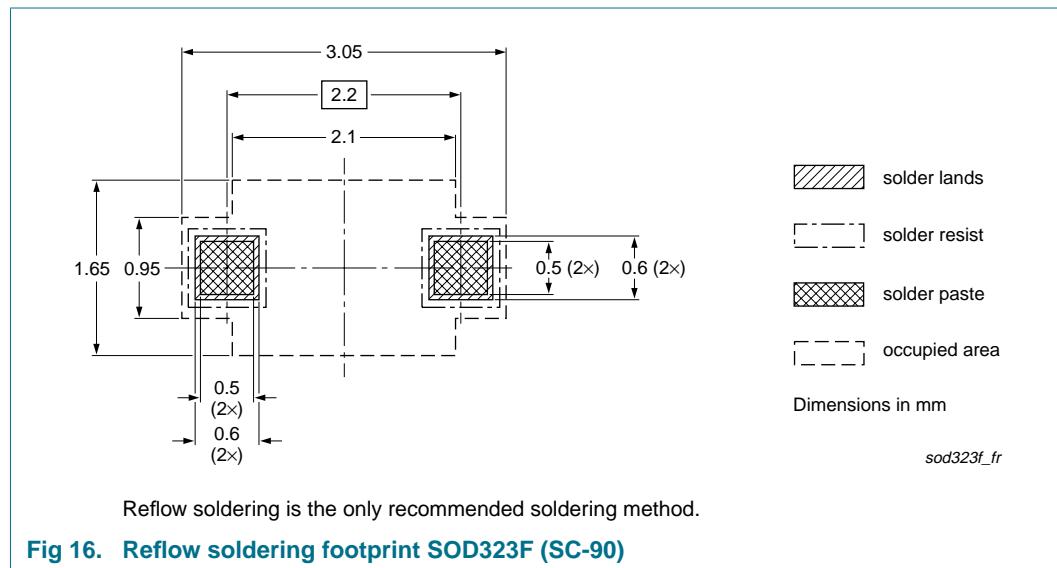
**Table 8. Packing methods**

The indicated -xxx are the last three digits of the 12NC ordering code.<sup>[1]</sup>

Type number	Package	Description	Packing quantity
PMEG4002EJ	SOD323F	4 mm pitch, 8 mm tape and reel	3000      10000 -115      -135

[1] For further information and the availability of packing methods, see [Section 14](#).

## 11. Soldering



## 12. Revision history

**Table 9. Revision history**

Document ID	Release date	Data sheet status	Change notice	Supersedes
PMEG4002EJ_1	20090515	Product data sheet	-	-

## 13. Legal information

### 13.1 Data sheet status

Document status <sup>[1][2]</sup>	Product status <sup>[3]</sup>	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
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