

# Chip Multilayer Ceramic Capacitors for General

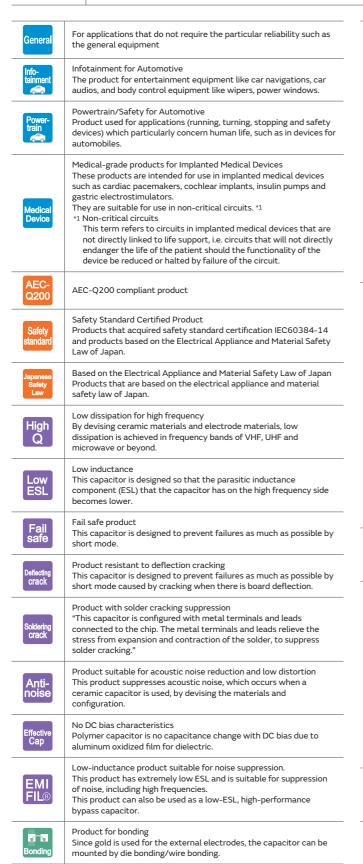




# Explanation of Symbols in This Catalog



Links are provided to the latest information from the PDF version of the catalog, which is available on the web.

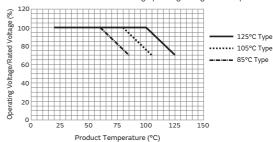


#### Derating 1

This product is suitable when a voltage continuously applied to a capacitor in an operating circuit, is used below (derated) the rated voltage of the capacitor. This model guarantees the test conditions in the endurance test, at a rated voltage x 100% at the maximum operating temperature. A reliability assurance level equivalent to a common product can be secured, by using this product within the voltage and temperature derated conditions recommended in the figure below.

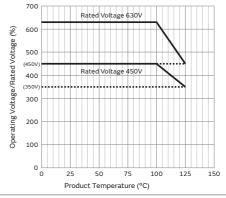
D1

Recommended Conditions of the Derating Operating Voltage and Temperature



#### Derating 2

When the product temperature exceeds  $105^{\circ}$ C, please use this product within the voltage and temperature derated conditions in the figure below.



D3

D2

Derating 3

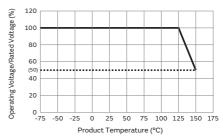
Please apply the derating curve according to the operating temperature.

Please refer to detailed specifications sheet for details.

Derating 4

When the product temperature exceeds 125°C, please use this product within the voltage and temperature derated conditions in the figure below.





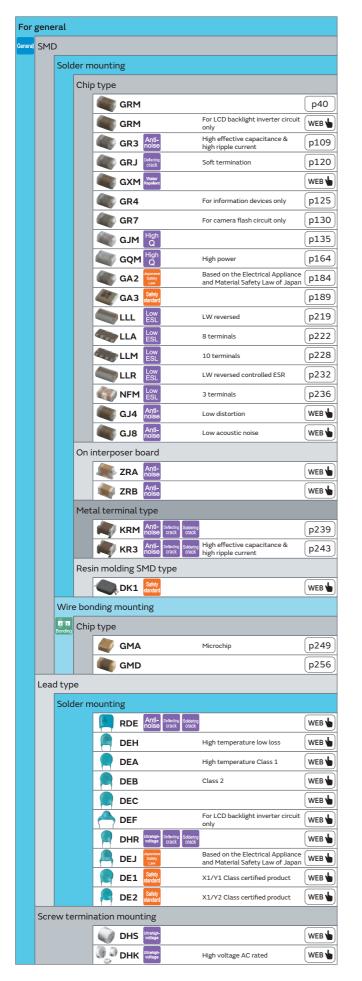
D5

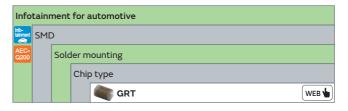
Derating
5

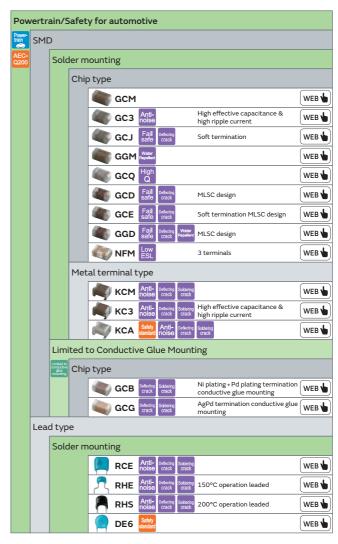
Derating 5

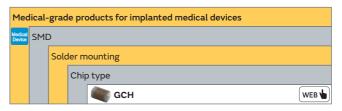
Please apply the rated voltage derating over 150 °C. Please refer to detailed specifications sheet for details.

# Selection Guide for Capacitors









### Part Numbering

Chip Multilayer Ceramic Capacitors for General



(Part Number)

GR M 18 8 B1 1H 102 K A01 D

#### 1 Product ID 2 Series

Product ID	Code	Series
GA	2	Based on the Electrical Appliance and Material Safety Law of Japan Chip Multilayer Ceramic Capacitors for General Purpose
GA	3	Safety Standard Certified Chip Multilayer Ceramic Capacitors for General Purpose
GJ	М	High Q Chip Multilayer Ceramic Capacitors for General Purpose
OM	Α	Wire Bonding Mount Multilayer Microchip Capacitors for General Purpose
GM	D	Wire Bonding/AuSn Soldering Mount Chip Multilayer Ceramic Capacitors for General Purpose
GQ	М	High Q and High Power Chip Multilayer Ceramic Capacitors for General Purpose
	3	High Effective Capacitance & High Ripple Current Chip Multilayer Ceramic Capacitors for General Purpose
	4	Chip Multilayer Ceramic Capacitors for Camera Flash Circuit only
GR	7	Chip Multilayer Ceramic Capacitors for Ethernet LAN and Primary-secondary Coupling of DC-DC Converters
	J	Soft Termination Chip Multilayer Ceramic Capacitors for General Purpose
	М	Chip Multilayer Ceramic Capacitors for General Purpose
KR	3	High Effective Capacitance & High Allowable Ripple Current Metal Terminal Type Multilayer Ceramic Capacitors for General Purpose
KK	М	Metal Terminal Type Multilayer Ceramic Capacitors for General Purpose
LL	Α	8 Terminals Low ESL Chip Multilayer Ceramic Capacitors for General Purpose
	L	LW Reversed Low ESL Chip Multilayer Ceramic Capacitors for General Purpose
LL	М	10 Terminals Low ESL Chip Multilayer Ceramic Capacitors for General Purpose
	R	LW Reversed Controlled ESR Low ESL Chip Multilayer Ceramic Capacitors for General Purpose

### 3Chip Dimensions (LxW)

Code	Dimensions (LxW)	EIA
02	0.4x0.2mm	01005
OD	0.38x0.38mm	015015
03	0.6x0.3mm	0201
05	0.5x0.5mm	0202
08	0.8x0.8mm	0303
10	0.6x1.0mm	02404
15	1.0x0.5mm	0402
18	1.6x0.8mm	0603
21	2.0x1.25mm	0805
22	2.8x2.8mm	1111
31	3.2x1.6mm	1206
32	3.2x2.5mm	1210
42	4.5x2.0mm	1808
43	4.5x3.2mm	1812
52	5.7x2.8mm	2211
55	5.7x5.0mm	2220

Continued on the following page.  $\nearrow$ 

(Part Number)

GR M 18 8 B1 1H 102 K A01 D

9 9 9 9 6 0 0 9 9 0

### Continued from the preceding page. $\searrow$

#### 4 Height Dimension (T) (Except KR□)

Code	Dimension (T)
2	0.2mm
3	0.3mm
4	0.4mm
5	0.5mm
6	0.6mm
7	0.7mm
8	0.8mm
9	0.85mm
Α	1.0mm
В	1.25mm
С	1.6mm
D	2.0mm
E	2.5mm
М	1.15mm
Q	1.5mm
Х	Depends on individual standards.

#### 4Height Dimension (T) (KR□ Only)

Code	Dimension (T)
E	1.8mm
F	1.9mm
K	2.7mm
L	2.8mm
Q	3.7mm
Т	4.8mm
W	6.4mm

#### **⑤**Temperature Characteristics

Temperature Temper												
Characteristic Codes		odes	Temperature Characteristics		Operating	Capacitance Change Each Temperature (%)						
Code	Public		Reference	Temperature (	Capacitance Change or Temperature	Temperature Range	-55°C		*6		-10°C	
Code	STD Co	de	Temperature	Range	Coefficient	0	Max.	Min.	Max.	Min.	Max.	Min.
1X	SL	JIS	20°C	20 to 85°C	+350 to -1000ppm/°C	–55 to 125°C	-	-	-	-	-	-
2C	СН	JIS	20°C	20 to 125°C	0±60ppm/°C	–55 to 125°C	0.82	-0.45	0.49	-0.27	0.33	-0.18
3C	CJ	JIS	20°C	20 to 125°C	0±120ppm/°C	–55 to 125°C	1.37	-0.9	0.82	-0.54	0.55	-0.36
3U	UJ	JIS	20°C	20 to 85°C	-750±120ppm/°C	–25 to 85°C	-	-	4.94	2.84	3.29	1.89
4C	CK	JIS	20°C	20 to 125°C	0±250ppm/°C	–55 to 125°C	2.56	-1.88	1.54	-1.13	1.02	-0.75
5C	COG	EIA	25°C	25 to 125°C	0±30ppm/°C	–55 to 125°C	0.58	-0.24	0.4	-0.17	0.25	-0.11
5G	X8G	*2	25°C	25 to 150°C	0±30ppm/°C	–55 to 150°C	0.58	-0.24	0.4	-0.17	0.25	-0.11
7U	U2J	EIA	25°C	25 to 125°C *3	-750±120ppm/°C	–55 to 125°C	8.78	5.04	6.04	3.47	3.84	2.21
B1	B *1	JIS	20°C	−25 to 85°C	±10%	–25 to 85°C	-	-	-	-	-	-
В3	В	JIS	20°C	−25 to 85°C	±10%	–25 to 85°C	-	-	-	-	-	-
С7	X7S	EIA	25°C	-55 to 125°C	±22%	–55 to 125°C	-	-	-	-	-	-
C8	X6S	EIA	25°C	-55 to 105°C	±22%	–55 to 105°C	-	-	-	-	-	-
D7	X7T	EIA	25°C	-55 to 125°C	+22%, -33%	–55 to 125°C	-	-	-	-	-	-
D8	X6T	EIA	25°C	-55 to 105°C	+22%, -33%	–55 to 105°C	-	-	-	-	-	-
E7	X7U	EIA	25°C	-55 to 125°C	+22%, –56%	–55 to 125°C	-	-	-	-	-	-
R1	R *1	JIS	20°C	-55 to 125°C	±15%	–55 to 125°C	-	-	-	-	-	-
R6	X5R	EIA	25°C	−55 to 85°C	±15%	–55 to 85°C	-	-	-	-	-	-
R7	X7R	EIA	25°C	-55 to 125°C	±15%	–55 to 125°C	-	-	-	-	-	-
wo	V7T		25°C	FF. 40555	±10% *4	–55 to 125°C	-	-	-	-	-	-
WO	X7T	EIA	25°C	–55 to 125°C	+22%, -33% *5	-55 to 125°C	-	-	-	-	-	-

 $<sup>^{*}1</sup>$  Capacitance change is specified with 50% rated voltage applied.

Continued on the following page.  ${\cal J}$ 

 $<sup>^{*}2</sup>$  Murata Temperature Characteristic Code.

<sup>\*3</sup> Rated Voltage 100Vdc max: 25 to 85°C

<sup>\*4</sup> Apply DC350V bias.

<sup>\*5</sup> No DC bias.

<sup>\*6 –25°</sup>C (Reference Temperature 20°C) / –30°C (Reference Temperature 25°C)

(Part Number)

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#### Continued from the preceding page.

#### **6**Rated Voltage

- rated veltage	
Code	Rated Voltage
OE	DC2.5V
0G	DC4V
٥٦	DC6.3V
1A	DC10V
1C	DC16V
1E	DC25V
1H	DC50V
1J	DC63V
1K	DC80V
2A	DC100V
2D	DC200V
2E	DC250V
2W	DC450V
2H	DC500V
2J	DC630V
ЗА	DC1kV
3D	DC2kV
3F	DC3.15kV
BB	DC350V
E2	AC250V
GB	X2; AC250V (Safety Standard Certified Type GB)
GD	Y3; AC250V (Safety Standard Certified Type GD)
GF	Y2, X1/Y2; AC250V (Safety Standard Certified Type GF)
YA	DC35V

#### Capacitance

Expressed by three-digit alphanumerics. The unit is picofarad (pF). The first and second figures are significant digits, and the third figure expresses the number of zeros which follow the two numbers. If there is a decimal point, it is expressed by the capital letter "R." In this case, all figures are significant digits. If any alphabet, other than "R", is included, this indicates the specific part number is a non-standard part.

Ex.)	Code	Capacitance
	R50	0.50pF
	1R0	1.0pF
	100	10pF
	103	10000pF

#### 8 Capacitance Tolerance

Code	Capacitance Tolerance	
В	±0.1pF	
С	±0.25pF	
D	±0.5pF (Less than 10pF)	
Ь	±0.5% (10pF and over)	
F	±1%	
G	±2%	
J	±5%	
K	±10%	
М	±20%	
W	±0.05pF	

**9**Individual Specification Code (Except **LLR**) Expressed by three figures.

### **9**ESR (**LLR** Only)

Code	ESR
E01	100mΩ
E03	220mΩ
E05	470mΩ
E07	1000mΩ

#### Packaging

Code	Packaging	
L	ø180mm Embossed Taping	
D/E/W	ø180mm Paper Taping	
K	ø330mm Embossed Taping	
J/F	ø330mm Paper Taping	
Т	Bulk Tray	

Please contact us if you find any part number not provided in this table.

GR7

Ξ

KR3

High Q and High Power Chip Multilayer Ceramic Capacitors for General Purpose

# **GQM** Series





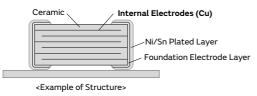


## High Frequency Capacitor Ideal for PA Design of Base Stations

#### **Features**

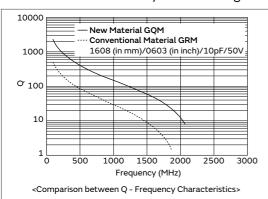
Mainly ideal for base stations of mobile communication devices and temperature compensation of related modules.

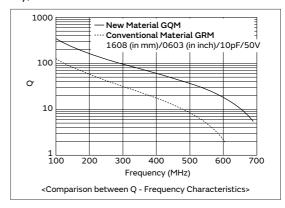
This product is ideal for temperature compensation of high frequency circuits, such as resonant circuits, tuning circuits, and impedance matching circuits where the operating characteristics of the device are greatly affected by the capacitance fluctuation.



High Q and low ESR in VHF, UHF and microwave frequency bands.

High Q and low ESR were achieved at a high frequency by adopting ceramic material as the dielectric material which enables an extremely low loss at high frequency, and base metal electrodes as the internal electrodes.





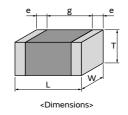
### (3) Can be used for tight tolerance.

In addition to standard tolerance, the allowable range of this product is also suitable for the following narrow tolerance.

Capacitance Range	Standard Capacitance Tolerance (Capacitance Tolerance Symbol)	Narrow Capacitance Tolerance (Capacitance Tolerance Symbol)			
to 0.9pF	±0.1pF (B)	±0.05pF (W)			
1.0 to 5.0pF	±0.25pF (C)	±0.05pF (W), ±0.1pF (B)			
5.1 to 9.9pF	±0.5pF (D)	±0.05pF (W), ±0.1pF (B), ±0.25pF (C)			
10pF to	±5% (J)	±2% (G)			

## Specifications

Size (mm)	1.0×0.5mm to 2.8×2.8mm
Rated Voltage	50Vdc to 500Vdc
Capacitance	0.10pF to 510pF
Main Applications	Measuring instruments, other ultra compact/thin devices



This catalog contains only a portion of the product lineup.

Please refer to the capacitor search tool on the Murata Web site for details.

# GQM Series Temperature Compensating Type Part Number List

#### 1 0×0 5mm

1.0×0.	5mm					
T max.	Rated Voltage	TC Code	Cap.	Tol.	Part Number	p*
0.55mm	200Vdc	COG	0.10pF	±0.1pF	GQM1555C2DR10BB01#	p172
			0.20pF	±0.1pF	GQM1555C2DR20BB01#	p172
			0.30pF	±0.1pF	GQM1555C2DR30BB01#	p172
				±0.25pF	GQM1555C2DR30CB01#	p172
			0.40pF	±0.1pF	GQM1555C2DR40BB01#	p172
				±0.25pF	GQM1555C2DR40CB01#	p172
			0.50pF	±0.1pF	GQM1555C2DR50BB01#	p172
				±0.25pF	GQM1555C2DR50CB01#	p172
			0.60pF	±0.1pF	GQM1555C2DR60BB01#	p172
				±0.25pF	GQM1555C2DR60CB01#	p172
			0.70pF	±0.1pF	GQM1555C2DR70BB01#	p172
				±0.25pF	GQM1555C2DR70CB01#	p172
			0.75pF	±0.1pF	GQM1555C2DR75BB01#	p172
				±0.25pF	GQM1555C2DR75CB01#	p172
			0.80pF	±0.1pF	GQM1555C2DR80BB01#	p172
				-	GQM1555C2DR80CB01#	p172
			0.90pF	±0.1pF	GQM1555C2DR90BB01#	p172
				±0.25pF	GQM1555C2DR90CB01#	p172
			1.0pF	±0.1pF	GQM1555C2D1R0BB01#	p172
					GQM1555C2D1R0CB01#	p172
			1.1pF	±0.1pF	GQM1555C2D1R1BB01#	p172
			1.2-5		GQM1555C2D1R1CB01#	p172
			1.2pF	±0.1pF	GQM1555C2D1R2BB01#	p172
			1 255		GQM1555C2D1R2CB01#	p172
			1.3pF	±0.1pF	GQM1555C2D1R3BB01# GQM1555C2D1R3CB01#	p172
			1.5pF	±0.25pF ±0.1pF	GQM1555C2D1R5BB01#	p172 p172
			т.эрі	±0.25pF	GQM1555C2D1R5CB01#	p172
			1.6pF	±0.1pF	GQM1555C2D1R6BB01#	p172
			2.00.	±0.25pF	GQM1555C2D1R6CB01#	p172
			1.8pF	±0.1pF		p172
					GQM1555C2D1R8CB01#	<u> </u>
			2.0pF	±0.1pF	GQM1555C2D2R0BB01#	p172
			•	±0.25pF	GQM1555C2D2R0CB01#	p172
			2.2pF	±0.1pF	GQM1555C2D2R2BB01#	p172
			•		GQM1555C2D2R2CB01#	p172
			2.4pF	±0.1pF	GQM1555C2D2R4BB01#	p172
				±0.25pF	GQM1555C2D2R4CB01#	p172
			2.7pF	±0.1pF	GQM1555C2D2R7BB01#	p172
				±0.25pF	GQM1555C2D2R7CB01#	p172
			3.0pF	±0.1pF	GQM1555C2D3R0BB01#	p172
				±0.25pF	GQM1555C2D3R0CB01#	p172
			3.3pF	±0.1pF	GQM1555C2D3R3BB01#	p172
				±0.25pF	GQM1555C2D3R3CB01#	p172
			3.6pF	±0.1pF	GQM1555C2D3R6BB01#	p172
				±0.25pF	GQM1555C2D3R6CB01#	p172
			3.9pF	±0.1pF	GQM1555C2D3R9BB01#	p172
				±0.25pF	GQM1555C2D3R9CB01#	p172
			4.0pF	±0.1pF	GQM1555C2D4R0BB01#	p172
				±0.25pF	GQM1555C2D4R0CB01#	p172
			4.3pF	±0.1pF	GQM1555C2D4R3BB01#	p172
				±0.25pF	GQM1555C2D4R3CB01#	p172

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T max.	Rated Voltage	TC Code	Сар.	Tol.	Part Number	p*
0.55mm	200Vdc	COG	4.7pF	±0.1pF	GQM1555C2D4R7BB01#	p172
				±0.25pF	GQM1555C2D4R7CB01#	p172
			5.0pF	±0.1pF	GQM1555C2D5R0BB01#	p172
				±0.25pF	GQM1555C2D5R0CB01#	p172
			5.1pF	±0.1pF	GQM1555C2D5R1BB01#	p172
				±0.25pF	GQM1555C2D5R1CB01#	p172
			5.6pF	±0.1pF	GQM1555C2D5R6BB01#	p172
				±0.25pF	GQM1555C2D5R6CB01#	p172
			6.0pF	±0.1pF	GQM1555C2D6R0BB01#	p172
				±0.25pF	GQM1555C2D6R0CB01#	p172
			6.2pF	±0.1pF	GQM1555C2D6R2BB01#	p172
				±0.25pF	GQM1555C2D6R2CB01#	p172
			6.8pF	±0.1pF	GQM1555C2D6R8BB01#	p172
				±0.25pF	GQM1555C2D6R8CB01#	p172
			7.0pF	±0.1pF	GQM1555C2D7R0BB01#	p172
				±0.25pF	GQM1555C2D7R0CB01#	p172
			7.5pF	±0.1pF	GQM1555C2D7R5BB01#	p172
				±0.25pF	GQM1555C2D7R5CB01#	p172
			8.0pF	±0.1pF	GQM1555C2D8R0BB01#	p172
				±0.25pF	GQM1555C2D8R0CB01#	p172
			8.2pF	±0.1pF	GQM1555C2D8R2BB01#	p172
				±0.25pF	GQM1555C2D8R2CB01#	p172
			9.0pF	±0.1pF	GQM1555C2D9R0BB01#	p172
				±0.25pF	GQM1555C2D9R0CB01#	p172
			9.1pF	±0.1pF	GQM1555C2D9R1BB01#	p172
				±0.25pF	GQM1555C2D9R1CB01#	p172
			10pF	±2%	GQM1555C2D100GB01#	p172
				±5%	GQM1555C2D100JB01#	p172
			11pF	±2%	GQM1555C2D110GB01#	p172
				±5%	GQM1555C2D110JB01#	p172
			12pF	±2%	GQM1555C2D120GB01#	p172
				±5%	GQM1555C2D120JB01#	p172
			13pF	±2%	GQM1555C2D130GB01#	p172
				±5%	GQM1555C2D130JB01#	p172
			15pF	±2%	GQM1555C2D150GB01#	p172
				±5%	GQM1555C2D150JB01#	p172
			16pF	±2%	GQM1555C2D160GB01#	p172
				±5%	GQM1555C2D160JB01#	p172
			18pF	±2%	GQM1555C2D180GB01#	p172
				±5%	GQM1555C2D180JB01#	p172
			20pF	±2%	GQM1555C2D200GB01#	p172
				±5%	GQM1555C2D200JB01#	p172
			22pF	±2%	GQM1555C2D220GB01#	p172
				±5%	GQM1555C2D220JB01#	p172
			24pF	±2%	GQM1555C2D240GB01#	p172
				±5%	GQM1555C2D240JB01#	p172
			27pF	±2%	GQM1555C2D270GB01#	p172
				±5%	GQM1555C2D270JB01#	p172
			30pF	±2%	GQM1555C2D300GB01#	p172
				±5%	GQM1555C2D300JB01#	p172
			33pF	±2%	GQM1555C2D330GB01#	p172
			•	±5%	GQM1555C2D330JB01#	p172
	100Vdc	COG	36pF	±2%	GQM1555C2A360GB01#	p172
			•	±5%	GQM1555C2A360JB01#	p172
						<u> </u>

Part number # indicates the package specification code.

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 $<sup>\</sup>ensuremath{^*:}$  Refers to the page of the "Specifications and Test Methods".

Part Number ±0.5pF **GQM1875C2E6R0DB12#** p178

±0.25pF **GQM1875C2E6R2CB12#** p178 ±0.5pF **GQM1875C2E6R2DB12#** p178

0.8mm

250Vdc

COG

# GR4

# $\exists$

# KR3

# GRJ

# GP GD

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ঠ	녽	
	_	

# 166

# GQM Series Temperature Compensating Type Part Number List

(→ 1.0×0.5mm)

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T max.	Rated Voltage	TC Code	Cap.	Tol.	Part Number	p*
0.55mm	100Vdc	COG	39pF	±2%	GQM1555C2A390GB01#	p172
				±5%	GQM1555C2A390JB01#	p172
			43pF	±2%	GQM1555C2A430GB01#	p172
				±5%	GQM1555C2A430JB01#	p172
			47pF	±2%	GQM1555C2A470GB01#	p172
				±5%	GOM1555C2A470JB01#	p172

				±5%	GQM1555C2A470JB01#	p172									
1.6×0	.8mm														
T max.	Rated Voltage	TC Code	Cap.	Tol.	Part Number	p*									
0.8mm	250Vdc	COG	1.0pF	±0.1pF	GQM1875C2E1R0BB12#	p178									
				±0.25pF	GQM1875C2E1R0CB12#	p178									
			1.1pF	±0.1pF	GQM1875C2E1R1BB12#	p178									
				±0.25pF	GQM1875C2E1R1CB12#	p178									
			1.2pF	±0.1pF	GQM1875C2E1R2BB12#	p178									
				±0.25pF	GQM1875C2E1R2CB12#	p178									
			1.3pF	±0.1pF	GQM1875C2E1R3BB12#	p178									
				±0.25pF	GQM1875C2E1R3CB12#	p178									
			1.5pF	±0.1pF	GQM1875C2E1R5BB12#	p178									
				±0.25pF	GQM1875C2E1R5CB12#	p178									
			1.6pF	±0.1pF	GQM1875C2E1R6BB12#	p178									
				±0.25pF	GQM1875C2E1R6CB12#	p178									
			1.8pF	±0.1pF	GQM1875C2E1R8BB12#	p178									
				±0.25pF	GQM1875C2E1R8CB12#	p178									
			2.0pF	±0.1pF	GQM1875C2E2R0BB12#	p178									
				±0.25pF	GQM1875C2E2R0CB12#	p178									
			2.2pF	±0.1pF	GQM1875C2E2R2BB12#	p178									
				±0.25pF	GQM1875C2E2R2CB12#	p178									
			2.4pF	±0.1pF	GQM1875C2E2R4BB12#	p178									
				±0.25pF	GQM1875C2E2R4CB12#	p178									
			2.7pF	±0.1pF	GQM1875C2E2R7BB12#	p178									
				±0.25pF	GQM1875C2E2R7CB12#	p178									
			3.0pF	±0.1pF	GQM1875C2E3R0BB12#	p178									
													±0.25pF	GQM1875C2E3R0CB12#	p178
			3.3pF	±0.1pF	GQM1875C2E3R3BB12#	p178									
				±0.25pF	GQM1875C2E3R3CB12#	p178									
			3.6pF	±0.1pF	GQM1875C2E3R6BB12#	p178									
				±0.25pF	GQM1875C2E3R6CB12#	p178									
			3.9pF	±0.1pF	GQM1875C2E3R9BB12#	p178									
				±0.25pF	GQM1875C2E3R9CB12#	p178									
			4.0pF	±0.1pF	GQM1875C2E4R0BB12#	p178									
				±0.25pF	GQM1875C2E4R0CB12#	p178									
			4.3pF	±0.1pF	GQM1875C2E4R3BB12#	p178									
				±0.25pF	GQM1875C2E4R3CB12#	p178									
			4.7pF	±0.1pF	GQM1875C2E4R7BB12#	p178									
				±0.25pF	GQM1875C2E4R7CB12#	p178									
			5.0pF	±0.1pF	GQM1875C2E5R0BB12#	p178									
				±0.25pF	GQM1875C2E5R0CB12#	p178									
			5.1pF	±0.25pF	GQM1875C2E5R1CB12#	p178									
				±0.5pF	GQM1875C2E5R1DB12#	p178									
			5.6pF	±0.25pF	GQM1875C2E5R6CB12#	p178									
				±0.5pF	GQM1875C2E5R6DB12#	p178									
			6.0pF	±0.25pF	GQM1875C2E6R0CB12#	p178									

			±0.5pF	GQM1875C2E6R2DB12#	p1/8
		6.8pF	±0.25pF	GQM1875C2E6R8CB12#	p178
			±0.5pF	GQM1875C2E6R8DB12#	p178
		7.0pF	±0.25pF	GQM1875C2E7R0CB12#	p178
			±0.5pF	GQM1875C2E7R0DB12#	p178
		7.5pF	±0.25pF	GQM1875C2E7R5CB12#	p178
			±0.5pF	GQM1875C2E7R5DB12#	p178
		8.0pF	±0.25pF	GQM1875C2E8R0CB12#	p178
			±0.5pF	GQM1875C2E8R0DB12#	p178
		8.2pF	±0.25pF	GQM1875C2E8R2CB12#	p178
			±0.5pF	GQM1875C2E8R2DB12#	p178
		9.0pF	±0.25pF	GQM1875C2E9R0CB12#	p178
			±0.5pF	GQM1875C2E9R0DB12#	p178
		9.1pF	±0.25pF	GQM1875C2E9R1CB12#	p178
			±0.5pF	GQM1875C2E9R1DB12#	p178
		10pF	±2%	GQM1875C2E100GB12#	p178
			±5%	GQM1875C2E100JB12#	p178
		11pF	±2%	GQM1875C2E110GB12#	p178
			±5%	GQM1875C2E110JB12#	p178
		12pF	±2%	GQM1875C2E120GB12#	p178
			±5%	GQM1875C2E120JB12#	p178
		13pF	±2%	GQM1875C2E130GB12#	p178
			±5%	GQM1875C2E130JB12#	p178
		15pF	±2%	GQM1875C2E150GB12#	p178
			±5%	GQM1875C2E150JB12#	p178
		16pF	±2%	GQM1875C2E160GB12#	p178
			±5%	GQM1875C2E160JB12#	p178
		18pF	±2%	GQM1875C2E180GB12#	p178
			±5%	GQM1875C2E180JB12#	p178
		20pF	±2%	GQM1875C2E200GB12#	p178
			±5%	GQM1875C2E200JB12#	p178
		22pF	±2%	GQM1875C2E220GB12#	p178
			±5%	GQM1875C2E220JB12#	p178
		24pF	±2%	GQM1875C2E240GB12#	p178
			±5%	GQM1875C2E240JB12#	p178
		27pF	±2%	GQM1875C2E270GB12#	p178
			±5%	GQM1875C2E270JB12#	p178
		30pF	±2%	GQM1875C2E300GB12#	p178
			±5%	GQM1875C2E300JB12#	p178
		33pF	±2%	GQM1875C2E330GB12#	p178
			±5%	GQM1875C2E330JB12#	p178
		36pF	±2%	GQM1875C2E360GB12#	p178
			±5%	GQM1875C2E360JB12#	p178
		39pF	±2%	GQM1875C2E390GB12#	p178
			±5%	GQM1875C2E390JB12#	p178
		43pF	±2%	GQM1875C2E430GB12#	p178
			±5%	GQM1875C2E430JB12#	p178
		47pF	±2%	GQM1875C2E470GB12#	p178
			±5%	GQM1875C2E470JB12#	p178
	X8G	1.0pF	±0.1pF	GQM1875G2E1R0BB12#	p175
			±0.25pF	GQM1875G2E1R0CB12#	p175
		1.1pF	±0.1pF	GQM1875G2E1R1BB12#	p175
		Part nun	nber # indi	cates the package specification	ı code.
Rata					

Сар.

6.0pF

6.2pF

# GQM Series Temperature Compensating Type Part Number List

(→ 1.6	«0.8mm	1)	•		•	
T max.	Rated Voltage	TC Code	Cap.	Tol.	Part Number	p*
0.8mm	250Vdc	X8G	1.1pF	±0.25pF	GQM1875G2E1R1CB12#	p175
			1.2pF	±0.1pF	GQM1875G2E1R2BB12#	p175
				±0.25pF	GQM1875G2E1R2CB12#	p175
			1.3pF	±0.1pF	GQM1875G2E1R3BB12#	p175
				±0.25pF	GQM1875G2E1R3CB12#	p175
			1.5pF	±0.1pF	GQM1875G2E1R5BB12#	p175
					GQM1875G2E1R5CB12#	p175
			1.6pF	-	GQM1875G2E1R6BB12#	p175
			105		GQM1875G2E1R6CB12#	p175
			1.8pF	<u> </u>	GQM1875G2E1R8BB12#	p175
			2.0pF		GQM1875G2E1R8CB12#	p175
			2.0pr	±0.1pF	GQM1875G2E2R0BB12# GQM1875G2E2R0CB12#	p175
			2.2pF	· ·	GQM1875G2E2R2BB12#	p175
			2.201	· ·	GQM1875G2E2R2CB12#	p175
			2.4pF	· ·	GQM1875G2E2R4BB12#	p175
				<u> </u>	GQM1875G2E2R4CB12#	p175
			2.7pF	±0.1pF	GQM1875G2E2R7BB12#	p175
				±0.25pF	GQM1875G2E2R7CB12#	p175
			3.0pF	±0.1pF	GQM1875G2E3R0BB12#	p175
				±0.25pF	GQM1875G2E3R0CB12#	p175
			3.3pF	±0.1pF	GQM1875G2E3R3BB12#	p175
				±0.25pF	GQM1875G2E3R3CB12#	p175
			3.6pF	±0.1pF	GQM1875G2E3R6BB12#	p175
				±0.25pF	GQM1875G2E3R6CB12#	p175
			3.9pF	±0.1pF	GQM1875G2E3R9BB12#	p175
				±0.25pF	GQM1875G2E3R9CB12#	p175
			4.0pF	±0.1pF	GQM1875G2E4R0BB12#	p175
				· ·	GQM1875G2E4R0CB12#	p175
			4.3pF	±0.1pF	GQM1875G2E4R3BB12#	p175
			4.7pF	· ·	GQM1875G2E4R3CB12# GQM1875G2E4R7BB12#	p175 p175
			4.7 pr	<u> </u>	GQM1875G2E4R7CB12#	p175
			5.0pF		GQM1875G2E5R0BB12#	p175
				<u> </u>	GQM1875G2E5R0CB12#	p175
			5.1pF	· ·	GQM1875G2E5R1CB12#	p175
			·	±0.5pF	GQM1875G2E5R1DB12#	p175
			5.6pF	±0.25pF	GQM1875G2E5R6CB12#	p175
				±0.5pF	GQM1875G2E5R6DB12#	p175
			6.0pF	±0.25pF	GQM1875G2E6R0CB12#	p175
				±0.5pF	GQM1875G2E6R0DB12#	p175
			6.2pF	±0.25pF	GQM1875G2E6R2CB12#	p175
				±0.5pF	GQM1875G2E6R2DB12#	p175
			6.8pF	±0.25pF	GQM1875G2E6R8CB12#	p175
					GQM1875G2E6R8DB12#	p175
			7.0pF		GQM1875G2E7R0CB12#	p175
			7 5 5		GQM1875G2E7R0DB12#	p175
			7.5pF	<u> </u>	GQM1875G2E7R5CB12#	p175
			Q OpE		GQM1875G2E7R5DB12#	p175
			8.0pF	±0.25pF ±0.5pF	GQM1875G2E8R0CB12# GQM1875G2E8R0DB12#	p175 p175
			8.2pF	· ·	GQM1875G2E8R0DB12#	p175
			о. <u>-</u> р.	±0.5pF	GQM1875G2E8R2DB12#	p175
			9.0pF	· ·	GQM1875G2E9R0CB12#	p175
			•			Ľ

T max.	Rated Voltage	TC Code	Cap.	Tol.	Part Number	p*
0.8mm	250Vdc	X8G	9.0pF	±0.5pF	GQM1875G2E9R0DB12#	p175
			9.1pF	±0.25pF	GQM1875G2E9R1CB12#	p175
				±0.5pF	GQM1875G2E9R1DB12#	p175
			10pF	±2%	GQM1875G2E100GB12#	p175
				±5%	GQM1875G2E100JB12#	p175
			11pF	±2%	GQM1875G2E110GB12#	p175
				±5%	GQM1875G2E110JB12#	p175
			12pF	±2%	GQM1875G2E120GB12#	p175
				±5%	GQM1875G2E120JB12#	p175
			13pF	±2%	GQM1875G2E130GB12#	p175
				±5%	GQM1875G2E130JB12#	p175
			15pF	±2%	GQM1875G2E150GB12#	p175
				±5%	GQM1875G2E150JB12#	p175
			16pF	±2%	GQM1875G2E160GB12#	p175
				±5%	GQM1875G2E160JB12#	p175
			18pF	±2%	GQM1875G2E180GB12#	p175
				±5%	GQM1875G2E180JB12#	p175
			20pF	±2%	GQM1875G2E200GB12#	p175
				±5%	GQM1875G2E200JB12#	p175
			22pF	±2%	GQM1875G2E220GB12#	p175
				±5%	GQM1875G2E220JB12#	p175
			24pF	±2%	GQM1875G2E240GB12#	p175
				±5%	GQM1875G2E240JB12#	p175
			27pF	±2%	GQM1875G2E270GB12#	p175
				±5%	GQM1875G2E270JB12#	p175
			30pF	±2%	GQM1875G2E300GB12#	p175
				±5%	GQM1875G2E300JB12#	p175

#### 2.0×1.25mm

T max.	Rated Voltage	TC Code	Cap.	Tol.	Part Number	p*					
1.0mm	500Vdc	X8G	1.0pF	±0.1pF	GQM2195G2H1R0BB12#	p175					
				±0.25pF	GQM2195G2H1R0CB12#	p175					
			1.1pF	±0.1pF	GQM2195G2H1R1BB12#	p175					
				±0.25pF	GQM2195G2H1R1CB12#	p175					
			1.2pF	±0.1pF	GQM2195G2H1R2BB12#	p175					
				±0.25pF	GQM2195G2H1R2CB12#	p175					
			1.3pF	±0.1pF	GQM2195G2H1R3BB12#	p175					
				±0.25pF	GQM2195G2H1R3CB12#	p175					
			1.5pF	±0.1pF	GQM2195G2H1R5BB12#	p175					
					±0.25pF	GQM2195G2H1R5CB12#	p175				
									1.6pF	±0.1pF	GQM2195G2H1R6BB12#
				±0.25pF	GQM2195G2H1R6CB12#	p175					
			1.8pF	±0.1pF	GQM2195G2H1R8BB12#	p175					
				±0.25pF	GQM2195G2H1R8CB12#	p175					
			2.0pF	±0.1pF	GQM2195G2H2R0BB12#	p175					
				±0.25pF	GQM2195G2H2R0CB12#	p175					
			2.2pF	±0.1pF	GQM2195G2H2R2BB12#	p175					
				±0.25pF	GQM2195G2H2R2CB12#	p175					
			2.4pF	±0.1pF	GQM2195G2H2R4BB12#	p175					
				±0.25pF	GQM2195G2H2R4CB12#	p175					
			2.7pF	±0.1pF	GQM2195G2H2R7BB12#	p175					
				±0.25pF	GQM2195G2H2R7CB12#	p175					

Part number # indicates the package specification code.

 $<sup>\</sup>ensuremath{^*:}$  Refers to the page of the "Specifications and Test Methods".

# GRM

GR3

GR7

GD C

GA3 GF

# GR4

 $\exists$ 

KR3

# 168

# GQM Series Temperature Compensating Type Part Number List

(→ 2.0×1.25mm)

T max.	Rated Voltage	TC Code	Cap.	Tol.	Part Number	p*
1.0mm	500Vdc	X8G	3.0pF	±0.1pF	GQM2195G2H3R0BB12#	p175
				±0.25pF	GQM2195G2H3R0CB12#	p175
			3.3pF	±0.1pF	GQM2195G2H3R3BB12#	p175
				±0.25pF	GQM2195G2H3R3CB12#	p175
			3.6pF	±0.1pF	GQM2195G2H3R6BB12#	p175
				±0.25pF	GQM2195G2H3R6CB12#	p175
			3.9pF	±0.1pF	GQM2195G2H3R9BB12#	p175
				±0.25pF	GQM2195G2H3R9CB12#	p175
			4.0pF	±0.1pF	GQM2195G2H4R0BB12#	p175
				±0.25pF	GQM2195G2H4R0CB12#	p175
			4.3pF	±0.1pF	GQM2195G2H4R3BB12#	p175
				±0.25pF	GQM2195G2H4R3CB12#	p175
			4.7pF	±0.1pF	GQM2195G2H4R7BB12#	p175
				±0.25pF	GQM2195G2H4R7CB12#	p175
			5.0pF		GQM2195G2H5R0BB12#	p175
			•	-	GQM2195G2H5R0CB12#	p175
			5.1pF		GQM2195G2H5R1CB12#	p175
				-	GQM2195G2H5R1DB12#	p175
			5.6pF		GQM2195G2H5R6CB12#	p175
				±0.5pF	GQM2195G2H5R6DB12#	p175
			6.0pF		GQM2195G2H6R0CB12#	p175
			•	±0.5pF	GQM2195G2H6R0DB12#	p175
			6.2pF		GQM2195G2H6R2CB12#	p175
				<u> </u>	GQM2195G2H6R2DB12#	p175
			6.8pF	-	GQM2195G2H6R8CB12#	p175
			0.00.	<u> </u>	GQM2195G2H6R8DB12#	p175
			7.0pF		GQM2195G2H7R0CB12#	p175
				±0.5pF	GQM2195G2H7R0DB12#	p175
			7.5pF		GQM2195G2H7R5CB12#	p175
			7.50	±0.5pF	GQM2195G2H7R5DB12#	p175
			8.0pF	<u> </u>	GQM2195G2H8R0CB12#	p175
			о.ор.	<u> </u>	GQM2195G2H8R0DB12#	p175
			8.2pF	•	-	p175
			0.201	<u> </u>	-	p175
			9.0pF	<u> </u>	GQM2195G2H9R0CB12#	p175
			э.орі	±0.25pi	GQM2195G2H9R0DB12#	i –
			0.1nE			p175 p175
			9.1pF		GQM2195G2H9R1CB12#	<del>-</del>
			10pF	±0.5pF	GQM2195G2H9R1DB12#	p175
			10pF	±2%	GQM2195G2H100GB12#	p175
			11nF	±5%	GQM2195G2H100JB12#	p175
			11pF	±2%	GQM2195G2H110GB12#	p175
			12-5	±5%	GQM2195G2H110JB12#	p175
			12pF	±2%	GQM2195G2H120GB12#	p175
			12-5	±5%	GQM2195G2H120JB12#	p175
			13pF	±2%	GQM2195G2H130GB12#	p175
			15	±5%	GQM2195G2H130JB12#	p175
			15pF	±2%	GQM2195G2H150GB12#	p175
			15 -	±5%	GQM2195G2H150JB12#	p175
			16pF	±2%	GQM2195G2H160GB12#	p175
				±5%	GQM2195G2H160JB12#	p175
			18pF	±2%	GQM2195G2H180GB12#	p175
				±5%	GQM2195G2H180JB12#	p175
			20pF	±2%	GQM2195G2H200GB12#	p175
				±5%	GQM2195G2H200JB12#	p175
*: Refers t	o the page	of the	"Specificat	ions and T	est Methods".	

-	Detector	то				
T max.	Rated Voltage	TC Code	Cap.	Tol.	Part Number	p*
1.0mm	500Vdc	X8G	22pF	±2%	GQM2195G2H220GB12#	p175
				±5%	GQM2195G2H220JB12#	p175
	250Vdc	COG	1.0pF	±0.1pF	GQM2195C2E1R0BB12#	p178
				±0.25pF	GQM2195C2E1R0CB12#	p178
			1.1pF	±0.1pF	GQM2195C2E1R1BB12#	p178
				±0.25pF	GQM2195C2E1R1CB12#	p178
			1.2pF	±0.1pF	GQM2195C2E1R2BB12#	p178
				±0.25pF	GQM2195C2E1R2CB12#	p178
			1.3pF	±0.1pF	GQM2195C2E1R3BB12#	p178
				±0.25pF	GQM2195C2E1R3CB12#	p178
			1.5pF	±0.1pF	GQM2195C2E1R5BB12#	p178
				· ·	GQM2195C2E1R5CB12#	p178
			1.6pF	±0.1pF	GQM2195C2E1R6BB12#	p178
				· ·	GQM2195C2E1R6CB12#	p178
			1.8pF	±0.1pF	GQM2195C2E1R8BB12#	p178
				·	GQM2195C2E1R8CB12#	p178
			2.0pF	±0.1pF	GQM2195C2E2R0BB12#	p178
			22.5	· ·	GQM2195C2E2R0CB12#	p178
			2.2pF	±0.1pF	GQM2195C2E2R2BB12#	p178
			2.45	·	GQM2195C2E2R2CB12#	p178
			2.4pF	±0.1pF	GQM2195C2E2R4BB12#	p178
			2.755	·	GQM2195C2E2R4CB12#	p178
			2.7pF	±0.1pF	GQM2195C2E2R7BB12#	p178
		_	3.0pF	·	GQM2195C2E2R7CB12# GQM2195C2E3R0BB12#	p178
			3.0pr	±0.1pF ±0.25pF	,	p178
			3.3pF	±0.25pi	GQM2195C2E3R3BB12#	p178
					GQM2195C2E3R3CB12#	p178
			3.6pF	±0.1pF	GQM2195C2E3R6BB12#	p178
			3.0рг	±0.25pF	,	p178
			3.9pF	±0.1pF	GQM2195C2E3R9BB12#	p178
					GQM2195C2E3R9CB12#	p178
			4.0pF	±0.1pF	GQM2195C2E4R0BB12#	p178
					GQM2195C2E4R0CB12#	p178
			4.3pF	±0.1pF	GQM2195C2E4R3BB12#	p178
					GQM2195C2E4R3CB12#	p178
			4.7pF	· ·	GQM2195C2E4R7BB12#	p178
			·		GQM2195C2E4R7CB12#	p178
			5.0pF	±0.1pF	GQM2195C2E5R0BB12#	p178
				±0.25pF	GQM2195C2E5R0CB12#	p178
			5.1pF	±0.25pF	GQM2195C2E5R1CB12#	p178
			•	±0.5pF	GQM2195C2E5R1DB12#	p178
			5.6pF	±0.25pF	GQM2195C2E5R6CB12#	p178
				±0.5pF	GQM2195C2E5R6DB12#	p178
			6.0pF	-	GQM2195C2E6R0CB12#	p178
				±0.5pF	GQM2195C2E6R0DB12#	p178
			6.2pF	-	GQM2195C2E6R2CB12#	p178
				±0.5pF	GQM2195C2E6R2DB12#	p178
			6.8pF	-	GQM2195C2E6R8CB12#	p178
				±0.5pF	GQM2195C2E6R8DB12#	p178
			7.0pF	±0.25pF	GQM2195C2E7R0CB12#	p178
			•	±0.5pF	GQM2195C2E7R0DB12#	p178
	1	1 1	7.5pF		GQM2195C2E7R5CB12#	p178

±0.5pF **GQM2195C2E7R5DB12#** p178

# GQM Series Temperature Compensating Type Part Number List

(→ 2.0>	1.25m	m)				
T max.	Rated Voltage	TC Code	Cap.	Tol.	Part Number	p*
1.0mm	250Vdc	COG	8.0pF	±0.25pF	GQM2195C2E8R0CB12#	p178
				±0.5pF	GQM2195C2E8R0DB12#	p178
			8.2pF	±0.25pF	GQM2195C2E8R2CB12#	p178
				±0.5pF	GQM2195C2E8R2DB12#	p178
			9.0pF	±0.25pF	GQM2195C2E9R0CB12#	p178
				±0.5pF	GQM2195C2E9R0DB12#	p178
			9.1pF	±0.25pF	GQM2195C2E9R1CB12#	p178
				±0.5pF	GQM2195C2E9R1DB12#	p178
			10pF	±2%	GQM2195C2E100GB12#	p178
				±5%	GQM2195C2E100JB12#	p178
			11pF	±2%	GQM2195C2E110GB12#	p178
				±5%	GQM2195C2E110JB12#	p178
			12pF	±2%	GQM2195C2E120GB12#	p178
				±5%	GQM2195C2E120JB12#	p178
			13pF	±2%	GQM2195C2E130GB12#	p178
				±5%	GQM2195C2E130JB12#	p178
			15pF	±2%	GQM2195C2E150GB12#	p178
			46.5	±5%	GQM2195C2E150JB12#	p178
			16pF	±2%	GQM2195C2E160GB12#	p178
			1055	±5%	GQM2195C2E160JB12#	p178
			18pF	±2% ±5%	GQM2195C2E180GB12# GQM2195C2E180JB12#	p178 p178
			20pF	±2%	GQM2195C2E200GB12#	p178
			Zopi	±5%	GQM2195C2E200JB12#	p178
			22pF	±2%	GQM2195C2E220GB12#	p178
				±5%	GQM2195C2E220JB12#	p178
			24pF	±2%	GQM2195C2E240GB12#	p178
			·	±5%	GQM2195C2E240JB12#	p178
			27pF	±2%	GQM2195C2E270GB12#	p178
				±5%	GQM2195C2E270JB12#	p178
			30pF	±2%	GQM2195C2E300GB12#	p178
				±5%	GQM2195C2E300JB12#	p178
			33pF	±2%	GQM2195C2E330GB12#	p178
				±5%	GQM2195C2E330JB12#	p178
			36pF	±2%	GQM2195C2E360GB12#	p178
				±5%	GQM2195C2E360JB12#	p178
			39pF	±2%	GQM2195C2E390GB12#	p178
				±5%	GQM2195C2E390JB12#	p178
			43pF	±2%	GQM2195C2E430GB12#	p178
				±5%	GQM2195C2E430JB12#	p178
			47pF	±2%	GQM2195C2E470GB12#	p178
				±5%	GQM2195C2E470JB12#	p178
			51pF	±2%	GQM2195C2E510GB12#	p178
			F.C.: F	±5%	GQM2195C2E510JB12#	p178
			56pF	±2%	GQM2195C2E560GB12#	p178
			62nE	±5% ±2%	GQM2195C2E560JB12# GQM2195C2E620GB12#	p178
			62pF	±2% ±5%	GQM2195C2E620GB12# GQM2195C2E620JB12#	p178 p178
			68pF	±3 %	GQM2195C2E680GB12#	p178
			- ~P'	±5%	GQM2195C2E680JB12#	p178
			75pF	±2%	GQM2195C2E750GB12#	p178
			,	±5%	GQM2195C2E750JB12#	p178
			82pF	±2%	GQM2195C2E820GB12#	p178
				±5%	GQM2195C2E820JB12#	p178

T max.	Rated Voltage	TC Code	Cap.	Tol.	Part Number	p*
1.0mm	250Vdc	COG	91pF	±2%	GQM2195C2E910GB12#	p178
				±5%	GQM2195C2E910JB12#	p178
			100pF	±2%	GQM2195C2E101GB12#	p178
				±5%	GQM2195C2E101JB12#	p178
		X8G	1.0pF	±0.1pF	GQM2195G2E1R0BB12#	p175
				±0.25pF	GQM2195G2E1R0CB12#	p175
			1.1pF	±0.1pF	GQM2195G2E1R1BB12#	p175
				±0.25pF	GQM2195G2E1R1CB12#	p175
			1.2pF	±0.1pF	GQM2195G2E1R2BB12#	p175
				±0.25pF	GQM2195G2E1R2CB12#	p175
			1.3pF	±0.1pF	GQM2195G2E1R3BB12#	p175
				±0.25pF	GQM2195G2E1R3CB12#	p175
			1.5pF	±0.1pF	GQM2195G2E1R5BB12#	p175
				±0.25pF	GQM2195G2E1R5CB12#	p175
			1.6pF	±0.1pF	GQM2195G2E1R6BB12#	p175
				±0.25pF	GQM2195G2E1R6CB12#	p175
			1.8pF	±0.1pF	GQM2195G2E1R8BB12#	p175
				±0.25pF	GQM2195G2E1R8CB12#	p175
			2.0pF	±0.1pF	GQM2195G2E2R0BB12#	p175
				±0.25pF	GQM2195G2E2R0CB12#	p175
			2.2pF	±0.1pF	GQM2195G2E2R2BB12#	p175
				±0.25pF	GQM2195G2E2R2CB12#	p175
			2.4pF	±0.1pF	GQM2195G2E2R4BB12#	p175
				±0.25pF	GQM2195G2E2R4CB12#	p175
			2.7pF	±0.1pF	GQM2195G2E2R7BB12#	p175
				±0.25pF	GQM2195G2E2R7CB12#	p175
			3.0pF	±0.1pF	GQM2195G2E3R0BB12#	p175
				±0.25pF	GQM2195G2E3R0CB12#	p175
			3.3pF	±0.1pF	GQM2195G2E3R3BB12#	p175
				±0.25pF	GQM2195G2E3R3CB12#	p175
			3.6pF	±0.1pF	GQM2195G2E3R6BB12#	p175
				±0.25pF	GQM2195G2E3R6CB12#	p175
			3.9pF	±0.1pF	GQM2195G2E3R9BB12#	p175
				±0.25pF	GQM2195G2E3R9CB12#	p175
			4.0pF	±0.1pF	GQM2195G2E4R0BB12#	p175
				±0.25pF	GQM2195G2E4R0CB12#	p175
			4.3pF	±0.1pF	GQM2195G2E4R3BB12#	p175
				±0.25pF	GQM2195G2E4R3CB12#	p175
			4.7pF	±0.1pF	GQM2195G2E4R7BB12#	p175
				±0.25pF	GQM2195G2E4R7CB12#	p175
			5.0pF	±0.1pF	GQM2195G2E5R0BB12#	p175
				±0.25pF	GQM2195G2E5R0CB12#	p175
			5.1pF	±0.25pF	GQM2195G2E5R1CB12#	p175
				±0.5pF	GQM2195G2E5R1DB12#	p175
			5.6pF	±0.25pF	GQM2195G2E5R6CB12#	p175
				±0.5pF	GQM2195G2E5R6DB12#	p175
			6.0pF	±0.25pF	GQM2195G2E6R0CB12#	p175
				±0.5pF	GQM2195G2E6R0DB12#	p175
			6.2pF	±0.25pF	GQM2195G2E6R2CB12#	p175
				±0.5pF	GQM2195G2E6R2DB12#	p175
			6.8pF	±0.25pF	GQM2195G2E6R8CB12#	p175
				±0.5pF	GQM2195G2E6R8DB12#	p175
			7.0pF	±0.25pF	GQM2195G2E7R0CB12#	p175
_				±0.5pF	GQM2195G2E7R0DB12#	p175

Part number # indicates the package specification code.

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 $<sup>*\</sup>mbox{:}$  Refers to the page of the "Specifications and Test Methods".

# 3 // GRM

# // GR3 //

GR4 // GRJ

// GR7 //

 $\exists$ 

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# GQM Se

# GQM Series Temperature Compensating Type Part Number List

(→ 2.0×1.25mm)						
T max.	Rated Voltage	TC Code	Cap.	Tol.	Part Number	p*
1.0mm	250Vdc	X8G	7.5pF	±0.25pF	GQM2195G2E7R5CB12#	p175
				±0.5pF	GQM2195G2E7R5DB12#	p175
			8.0pF	±0.25pF	GQM2195G2E8R0CB12#	p175
				±0.5pF		p175
			8.2pF	±0.25pF		p175
				±0.5pF	GQM2195G2E8R2DB12#	p175
			9.0pF	-	GQM2195G2E9R0CB12#	p175
					GQM2195G2E9R0DB12#	p175
			9.1pF	-	GQM2195G2E9R1CB12#	p175
				±0.5pF		p175
			10pF	±2%	GQM2195G2E100GB12#	p175
				±5%	GQM2195G2E100JB12#	p175
			11pF	±2%		p175
				±5%	GQM2195G2E110JB12#	p175
			12pF	±2%	GQM2195G2E120GB12#	p175
				±5%	GQM2195G2E120JB12#	p175
			13pF	±2%		<del>-</del>
			4	±5%	GQM2195G2E130JB12#	p175
			15pF	±2%	GQM2195G2E150GB12#	p175
			16.5	±5%	GQM2195G2E150JB12#	p175
			16pF	±2%	GQM2195G2E160GB12#	-
			10.5	±5%	GQM2195G2E160JB12#	p175
			18pF	±2%	GQM2195G2E180GB12#	p175
			20-5	±5%	GQM2195G2E180JB12#	p175
			20pF	±2% ±5%	GQM2195G2E200GB12# GQM2195G2E200JB12#	p175 p175
			22pF	±2%	GQM2195G2E220GB12#	p175
			ZZPI	±5%	GQM2195G2E220JB12#	p175
			24pF	±2%	·	p175
				±5%	GQM2195G2E240JB12#	p175
			27pF	±2%	GQM2195G2E270GB12#	p175
			·	±5%	GQM2195G2E270JB12#	p175
			30pF	±2%	GQM2195G2E300GB12#	p175
				±5%	GQM2195G2E300JB12#	p175
			33pF	±2%	GQM2195G2E330GB12#	p175
				±5%	GQM2195G2E330JB12#	p175
			36pF	±2%	GQM2195G2E360GB12#	p175
				±5%	GQM2195G2E360JB12#	p175
			39pF	±2%	GQM2195G2E390GB12#	p175
				±5%	GQM2195G2E390JB12#	p175
			43pF	±2%	GQM2195G2E430GB12#	p175
				±5%	GQM2195G2E430JB12#	p175
			47pF	±2%	GQM2195G2E470GB12#	p175
				±5%	GQM2195G2E470JB12#	p175
			51pF	±2%	GQM2195G2E510GB12#	p175
				±5%	GQM2195G2E510JB12#	p175
			56pF	±2%	GQM2195G2E560GB12#	p175
				±5%	GQM2195G2E560JB12#	p175
			62pF	±2%	GQM2195G2E620GB12#	p175
				±5%	GQM2195G2E620JB12#	p175
			68pF	±2%	GQM2195G2E680GB12#	p175
				±5%	GQM2195G2E680JB12#	p175
			75pF	±2%		p175
		<u> </u>		±5%	GQM2195G2E750JB12#	p175

T max.	Rated Voltage	TC Code	Cap.	Tol.	Part Number	p*
1.0mm	250Vdc	X8G	82pF	±2%	GQM2195G2E820GB12#	p175
				±5%	GQM2195G2E820JB12#	p175

#### 2.8×2.8mm

2.8×2.8mm						
T max.	Rated Voltage	TC Code	Cap.	Tol.	Part Number	p*
1.35mm	500Vdc	COG	1.0pF	±0.1pF	GQM22M5C2H1R0BB01#	p181
				±0.25pF	GQM22M5C2H1R0CB01#	p181
			1.1pF	±0.1pF	GQM22M5C2H1R1BB01#	p181
				±0.25pF	GQM22M5C2H1R1CB01#	p181
			1.2pF	±0.1pF	GQM22M5C2H1R2BB01#	p181
				±0.25pF	GQM22M5C2H1R2CB01#	p181
			1.3pF	±0.1pF	GQM22M5C2H1R3BB01#	p181
				±0.25pF	GQM22M5C2H1R3CB01#	p181
			1.5pF	±0.1pF	GQM22M5C2H1R5BB01#	p181
				±0.25pF	GQM22M5C2H1R5CB01#	p181
			1.6pF	±0.1pF	GQM22M5C2H1R6BB01#	p181
				±0.25pF	GQM22M5C2H1R6CB01#	p181
			1.8pF	±0.1pF	GQM22M5C2H1R8BB01#	p181
				±0.25pF	GQM22M5C2H1R8CB01#	p181
			2.0pF	±0.1pF	GQM22M5C2H2R0BB01#	p181
				±0.25pF	GQM22M5C2H2R0CB01#	p181
			2.2pF	±0.1pF	GQM22M5C2H2R2BB01#	p181
				±0.25pF	GQM22M5C2H2R2CB01#	p181
			2.4pF	±0.1pF	GQM22M5C2H2R4BB01#	p181
				±0.25pF	GQM22M5C2H2R4CB01#	p181
			2.7pF	±0.1pF	GQM22M5C2H2R7BB01#	p181
				±0.25pF	GQM22M5C2H2R7CB01#	p181
			3.0pF	±0.1pF	GQM22M5C2H3R0BB01#	p181
				±0.25pF	GQM22M5C2H3R0CB01#	p181
			3.3pF	±0.1pF	GQM22M5C2H3R3BB01#	p181
				-	GQM22M5C2H3R3CB01#	<u> </u>
			3.6pF	±0.1pF	GQM22M5C2H3R6BB01#	<u>-</u>
					GQM22M5C2H3R6CB01#	<u> </u>
			3.9pF	±0.1pF	GQM22M5C2H3R9BB01#	<u> </u>
				-	GQM22M5C2H3R9CB01#	<u> </u>
			4.0pF	±0.1pF	GQM22M5C2H4R0BB01#	<u> </u>
					GQM22M5C2H4R0CB01#	<u> </u>
			4.3pF	±0.1pF	GQM22M5C2H4R3BB01#	i
					GQM22M5C2H4R3CB01#	<del>-</del>
			4.7pF	±0.1pF	GQM22M5C2H4R7BB01#	<u> </u>
					GQM22M5C2H4R7CB01#	i
			5.0pF	•	GQM22M5C2H5R0BB01#	i
					GQM22M5C2H5R0CB01#	i
			5.1pF	· ·	GQM22M5C2H5R1CB01#	<u> </u>
				GQM22M5C2H5R1DB01#		
			5.6pF	-	GQM22M5C2H5R6CB01#	i –
			60-5	•	GQM22M5C2H5R6DB01#	i –
			6.0pF	-	GQM22M5C2H6R0CB01#	i
			6.255	±0.5pF	GQM22M5C2H6R0DB01#	_
			6.2pF		GQM22M5C2H6R2CB01#	i
			605	±0.5pF	GQM22M5C2H6R2DB01#	i
			6.8pF		GQM22M5C2H6R8CB01#	

<sup>\*:</sup> Refers to the page of the "Specifications and Test Methods".

# GQM Series Temperature Compensating Type Part Number List

(→ 2.8)	2.8mm	1)	•		•	
T max.	Rated Voltage	TC Code	Cap.	Tol.	Part Number	p*
1.35mm	500Vdc	COG	6.8pF	±0.5pF	GQM22M5C2H6R8DB01#	p181
			7.0pF	±0.25pF	GQM22M5C2H7R0CB01#	p181
				±0.5pF	GQM22M5C2H7R0DB01#	p181
			7.5pF	±0.25pF	GQM22M5C2H7R5CB01#	p181
				±0.5pF	GQM22M5C2H7R5DB01#	p181
			8.0pF	±0.25pF	GQM22M5C2H8R0CB01#	p181
				±0.5pF	GQM22M5C2H8R0DB01#	p181
			8.2pF	±0.25pF	GQM22M5C2H8R2CB01#	p181
				±0.5pF	GQM22M5C2H8R2DB01#	p181
			9.0pF	-	GQM22M5C2H9R0CB01#	i -
				±0.5pF	GQM22M5C2H9R0DB01#	i
			9.1pF	-	GQM22M5C2H9R1CB01#	i
			10-5	±0.5pF	GQM22M5C2H9R1DB01#	i
			10pF	±2%	GQM22M5C2H100GB01#	<del> </del>
			11nE	±5% ±2%	GQM22M5C2H100JB01#	<u> </u>
			11pF	±2 %	GQM22M5C2H110GB01# GQM22M5C2H110JB01#	p181
			12pF	±2%	GQM22M5C2H120GB01#	<u> </u>
			120.	±5%		p181
			13pF	±2%	GQM22M5C2H130GB01#	<u> </u>
				±5%		p181
			15pF	±2%	GQM22M5C2H150GB01#	<u> </u>
				±5%	GQM22M5C2H150JB01#	p181
			16pF	±2%	GQM22M5C2H160GB01#	p181
				±5%	GQM22M5C2H160JB01#	p181
			18pF	±2%	GQM22M5C2H180GB01#	p181
				±5%	GQM22M5C2H180JB01#	p181
			20pF	±2%	GQM22M5C2H200GB01#	p181
				±5%	GQM22M5C2H200JB01#	p181
			22pF	±2%	GQM22M5C2H220GB01#	p181
				±5%	GQM22M5C2H220JB01#	p181
			24pF	±2%	GQM22M5C2H240GB01#	p181
				±5%	GQM22M5C2H240JB01#	p181
			27pF	±2%	GQM22M5C2H270GB01#	<u>'</u>
			205	±5%	•	p181
			30pF	±2%	GQM22M5C2H300GB01#	_
			33pF	±5% ±2%	GQM22M5C2H300JB01# GQM22M5C2H330GB01#	p181
			ээрі	±5%		p181
			36pF	±2%	GQM22M5C2H360GB01#	<u> </u>
			σоμ.	±5%	GQM22M5C2H360JB01#	p181
			39pF	±2%	GQM22M5C2H390GB01#	
			·	±5%		p181
			43pF	±2%	GQM22M5C2H430GB01#	p181
				±5%	GQM22M5C2H430JB01#	p181
			47pF	±2%	GQM22M5C2H470GB01#	p181
				±5%	GQM22M5C2H470JB01#	p181
			51pF	±2%	GQM22M5C2H510GB01#	p181
				±5%	GQM22M5C2H510JB01#	p181
			56pF	±2%	GQM22M5C2H560GB01#	p181
				±5%	GQM22M5C2H560JB01#	p181
			62pF	±2%	GQM22M5C2H620GB01#	p181
				±5%	GQM22M5C2H620JB01#	p181
			68pF	±2%	GQM22M5C2H680GB01#	p181

T max.	Rated Voltage	TC Code	Cap.	Tol.	Part Number	p*
1.35mm	500Vdc	COG	68pF	±5%	GQM22M5C2H680JB01#	p181
			75pF	±2%	GQM22M5C2H750GB01#	p181
				±5%	GQM22M5C2H750JB01#	p181
			82pF	±2%	GQM22M5C2H820GB01#	p181
				±5%	GQM22M5C2H820JB01#	p181
			91pF	±2%	GQM22M5C2H910GB01#	p181
				±5%	GQM22M5C2H910JB01#	p181
			100pF	±2%	GQM22M5C2H101GB01#	p181
				±5%	GQM22M5C2H101JB01#	p181

 $<sup>\</sup>ensuremath{^*:}$  Refers to the page of the "Specifications and Test Methods".

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# GQM Series Specifications and Test Methods (1)

No	lte	em	Specification	Test Method (Ref. Standard: JIS C 5101, IEC60384)
1	Rated Voltage	e	Shown in Rated value.	The rated voltage is defined as the maximum voltage which may be applied continuously to the capacitor. When AC voltage is superimposed on DC voltage, VP-P or VO-P, whichever is larger, should be maintained within the rated voltage range.
2	Appearance		No defects or abnormalities.	Visual inspection.
3	Dimension		Within the specified dimensions.	Using Measuring instrument of dimension.
4	Voltage Proof	•	No defects or abnormalities.	Measurement Point: Between the terminations Applied Time: 1 to 5s Charge/discharge current: 50mA max.  Test Voltage:  Rated Voltage  100V 300% of Rated Voltage 200V 250% of Rated Voltage
5	Insulation Res	sistance (I.R.)	More than $10000 M\Omega$	Measurement Point: Between the terminations Measurement Voltage: DC Rated Voltage Charging Time: 2min Charge/discharge current: 50mA max. Measurement Temperature: Room Temperature
6	Capacitance		Shown in Rated value.	Measurement Temperature: Room Temperature
7	Q		30pF and over: Q ≧ 1400 30pF and below: Q ≧ 800+20C C: Nominal Capacitance(pF)	Capacitance       Frequency       Voltage         C ≦ 1000pF       1.0±0.1kHz       0.5 to 5.0Vrms
8	Temperature Characteristics of Capacitance		Nominal values of the temperature coefficient is shown in Rated value. But, the Capacitance Change under 25°C is shown in Table A. Capacitance Drift Within ±0.2% or ±0.05pF (Whichever is larger.)	The capacitance change should be measured after 5 minutes at each specified temp. stage.  Capacitance value as a reference is the value in step 3. The capacitance drift is calculated by dividing the differences between the maximum and minimum measured values in the step 1, 3 and 5 by the cap. value in step 3.  Step Temperature (°C)  1 Reference Temp. ±2  2 Min. Operating Temp. ±3  3 Reference Temp. ±2  4 Max. Operating Temp. ±3  5 Reference Temp. ±2
9	Adhesive Stre	0	No removal of the terminations or other defect should occur.	Solder the capacitor on the test substrate shown in Fig.3. Applied Force: 5N Holding Time: 10±1s Applied Direction: In parallel with the test substrate and vertical with the capacitor side.
		Appearance No defects or abnormalities.		Solder the capacitor on the test substrate shown in Fig.3.
		Capacitance	Within the specified initial value.	Kind of Vibration: A simple harmonic motion  10Hz to 55Hz to 10Hz (1min)
10	Vibration	Q	Within the specified initial value.	Total amplitude: 1.5mm This motion should be applied for a period of 2h in each 3 mutually perpendicular directions (total of 6h).
	Cultatura	Appearance	No defects or abnormalities.	Solder the capacitor on the test substrate shown in Fig.1.
11	Substrate Bending Capacitance Test Change		Within ±5% or ±0.5pF (Whichever is larger)	Pressurization method: Shown in Fig.2 Flexure: 1mm Holding Time: 5±1s Soldering Method: Reflow soldering
12	2 Solderability		95% of the terminations is to be soldered evenly and continuously.	Test Method: Solder bath method Flux: Solution of rosin ethanol 25 (mass)% Preheat: 80 to 120°C for 10 to 30s Solder: Sn-3.0Ag-0.5Cu Solder Temp.: 245±5°C Immersion time: 2±0.5s
		Appearance	No defects or abnormalities.	
	Resistance to	Capacitance Change	Within ±2.5% or ±0.25pF (Whichever is larger)	Test Method: Solder bath method Solder: Sn-3.0Ag-0.5Cu
13	Soldering	Q	Within the specified initial value.	Solder Temp.: 270±5°C Immersion time: 10±0.5s
	Heat	I.R.	Within the specified initial value.	Exposure Time: 24±2h
		Voltage Proof	No defects.	Preheat: 120 to 150°C for 1min
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# GQM Series Specifications and Test Methods (1)

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No	ltem		Specification	Тє	Test Method (Ref. Standard: JIS C 5101, IEC60384)			
		Appearance	No defects or abnormalities.	1	the capacitor on the test substra	•		
		Capacitance Change	Within ±2.5% or ±0.25pF (Whichever is larger)	Perform the 5 cycles according to the four heat treatments shown in the following table.				
14	Temperature Sudden	Q	Within the specified initial value.	Ste <sub>l</sub>	Step         Temp. (°C)         Time (min)           1         Min. Operating Temp. +0/-3         30±3			
	Change	I.R.	Within the specified initial value.	2	Room Temp.	2 to 3		
		Voltage Proof	No defects.	3 4 Exposu	Max. Operating Temp. +3/-0 Room Temp. re Time: 24±2h	30±3 2 to 3		
		Appearance	No defects or abnormalities.	Solder the capacitor on the test substrate shown in Fig.3.				
	High Temperature High Humidity (Steady)	Capacitance Change	Within ±7.5% or ±0.75pF (Whichever is larger)	Test Te	100 3110 WITHIN 16.5.			
15		Q	30pF and over: Q ≧ 200 30pF and below: Q ≧ 100+10C/3 C: Nominal Capacitance(pF)	Test Time: 500±12h Applied Voltage: DC Rated Voltage Charge/discharge current: 50mA max. Exposure Time: 24±2h				
		I.R.	More than $500M\Omega$	Lxposu	TE TIME: 24:211			
		Appearance	No defects or abnormalities.					
		Capacitance Change	Within ±3% or ±0.3pF (Whichever is larger)	1	the capacitor on the test substra	•		
16	Durability	Q	30pF and over: Q ≥ 350 10pF and over, 30pF and below: Q ≥ 275+5C/2 10pF and below: Q ≥ 200+10C C: Nominal Capacitance (pF)	Test Time: 1000±12h Applied Voltage: 200% of the rated voltage Charge/discharge current: 50mA max. Exposure Time: 24±2h		age		
		I.R.	More than $1000M\Omega$					

Table A

		Capacitance Change from 25°C(%)							
Char.	-55	5°C	-30°C		-10°C				
	Max.	Min.	Max.	Min.	Max.	Min.			
5C	0.58	-0.24	0.40	-0.17	0.25	-0.11			

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KR3

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KR3

GMA

### **Substrate Bending Test**

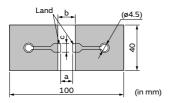
Test Substrate
 Material: Copper-clad laminated sheets for PCBs (Glass fabric base, epoxy resin)

Thickness: 0.8mm

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Copper foil thickness: 0.035mm

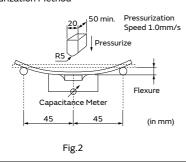
: Solder resist (Coat with heat resistant resin for solder)



Dauk Mussahau		Dimension (mm	1)
Part Number	a	ь	С
GQM15	0.4	1.5	0.5

Fig.1

- Kind of Solder: Sn-3.0Ag-0.5Cu
- Pressurization Method



#### Adhesive Strength of Termination, Vibration, Temperature Sudden Change, High Temperature High Humidity (Steady), Durability

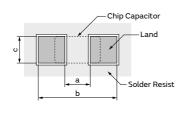
Test Substrate

Material: Copper-clad laminated sheets for PCBs (Glass fabric base, epoxy resin)

Thickness: 1.6mm or 0.8mm Copper foil thickness: 0.035mm

• Kind of Solder: Sn-3.0Ag-0.5Cu

Land Dimensions



Part Number	D	Dimension (mm	1)
Pait Number	a	ь	С
GOM15	0.4	1.5	0.5

Fig.3

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# GQM Series Specifications and Test Methods (2)

No	lte	em	Specification	Test Method (Ref. Standard: JIS C 5101, IEC60384)	
1	L Rated Voltage		Shown in Rated value.	The rated voltage is defined as the maximum voltage which may be applied continuously to the capacitor. When AC voltage is superimposed on DC voltage, VP-P or VO-P, whichever is larger, should be maintained within the rated voltage range.	
2	Appearance		No defects or abnormalities.	Visual inspection.	
3	Dimension		Within the specified dimensions.	Using Measuring instrument of dimension.	
4	Voltage Proof	•	No defects or abnormalities.	Measurement Point: Between the terminations Test Voltage: 250% of the rated voltage Applied Time: 1 to 5s Charge/discharge current: 50mA max.	
5	Insulation Res	sistance (I.R.)	More than $10000 M\Omega$	Measurement Point: Between the terminations Measurement Voltage: DC Rated Voltage Charging Time: 1min Charge/discharge current: 50mA max. Measurement Temperature: Room Temperature	
6	Capacitance		Shown in Rated value.	Measurement Temperature :Room Temperature	
7	Q		30pF and over: Q ≧ 1400 30pF and below: Q ≧ 800+20C C: Nominal Capacitance(pF)	Capacitance     Frequency     Voltage       C ≤ 1000pF     1.0±0.1MHz     0.5 to 5.0Vrms	
8	Temperature Characteristic of Capacitanc	es	Nominal values of the temperature coefficient is shown in Rated value. But, the Capacitance Change under 20°C/25°C is shown in Table A. Capacitance Drift Within ±0.2% or ±0.05pF (Whichever is larger.)	The capacitance change should be measured after 5 minutes at each specified temp. stage.  Capacitance value as a reference is the value in step 3.  The capacitance drift is calculated by dividing the differences between the maximum and minimum measured values in the step 1, 3 and 5 by the cap. value in step 3.  Step Temperature (°C)  1 Reference Temp. ±2  2 Min. Operating Temp. ±3  3 Reference Temp. ±2  4 Max. Operating Temp. ±3  5 Reference Temp. ±2	
9	Adhesive Stre		No removal of the terminations or other defect should occur.	Solder the capacitor on the test substrate shown in Fig.3.  Part Number Applied Force(N)  GQM18 5  GQM21 10  Holding Time: 10±1s  Applied Direction: In parallel with the test substrate and vertical with the capacitor side.	
		Appearance	No defects or abnormalities.	Solder the capacitor on the test substrate shown in Fig.3.	
		Capacitance	Within the specified initial value.	Kind of Vibration: A simple harmonic motion 10Hz to 55Hz to 10Hz (1min)	
10	· ·		Within the specified initial value.	Total amplitude: 1.5mm  This motion should be applied for a period of 2h in each 3 mutually perpendicular directions (total of 6h).	
		Appearance	No defects or abnormalities.	Solder the capacitor on the test substrate shown in Fig.1.	
11	Substrate 1 Bending Capacitance Test Change		Within ±5% or ±0.5pF (Whichever is larger)	Pressurization method: Shown in Fig.2 Flexure: 1mm Holding Time: 5±1s Soldering Method: Reflow soldering	
12	12 Solderability		95% of the terminations is to be soldered evenly and continuously.	Test Method: Solder bath method Flux: Solution of rosin ethanol 25 (mass)% Preheat: 80 to 120°C for 10 to 30s Solder: Sn-3.0Ag-0.5Cu Solder Temp.: 245±5°C Immersion time: 2±0.5s	

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# GQM Series Specifications and Test Methods (2)

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No	Ite	em	Specification	Tes	t Method (Ref. Standard: JIS C 5101, IEC60384)			
13	Resistance to Soldering Heat	Appearance Capacitance Change Q I.R.	No defects or abnormalities.  Within ±2.5% or ±0.25pF (Whichever is larger)  Within the specified initial value.  Within the specified initial value.	Solder: S Solder Te Immersion Exposure	chod: Solder bath method n-3.0Ag-0.5Cu emp.: 270±5°C on time: 10±0.5s E Time: 24±2h			
		Voltage Proof	No defects.	Preheat: 120 to 150°C for 1min				
		Appearance	No defects or abnormalities.		ne capacitor on the test substrate shown in Fig.3.			
14		Capacitance Change	Within ±2.5% or ±0.25pF (Whichever is larger)	shown in	the 5 cycles according to the four heat treatments the following table.			
	Temperature Sudden	Q.	Within the specified initial value.	Step 1	Temp. (°C) Time (min)  Min. Operating Temp. +0/-3 30±3			
	Change	I.R.	Within the specified initial value.	2	Room Temp. 2 to 3			
		Voltage Proof	No defects.	3 4 Exposure	Max. Operating Temp. +3/-0   30±3     Room Temp.   2 to 3     E Time: 24±2h			
		Appearance	No defects or abnormalities.	Solder the capacitor on the test substrate shown in Fig.3.				
	High Temperature	Capacitance Change	Within ±7.5% or ±0.75pF (Whichever is larger)	Test Ten Test Hun	nperature: 40±2°C nidity: 90 to 95%RH			
15	High Humidity (Steady)	Q	30pF and over: Q ≧ 200 30pF and below: Q ≧ 100+10C/3 C: Nominal Capacitance(pF)	Applied \ Charge/o	e: 500±12h Voltage: DC Rated Voltage discharge current: 50mA max. e Time: 24±2h			
		I.R.	More than $500M\Omega$	Exposure	7 Time. 2 12211			
		Appearance	No defects or abnormalities.					
		Capacitance Change	Within ±3% or ±0.3pF (Whichever is larger)		ne capacitor on the test substrate shown in Fig.3. hperature: Max. Operating Temp. ±3°C			
16	Durability	Q	30pF and over: Q $\ge$ 350 10pF and over, 30pF and below: Q $\ge$ 275+5C/2 10pF and below: Q $\ge$ 200+10C C: Nominal Capacitance (pF)	Test Tim Applied \ Charge/o	e: 1000±12h Voltage: 200% of the rated voltage discharge current: 50mA max. e Time: 24±2h			
		I.R.	More than 1000MΩ					

#### Table A

		Capacitance Change from 20°C/25°C (%)										
Char.	-55°C		-30°C		-25°C		-10°C					
	Max.	Min.	Max.	Min.	Max.	Min.	Max.	Min.				
2C	0.82	-0.45	-	-	0.49	-0.27	0.33	-0.18				
5C/5G	0.58	-0.24	0.40	-0.17	-	-	0.25	-0.11				

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# GQM Series Specifications and Test Methods (2)

Continued from the preceding page.

#### **Substrate Bending Test**

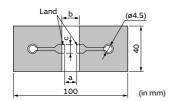
Test Substrate

Material: Copper-clad laminated sheets for PCBs (Glass fabric base, epoxy resin)

Thickness: 1.6mm

Copper foil thickness: 0.035mm

: Solder resist (Coat with heat resistant resin for solder)



Part Number	Dimension (mm)					
Part Number	a	b				
GQM18	1.0	3.0	1.2			
GQM21	1.2	4.0	1.65			

Fig.1

• Kind of Solder: Sn-3.0Ag-0.5Cu

Pressurization Method

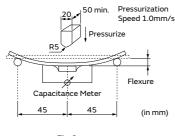


Fig.2

#### Adhesive Strength of Termination, Vibration, Temperature Sudden Change, High Temperature High Humidity (Steady), Durability

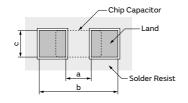
Test Substrate

Material: Copper-clad laminated sheets for PCBs (Glass fabric base, epoxy resin)

Thickness: 1.6mm or 0.8mm Copper foil thickness: 0.035mm

• Kind of Solder: Sn-3.0Ag-0.5Cu

Land Dimensions



Part Number	Dimension (mm)					
Pait Number	a	ь	С			
GQM18	1.0	3.0	1.2			
GQM21	1.2	4.0	1.65			

Fig.3

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# GQM Series Specifications and Test Methods (3)

No	<u>Ite</u>	em	Specification	Test Method (Ref. Standard: JIS C 5101, IEC60384)		
1	Rated Voltage	e	Shown in Rated value.	The rated voltage is defined as the maximum voltage which may be applied continuously to the capacitor. When AC voltage is superimposed on DC voltage, VP-P or VO-P, whichever is larger, should be maintained within the rated voltage range.		
2	Appearance		No defects or abnormalities.	Visual inspection.		
3	Dimension		Within the specified dimensions.	Using Measuring instrument of dimension.		
4	Voltage Proof		No defects or abnormalities.	Measurement Point: Between the terminations Test Voltage: 250% of the rated voltage Applied Time: 1 to 5s Charge/discharge current: 50mA max.		
5	Insulation Res	sistance (I.R.)	More than $10000 M\Omega$	Measurement Point: Between the terminations Measurement Voltage: DC Rated Voltage Charging Time: 2min Charge/discharge current: 50mA max. Measurement Temperature: Room Temperature		
6	Capacitance		Shown in Rated value.	Measurement Temperature: Room Temperature		
7	Q		30pF and over: Q ≧ 1400 30pF and below: Q ≧ 800+20C C: Nominal Capacitance (pF)	Capacitance     Frequency     Voltage       C ≦ 1000pF     1.0±0.1kHz     0.5 to 5.0Vrms		
8	Temperature Characteristics of Capacitance		Nominal values of the temperature coefficient is shown in Rated value. But, the Capacitance Change under 20°C/25°C is shown in Table A. Capacitance Drift Within ±0.2% or ±0.05pF (Whichever is larger.)	The capacitance change should be measured after 5 minutes at each specified temp. stage.  Capacitance value as a reference is the value in step 3.  The capacitance drift is calculated by dividing the differences between the maximum and minimum measured values in the step 1, 3 and 5 by the cap. value in step 3.  Step Temperature (°C)  1 Reference Temp. ±2  2 Min. Operating Temp. ±3  3 Reference Temp. ±2  4 Max. Operating Temp. ±3  5 Reference Temp. ±2		
9	Adhesive Stre	_	No removal of the terminations or other defect should occur.	Solder the capacitor on the test substrate shown in Fig.3.  Part Number Applied Force(N)  GQM18 5  GQM21 10  Holding Time: 10±1s  Applied Direction: In parallel with the test substrate and vertical with the capacitor side.		
		Appearance	No defects or abnormalities.	Solder the capacitor on the test substrate shown in Fig.3.		
		Capacitance	Within the specified initial value.	Kind of Vibration: A simple harmonic motion		
10	Vibration	Q	Within the specified initial value.	10Hz to 55Hz to 10Hz (1min) Total amplitude: 1.5mm This motion should be applied for a period of 2h in each 3 mutually perpendicular directions (total of 6h).		
		Appearance	No defects or abnormalities.	Solder the capacitor on the test substrate shown in Fig.1.		
11	Substrate Bending Test	Capacitance Change	Within ±5% or ±0.5pF (Whichever is larger)	Pressurization method: Shown in Fig.2 Flexure: 1mm Holding Time: 5±1s Soldering Method: Reflow soldering		
12	? Solderability		95% of the terminations is to be soldered evenly and continuously.	Test Method: Solder bath method Flux: Solution of rosin ethanol 25 (mass)% Preheat: 80 to 120°C for 10 to 30s Solder: Sn-3.0Ag-0.5Cu Solder Temp.: 245±5°C Immersion time: 2±0.5s		
		Appearance	No defects or abnormalities.			
	Resistance to	Capacitance Change	Within ±2.5% or ± 0.25pF (Whichever is larger)	Test Method: Solder bath method Solder: Sn-3.0Ag-0.5Cu		
13		Q	Within the specified initial value.	Solder Temp.: 270±5°C Immersion time: 10±0.5s		
	Heat	I.R.	Within the specified initial value.	Exposure Time: 24±2h		
		Voltage Proof	No defects.	Preheat: 120 to 150°C for 1min		

# GQM Series Specifications and Test Methods (3)

Continued from the preceding page.

No	lte	em	Specification	Tes	t Method (Ref. Standard: JIS C	5101, IEC60384)			
		Appearance	No defects or abnormalities.		Solder the capacitor on the test substrate shown in Fig Perform the 5 cycles according to the four heat treatm shown in the following table.				
		Capacitance Change	Within ±2.5% or ±0.25pF (Whichever is larger)	shown in					
14	Temperature Sudden	Q	Within the specified initial value.	Step 1	Temp. (°C) Min. Operating Temp. +0/-3	Time (min) 30±3			
	Change	I.R.	Within the specified initial value.	2	Room Temp.	2 to 3			
		Voltage Proof	No defects.	3 Max. Operating Temp. +3/-0		30±3 2 to 3			
	High Temperature High Humidity (Steady)	Appearance	No defects or abnormalities.	Solder the capacitor on the test substrate shown in Fig					
		Capacitance Change	Within ±7.5% or ±0.75pF (Whichever is larger)	Test Ten Test Hur	Test Temperature: 40±2°C Test Humidity: 90 to 95%RH				
15		Q	30pF and over: Q ≧ 200 30pF and below: Q ≧ 100+10C/3 C: Nominal Capacitance(pF)	Applied Charge/	Test Time: 500±12h Applied Voltage: DC Rated Voltage Charge/discharge current: 50mA max. Exposure Time: 24±2h				
		I.R.	More than $500M\Omega$	Lxposure					
		Appearance	No defects or abnormalities.						
		Capacitance Change	Within ±3% or ±0.3pF (Whichever is larger)	1	Solder the capacitor on the test substrate shown in Fig.3. Test Temperature: Max. Operating Temp. ±3°C				
16	Durability	Q	30pF and over: Q ≥ 350 10pF and over, 30pF and below: Q ≥ 275+5C/2 10pF and below: Q ≥ 200+10C C: Nominal Capacitance (pF)	Test Time: 1000±12h Applied Voltage: 200% of the rated voltage Charge/discharge current: 50mA max. Exposure Time: 24±2h		age			
		I.R.	More than $1000 M\Omega$						

Table A

		Capacitance Change from 20°C/25°C (%)									
Char.	-55°C		-30°C		-25°C		-10°C				
	Max.	Min.	Max.	Min.	Max.	Min.	Max.	Min.			
2C	0.82	-0.45	-	-	0.49	-0.27	0.33	-0.18			
5C/5G	0.58	-0.24	0.40	-0.17	-	-	0.25	-0.11			

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## GQM Series Specifications and Test Methods (3)

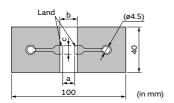
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#### **Substrate Bending Test**

 Test Substrate Material: Copper-clad laminated sheets for PCBs (Glass fabric base, epoxy resin)

Thickness: 1.6mm

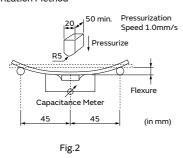
Copper foil thickness: 0.035mm : Solder resist (Coat with heat resistant resin for solder)



Part Number	Dimension (mm)					
Part Number	a	ь	С			
GQM18	1.0	3.0	1.2			
GQM21	1.2	4.0	1.65			

Fig.1

- Kind of Solder: Sn-3.0Ag-0.5Cu
- Pressurization Method



#### Adhesive Strength of Termination, Vibration, Temperature Sudden Change, High Temperature High Humidity (Steady), Durability

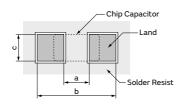
Test Substrate

Material: Copper-clad laminated sheets for PCBs (Glass fabric base, epoxy resin)

Thickness: 1.6mm or 0.8mm Copper foil thickness: 0.035mm

• Kind of Solder: Sn-3.0Ag-0.5Cu

Land Dimensions



Down Nove		Dimension (mm)						
Part Numb	umber	a	b	С				
GQM1	.8	1.0	3.0	1.2				
GOM2	1	1 2	40	1.65				

Fig.3

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# GQM Series Specifications and Test Methods (4)

No	lte	em	Specification	Test Method (Ref. Standard: JIS C 5101, IEC60384)
1	Rated Voltage		Shown in Rated value.	The rated voltage is defined as the maximum voltage which may be applied continuously to the capacitor. When AC voltage is superimposed on DC voltage, VP-P or VO-P, whichever is larger, should be maintained within the rated voltage range.
2	Appearance		No defects or abnormalities.	Visual inspection.
3	Dimension		Within the specified dimensions.	Using Measuring instrument of dimension.
4	Voltage Proof	F	No defects or abnormalities.	Measurement Point: Between the terminations Test Voltage: 250% of the rated voltage Applied Time: 1 to 5s Charge/discharge current: 50mA max.
5	Insulation Res	sistance (I.R.)	More than $10000 M\Omega$	Measurement Point: Between the terminations Measurement Voltage: DC Rated Voltage Charging Time: 2min Charge/discharge current: 50mA max. Measurement Temperature: Room Temperature
6	Capacitance		Shown in Rated value.	Measurement Temperature: Room Temperature
7	Q		30pF and over: Q ≧ 1400 30pF and below: Q ≧ 800+20C C: Nominal Capacitance(pF)	Capacitance     Frequency     Voltage       C ≦ 1000pF     1.0±0.1kHz     0.5 to 5.0Vrms
8	Temperature Characteristics of Capacitance		Nominal values of the temperature coefficient is shown in Rated value. But, the Capacitance Change under 25°C is shown in Table A. Capacitance Drift Within ±0.2% or ±0.05pF (Whichever is larger.)	The capacitance change should be measured after 5 minutes at each specified temp. stage.  Capacitance value as a reference is the value in step 3.  The capacitance drift is calculated by dividing the differences between the maximum and minimum measured values in the step 1, 3 and 5 by the cap. value in step 3.  Step Temperature (°C)  1 Reference Temp. ±2  2 Min. Operating Temp. ±3  3 Reference Temp. ±2  4 Max. Operating Temp. ±3  5 Reference Temp. ±2
9	Adhesive Stre	•	No removal of the terminations or other defect should occur.	Solder the capacitor on the test substrate shown in Fig.3. Applied Force: 10N Holding Time: 10±1s Applied Direction: In parallel with the test substrate and vertical with the capacitor side.
		Appearance	No defects or abnormalities.	Solder the capacitor on the test substrate shown in Fig.3.
		Capacitance	Within the specified initial value.	Kind of Vibration: A simple harmonic motion  10Hz to 55Hz to 10Hz (1min)
10	Vibration	Q	Within the specified initial value.	Total amplitude: 1.5mm This motion should be applied for a period of 2h in each 3 mutually perpendicular directions (total of 6h).
		Appearance	No defects or abnormalities.	Solder the capacitor on the test substrate shown in Fig.1.
11	Substrate Bending Test Capacitance Change		Within ±5% or ±0.5pF (Whichever is larger)	Pressurization method: Shown in Fig.2 Flexure:1mm Holding Time: 5±1s Soldering Method: Reflow soldering
12	2 Solderability		95% of the terminations is to be soldered evenly and continuously.	Test Method: Solder bath method Flux: Solution of rosin ethanol 25 (mass)% Preheat: 80 to 120°C for 10 to 30s Solder: Sn-3.0Ag-0.5Cu Solder Temp.: 245±5°C Immersion time: 2±0.5s
		Appearance	No defects or abnormalities.	
	Resistance to	Capacitance Change	Within ±2.5% or ±0.25pF (Whichever is larger)	Test Method: Solder bath method Solder: Sn-3.0Ag-0.5Cu
13	Soldering	Q	Within the specified initial value.	Solder Temp.: 270±5°C Immersion time: 10±0.5s
	Heat	I.R.	Within the specified initial value.	Exposure Time: 24±2h
		Voltage Proof	No defects.	Preheat: 120 to 150°C for 1min
		_		

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## GQM Series Specifications and Test Methods (4)

Continued from the preceding page.  $\searrow$ 

No	lte	em	Specification	Test Method (Ref. Standard: JIS C 5101, IEC60384)				
		Appearance	No defects or abnormalities.		ne capacitor on the test substrate shown in Fig.3.			
		Capacitance Change	Within ±2.5% or ±0.25pF (Whichever is larger)	shown in	the 5 cycles according to the four heat treatments the following table.			
14	Temperature Sudden	Q	Within the specified initial value.	Step 1	Temp. (°C) Time (min)  Min. Operating Temp. +0/-3 30±3			
	Change	I.R.	Within the specified initial value.	2	Room Temp. 2 to 3			
		Voltage Proof	No defects.	3 4 Exposure	Max. Operating Temp. +3/-0   30±3			
		Appearance	No defects or abnormalities.	Solder the capacitor on the test substrate shown in Fig.3.				
	High Temperature	Capacitance Change	Within ±7.5% or ±0.75pF (Whichever is larger)	Test Ten Test Hun	Test Temperature: 40±2°C Test Humidity: 90 to 95%RH Test Time: 500±12h Applied Voltage: DC Rated Voltage Charge/discharge current: 50mA max. Exposure Time: 24±2h			
15	High Humidity (Steady)	Q	30pF and over: Q $\ge$ 200 30pF and below: Q $\ge$ 100+10C/3 C: Nominal Capacitance(pF)	Applied \ Charge/o				
		I.R.	More than $500M\Omega$	Lxposure	5 TITTE. 24±211			
		Appearance	No defects or abnormalities.					
		Capacitance Change	Within ±3% or ±0.3pF (Whichever is larger)		ne capacitor on the test substrate shown in Fig.3. hperature: Max. Operating Temp. ±3°C			
16	Durability	Q	30pF and over: Q $\ge$ 350 10pF and over, 30pF and below: Q $\ge$ 275+5C/2 10pF and below: Q $\ge$ 200+10C C: Nominal Capacitance (pF)	Test Time: 1000±12h Applied Voltage: 150% of the rated voltage Charge/discharge current: 50mA max. Exposure Time: 24±2h				
		I.R.	More than $1000M\Omega$		1			

#### Table A

		Capacitance Change from 25°C(%)								
Char.	-55	5°C	-30	o°C	-10°C					
	Max.	Min.	Max.	Min.	Max.	Min.				
5C	0.58	-0.24	0.40	-0.17	0.25	-0.11				

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# GQM Series Specifications and Test Methods (4)

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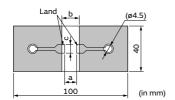
#### **Substrate Bending Test**

 Test Substrate Material: Copper-clad laminated sheets for PCBs (Glass fabric base, epoxy resin)

Thickness: 1.6mm

Copper foil thickness: 0.035mm

: Solder resist (Coat with heat resistant resin for solder)



Part Number	Dimension (mm)		
Part Number	a	ь	
GQM22	2.2	5.0	2.9

Fig.1

- Kind of Solder: Sn-3.0Ag-0.5Cu
- Pressurization Method

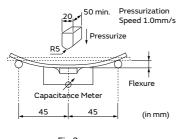


Fig.2

#### Adhesive Strength of Termination, Vibration, Temperature Sudden Change, High Temperature High Humidity (Steady), Durability

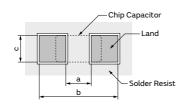
Test Substrate

Material: Copper-clad laminated sheets for PCBs (Glass fabric base, epoxy resin)

Thickness: 1.6mm or 0.8mm Copper foil thickness: 0.035mm

• Kind of Solder: Sn-3.0Ag-0.5Cu

Land Dimensions



Part Number	Dimension (mm)		
Pait Nullibei	a	ь	С
GQM22	2.2	5.0	2.9

Fig.3

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## **⚠** Caution/Notice



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## **1**Caution

#### **Storage and Operation Conditions**

- 1. The performance of chip multilayer ceramic capacitors and chip EMIFIL NFM series (henceforth just "capacitors") may be affected by the storage conditions. Please use them promptly after delivery.
  - 1-1. Maintain appropriate storage for the capacitors using the following conditions: Room Temperature of +5 to +40°C and a Relative Humidity of 20 to 70%. High temperature and humidity conditions and/or prolonged storage may cause deterioration of the packaging materials. If more than six months have elapsed since delivery, check packaging, mounting, etc. before use.

In addition, this may cause oxidation of the electrodes. If more than one year has elapsed since delivery, also check the solderability before use.

- 1-2. Corrosive gas can react with the termination (external) electrodes or lead wires of capacitors, and result in poor solderability. Do not store the capacitors in an atmosphere consisting of corrosive gas (e.g., hydrogen sulfide, sulfur dioxide, chlorine, ammonia gas, etc.).
- 1-3. Due to moisture condensation caused by rapid humidity changes, or the photochemical change caused by direct sunlight on the terminal electrodes and/or the resin/epoxy coatings, the solderability and electrical performance may deteriorate. Do not store capacitors under direct sunlight or in high humidity conditions.

#### Rating

#### 1. Temperature Dependent Characteristics

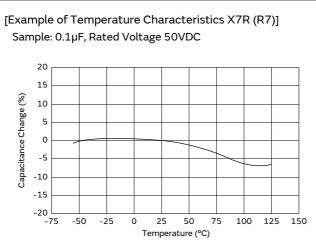
- 1. The electrical characteristics of a capacitor can change with temperature.
  - 1-1. For capacitors having larger temperature dependency, the capacitance may change with temperature changes.

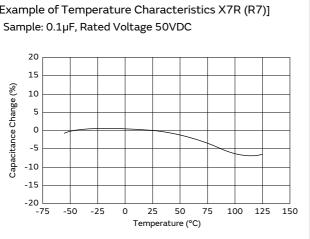
The following actions are recommended in order to ensure suitable capacitance values.

(1) Select a suitable capacitance for the operating temperature range.

(2) The capacitance may change within the rated temperature.

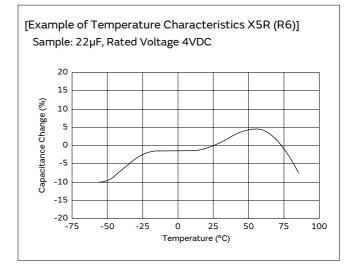
When you use a high dielectric constant type capacitor in a circuit that needs a tight (narrow) capacitance tolerance (e.g., a time-constant circuit), please carefully consider the temperature characteristics, and carefully confirm the various characteristics in actual use conditions and the actual system.





#### 2. Measurement of Capacitance

- 1. Measure capacitance with the voltage and frequency specified in the product specifications.
  - 1-1. The output voltage of the measuring equipment may decrease occasionally when capacitance is high. Please confirm whether a prescribed measured voltage is impressed to the capacitor.



1-2. The capacitance values of high dielectric constant type capacitors change depending on the AC voltage applied. Please consider the AC voltage characteristics when selecting a capacitor to be used in an AC circuit.

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### 3. Applied Voltage and Applied Current

- 1. Do not apply a voltage to the capacitor that exceeds the rated voltage as called out in the specifications.
  - 1-1. Applied voltage between the terminals of a capacitor shall be less than or equal to the rated voltage.
    - (1) When AC voltage is superimposed on DC voltage, the zero-to-peak voltage shall not exceed the rated DC voltage.
      - When AC voltage or pulse voltage is applied, the peak-to-peak voltage shall not exceed the rated DC voltage.
    - (2) Abnormal voltages (surge voltage, static electricity, pulse voltage, etc.) shall not exceed the rated DC voltage.

Typical Voltage Applied to the DC Capacitor

DC Voltage	DC Voltage+AC	AC Voltage	Pulse Voltage
E	E O	0	E

(E: Maximum possible applied voltage.)

#### 1-2. Influence of over voltage

Over voltage that is applied to the capacitor may result in an electrical short circuit caused by the breakdown of the internal dielectric layers. The time duration until breakdown depends on the applied voltage and the ambient temperature.

2. Use a safety standard certified capacitor in a power supply input circuit (AC filter), as it is also necessary to consider the withstand voltage and impulse withstand voltage defined for each device.

## 4. Type of Applied Voltage and Self-heating Temperature

1. Confirm the operating conditions to make sure that no large current is flowing into the capacitor due to the continuous application of an AC voltage or pulse voltage.

When a DC rated voltage product is used in an AC voltage circuit or a pulse voltage circuit, the AC current or pulse current will flow into the capacitor; therefore check the self-heating condition.

Please confirm the surface temperature of the capacitor so that the temperature remains within the upper limits of the operating temperature, including the rise in temperature due to self-heating. When the capacitor is used with a high-frequency voltage or pulse voltage, heat may be generated by dielectric loss.

#### <Applicable to Rated Voltage of less than 100VDC>

1-1. The load should be contained so that the self-heating of the capacitor body remains below 20°C, when measuring at an ambient temperature of 25°C.

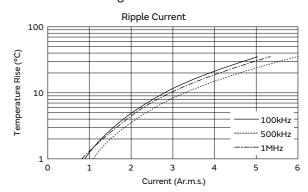
#### <Applicable to NFM Series>

3. The capacitors also have rated currents.

The current flowing between the terminals of a capacitor shall be less than or equal to the rated current. Using the capacitor beyond this range could lead to excessive heat.

[Example of Temperature Rise (Heat Generation) in Chip Multilayer Ceramic Capacitors in Contrast to Ripple Current]

Sample: R (R1) characteristics 10µF, Rated voltage: DC10V



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## **1**Caution

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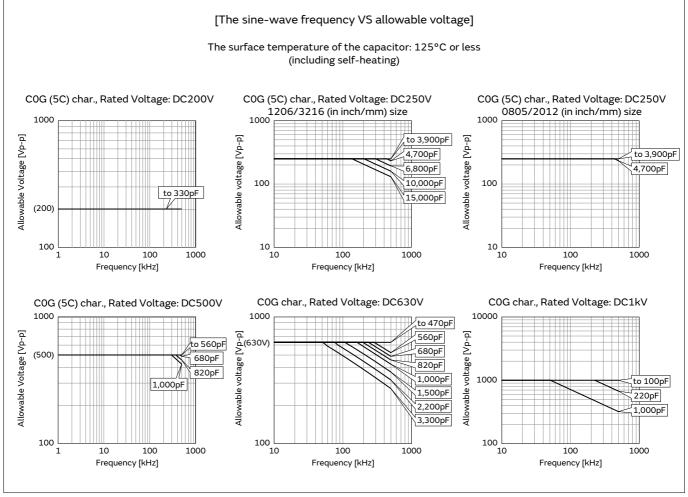
### <Applicable to Temperature Characteristics X7R (R7),</p> X7T (D7), X7T (W0) beyond Rated Voltage of 200VDC>

1-2. The load should be contained so that the self-heating of the capacitor body remains below 20°C, when measuring at an ambient temperature of 25°C. In addition, use a K thermocouple of ø0.1mm with less heat capacity when measuring, and measure in a condition where there is no effect from the radiant heat of other components or air flow caused by convection. Excessive generation of heat may cause deterioration of the characteristics and reliability of the capacitor. (Absolutely do not perform measurements while the cooling fan is operating, as an accurate measurement may not be performed.)

## <Applicable to Temperature Characteristics U2J (7U),</p> COG (5C) beyond Rated Voltage of 200VDC>

1-3. Since the self-heating is low in the low loss series, the allowable power becomes extremely high compared to the common X7R (R7) characteristics. However, when a load with self-heating of 20°C is applied at the rated voltage, the allowable power may be exceeded. When the capacitor is used in a high-frequency voltage circuit of 1kHz or more, the frequency of the applied voltage should be less than 500kHz sine wave (less than 100kHz for a product with rated voltage of DC3.15kV), to limit the voltage load so that the load remains within the derating shown in the following figure. In the case of non-sine wave, high-frequency components exceeding the fundamental frequency may be included. In such a case, please contact Murata. The excessive generation of heat may cause deterioration of the characteristics and reliability of the capacitor. (Absolutely do not perform measurements while the cooling fan is operating, as an accurate measurement

may not be performed.)



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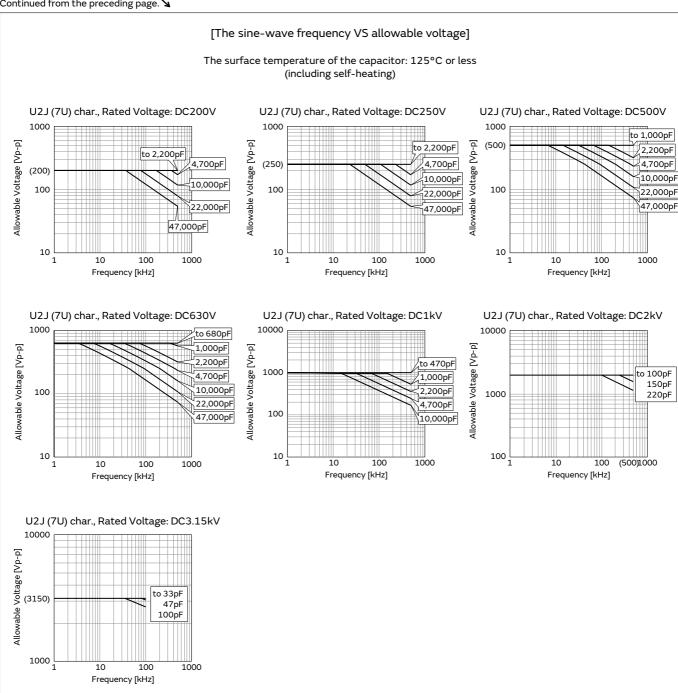
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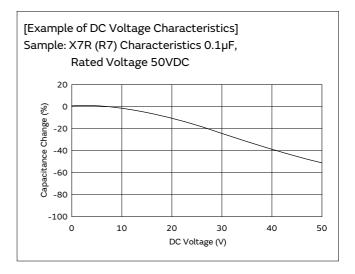
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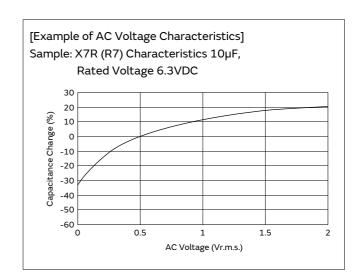
## **1**Caution

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### 5. DC Voltage and AC Voltage Characteristics

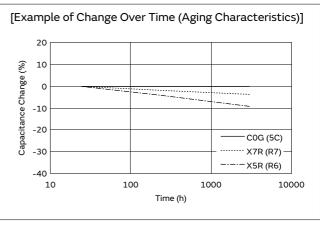
- The capacitance value of a high dielectric constant type capacitor changes depending on the DC voltage applied.
   Please consider the DC voltage characteristics when a capacitor is selected for use in a DC circuit.
  - 1-1. The capacitance of ceramic capacitors may change sharply depending on the applied voltage (see figure). Please confirm the following in order to secure the capacitance.
    - Determine whether the capacitance change caused by the applied voltage is within the allowed range.
    - (2) In the DC voltage characteristics, the rate of capacitance change becomes larger as voltage increases, even if the applied voltage is below the rated voltage. When a high dielectric constant type capacitor is used in a circuit that requires a tight (narrow) capacitance tolerance (e.g., a time constant circuit), please carefully consider the voltage characteristics, and confirm the various characteristics in the actual operating conditions of the system.
- 2. The capacitance values of high dielectric constant type capacitors changes depending on the AC voltage applied. Please consider the AC voltage characteristics when selecting a capacitor to be used in an AC circuit.





#### 6. Capacitance Aging

 The high dielectric constant type capacitors have an Aging characteristic in which the capacitance value decreases with the passage of time.
 When you use high dielectric constant type capacitors in a circuit that needs a tight (narrow) capacitance tolerance (e.g., a time-constant circuit), please carefully consider the characteristics of these capacitors, such as their aging, voltage, and temperature characteristics. In addition, check capacitors using your actual appliances at the intended environment and operating conditions.



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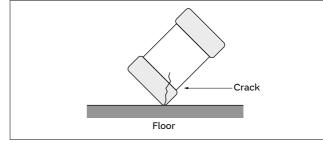
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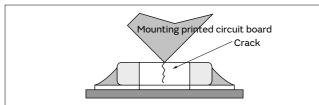
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#### 7. Vibration and Shock

- Please confirm the kind of vibration and/or shock, its condition, and any generation of resonance.
   Please mount the capacitor so as not to generate resonance, and do not allow any impact on the terminals.
- Mechanical shock due to being dropped may cause damage or a crack in the dielectric material of the capacitor.
  - Do not use a dropped capacitor because the quality and reliability may be deteriorated.
- 3. When printed circuit boards are piled up or handled, the corner of another printed circuit board should not be allowed to hit the capacitor, in order to avoid a crack or other damage to the capacitor.





### **Soldering and Mounting**

### 1. Mounting Position

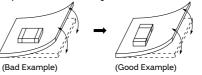
- Confirm the best mounting position and direction that minimizes the stress imposed on the capacitor during flexing or bending the printed circuit board.
  - 1-1. Choose a mounting position that minimizes the stress imposed on the chip during flexing or bending of the board.

#### <Applicable to NFM Series>

If you mount the capacitor near components that generate heat, take note of the heat from the other components and carefully check the self-heating of the capacitor before using.

If there is significant heat radiation from other components, it could lower the insulation resistance of the capacitor or produce excessive heat.

## [Component Direction]



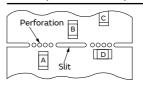
Locate chip horizontal to the direction in which stress acts.

#### [Chip Mounting Close to Board Separation Point]

It is effective to implement the following measures, to reduce stress in separating the board.

It is best to implement all of the following three measures; however, implement as many measures as possible to reduce stress.

Contents of Measures	Stress Level
(1) Turn the mounting direction of the component parallel to the board separation surface.	A > D *1
(2) Add slits in the board separation part.	A > B
(3) Keep the mounting position of the component away from the board separation surface.	A > C

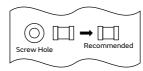


 $^{\star}1~{\rm A} > {\rm D}$  is valid when stress is added vertically to the perforation as with Hand Separation.

If a Cutting Disc is used, stress will be diagonal to the PCB, therefore A > D is invalid

#### [Mounting Capacitors Near Screw Holes]

When a capacitor is mounted near a screw hole, it may be affected by the board deflection that occurs during the tightening of the screw. Mount the capacitor in a position as far away from the screw holes as possible.



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## **(!)**Caution

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#### 2. Information before Mounting

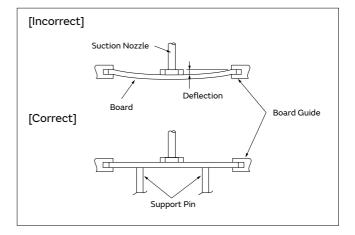
- 1. Do not re-use capacitors that were removed from the equipment.
- 2. Confirm capacitance characteristics under actual applied
- 3. Confirm the mechanical stress under actual process and equipment use.
- 4. Confirm the rated capacitance, rated voltage and other electrical characteristics before assembly.
- 5. Prior to use, confirm the solderability of capacitors that were in long-term storage.
- 6. Prior to measuring capacitance, carry out a heat treatment for capacitors that were in long-term storage.
- 7. The use of Sn-Zn based solder will deteriorate the reliability of the MLCC. Please contact our sales representative or product engineers on the use of Sn-Zn based solder in advance.
- 8. We have also produced a DVD which shows a summary of our recommendations, regarding the precautions for mounting. Please contact our sales representative to request the DVD.

#### 3. Maintenance of the Mounting (pick and place) Machine

- 1. Make sure that the following excessive forces are not applied to the capacitors. Check the mounting in the actual device under actual use conditions ahead of time.
  - 1-1. In mounting the capacitors on the printed circuit board, any bending force against them shall be kept to a minimum to prevent them from any damage or cracking. Please take into account the following precautions and recommendations for use in your process.
    - (1) Adjust the lowest position of the pickup nozzle so as not to bend the printed circuit board.
- 2. Dirt particles and dust accumulated in the suction nozzle and suction mechanism prevent the nozzle from moving smoothly. This creates excessive force on the capacitor during mounting, causing cracked chips. Also, the locating claw, when worn out, imposes uneven forces on the chip when positioning, causing cracked chips. The suction nozzle and the locating claw must be maintained, checked, and replaced periodically.

### <Applicable to ZRB Series>

- 3. To adjust the inspection tolerance for automated appearance sorting machine of mounting position, because ZRB series are easier to shift the mounting position than standard MLCC.
- 4. To check the overturn and reverse of chip.
- 5. To control mounting speed carefully, because ZRB series is heavier than standard MLCC.



Continued on the following page. 🖊

GR3

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GMD

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#### 4-1. Reflow Soldering

- When sudden heat is applied to the components, the mechanical strength of the components will decrease because a sudden temperature change causes deformation inside the components. In order to prevent mechanical damage to the components, preheating is required for both the components and the PCB.
   Preheating conditions are shown in table 1. It is required to keep the temperature differential between the solder and the components surface (ΔT) as small as possible.
- 2. When components are immersed in solvent after mounting, be sure to maintain the temperature difference ( $\Delta T$ ) between the component and the solvent within the range shown in table 1.

Table 1

Series	Chip Dimension Code (L/W)	Temperature Differential
GRM/GJM/GQM/GR3/ GRJ/KRM/LLR/NFM/GR7	02/03/15/18/21/31	AT<10000
LLL	02/03/15/18/1U/21/31	ΔT≦190°C
ZRB	15/18	
GR3/GRJ/GRM/KR3/KRM GA2/GA3/GR4	32/42/43/52/55	47412000
LLA/LLM	18/21/31	ΔT≦130°C
GQM	22	

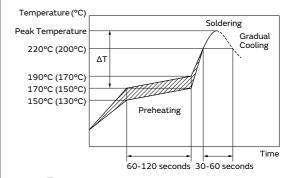
#### **Recommended Conditions**

	Pb-Sn Solder	Lead Free Solder
Peak Temperature	230 to 250°C	240 to 260°C
Atmosphere	Air	Air or N2

Pb-Sn Solder: Sn-37Pb Lead Free Solder: Sn-3.0Ag-0.5Cu

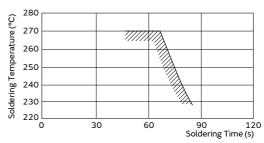
- 3. When a capacitor is mounted at a temperature lower than the peak reflow temperature recommended by the solder manufacturer, the following quality problems can occur. Consider factors such as the placement of peripheral components and the reflow temperature setting to prevent the capacitor's reflow temperature from dropping below the peak temperature specified. Be sure to evaluate the mounting situation beforehand and verify that none of the following problems occur.
  - Drop in solder wettability
  - Solder voids
  - Possible occurrence of whiskering
  - Drop in bonding strength
  - Drop in self-alignment properties
  - Possible occurrence of tombstones and/or shifting on the land patterns of the circuit board





Temperature Incase of Lead Free Solder ( ): In case of Pb-Sn Solder

### [Allowable Reflow Soldering Temperature and Time]



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GR4

GQM

GP (GD

NFM

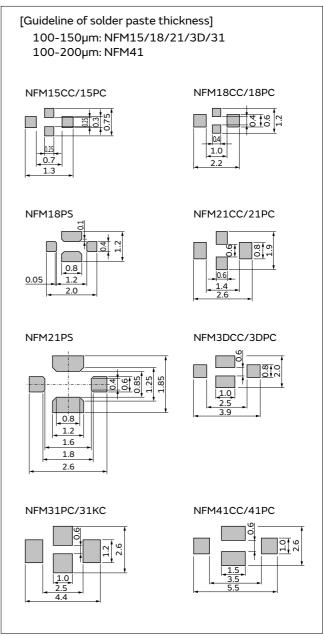
GMA

## **A**Caution

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- 4. Optimum Solder Amount for Reflow Soldering
  - 4-1. Overly thick application of solder paste results in a excessive solder fillet height.
    - This makes the chip more susceptible to mechanical and thermal stress on the board and may cause the chips to crack.
  - 4-2. Too little solder paste results in a lack of adhesive strength on the termination, which may result in chips breaking loose from the PCB.
  - 4-3. Please confirm that solder has been applied smoothly to the termination.

#### <Applicable to NFM Series>



## Inverting the PCB

Make sure not to impose any abnormal mechanical shocks to the PCB.

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### 4-2. Flow Soldering

1. Do not apply flow soldering to chips not listed in table 2.

Table 2

Series	Chip Dimension Code (L/W)	Temperature Differential
GR3/GRM	18/21/31	
GQM	18/21	
LLL	21/31	ΔΤ≦150°C
GRJ	18/21/31	
NFM	3D/31/41	

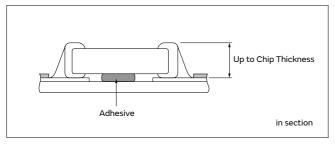
- 2. When sudden heat is applied to the components, the mechanical strength of the components will decrease because a sudden temperature change causes deformation inside the components. In order to prevent mechanical damage to the components, preheating is required for both of the components and the PCB. Preheating conditions are shown in table 2. It is required to keep the temperature differential between the solder and the components surface (ΔT) as low as possible.
- Excessively long soldering time or high soldering temperature can result in leaching of the terminations, causing poor adhesion or a reduction in capacitance value due to loss of contact between the inner electrodes and terminations.
- 4. When components are immersed in solvent after mounting, be sure to maintain the temperature differential ( $\Delta T$ ) between the component and solvent within the range shown in the table 2.

#### **Recommended Conditions**

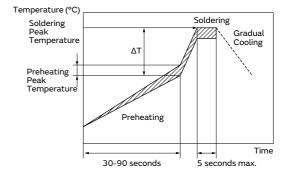
	Pb-Sn Solder	Lead Free Solder
Preheating Peak Temperature	90 to 110°C	100 to 120°C 140 to 160°C ( <b>NFM</b> )
Soldering Peak Temperature	240 to 250°C	250 to 260°C
Atmosphere	Air	Air or N2

Pb-Sn Solder: Sn-37Pb Lead Free Solder: Sn-3.0Ag-0.5Cu

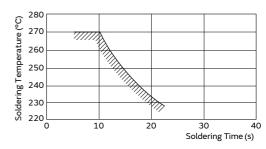
- 5. Optimum Solder Amount for Flow Soldering
  - 5-1. The top of the solder fillet should be lower than the thickness of the components. If the solder amount is excessive, the risk of cracking is higher during board bending or any other stressful condition.



## [Example of Temperature Conditions for Flow Soldering]



#### [Allowable Flow Soldering Temperature and Time]



In the case of repeated soldering, the accumulated soldering time must be within the range shown above.

Continued on the following page.

GRM

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GRJ

GR7 / GR4

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## **A**Caution

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#### 4-3. Correction of Soldered Portion

When sudden heat is applied to the capacitor, distortion caused by the large temperature difference occurs internally, and can be the cause of cracks. Capacitors also tend to be affected by mechanical and thermal stress depending on the board preheating temperature or the soldering fillet shape, and can be the cause of cracks. Please refer to "1. PCB Design" or "3. Optimum solder amount" for the solder amount and the fillet shapes.

Do not correct with a soldering iron for ZRB series. Correction with a soldering iron for ZRB series may cause loss suppress acoustic noise, because the solder amount become excessive.

- 1. Correction with a Soldering Iron
  - 1-1. In order to reduce damage to the capacitor, be sure to preheat the capacitor and the mounting board. Preheat to the temperature range shown in Table 3. A hot plate, hot air type preheater, etc. can be used for preheating.
  - 1-2. After soldering, do not allow the component/PCB to cool down rapidly.
  - 1-3. Perform the corrections with a soldering iron as quickly as possible. If the soldering iron is applied too long, there is a possibility of causing solder leaching on the terminal electrodes, which will cause deterioration of the adhesive strength and other problems.

Table 3

Series	Chip Dimension Code (L/W)	Temperature of Soldering Iron Tip	Preheating Temperature	Temperature Differential (ΔT)	Atmosphere		
GJM/GQM/GR3/GRJ/GRM/GR7	03/15/18/21/31	350°C max.	150°C min.	ΔΤ≦190°C	Air		
GRJ/GRM/GR4/GA2/GA3	32/42/43/52/55	280°C max.	150°C min.	ΔΤ≤130°C	Air		
GQM	22	280°C IIIdx.	150 C 111111.	Δ1=130°C	All		
NEM	3D/41	350°C max.	150°C min.	ΔΤ≦190°C	Air		
NFM	15	340°C max.	130 € 111111.	Δ1=190°C	Air		

<sup>\*</sup>Applicable for both Pb-Sn and Lead Free Solder.

Pb-Sn Solder: Sn-37Pb

Lead Free Solder: Sn-3.0Ag-0.5Cu

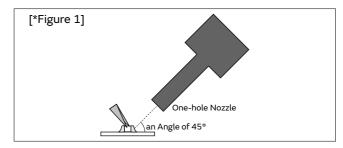
#### 2. Correction with Spot Heater

Compared to local heating with a soldering iron, hot air heating by a spot heater heats the overall component and board, therefore, it tends to lessen the thermal shock. In the case of a high density mounted board, a spot heater can also prevent concerns of the soldering iron making direct contact with the component.

- 2-1. If the distance from the hot air outlet of the spot heater to the component is too close, cracks may occur due to thermal shock. To prevent this problem, follow the conditions shown in Table 4.
- 2-2. In order to create an appropriate solder fillet shape, it is recommended that hot air be applied at the angle shown in Figure 1.

Table 4

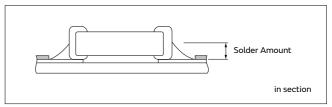
Distance	5mm or more	
Hot Air Application Angle	45° *Figure 1	
Hot Air Temperature Nozzle Outlet	400°C max.	
A selice time Time	Less than 10 seconds (1206 (3216M) size or smaller)	
Application Time	Less than 30 seconds (1210 (3225M) size or larger)	



- 3. Optimum solder amount when re-working with a soldering iron
  - 3-1. If the solder amount is excessive, the risk of cracking is higher during board bending or any other stressful condition.

Too little solder amount results in a lack of adhesive strength on the termination, which may result in chips breaking loose from the PCB.

Please confirm that solder has been applied smoothly and rising to the end surface of the chip.



<sup>\*</sup>Please manage  $\Delta T$  in the temperature of soldering iron and the preheating temperature.

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- 3-2. A soldering iron with a tip of ø3mm or smaller should be used. It is also necessary to keep the soldering iron from touching the components during the re-work.
- 3-3. Solder wire with Ø0.5mm or smaller is required for soldering.

#### <Applicable to KR3/KRM Series>

4. For the shape of the soldering iron tip, refer to the figure on the right.

Regarding the type of solder, use a wire diameter of ø0.5mm or less (rosin core wire solder).

- 4-1. How to Apply the Soldering Iron Apply the tip of the soldering iron against the lower end of the metal terminal.
  - In order to prevent cracking caused by sudden heating of the ceramic device, do not touch the ceramic base directly.
  - 2) In order to prevent deviations and dislocating of the chip, do not touch the junction of the chip and the metal terminal, and the metal portion on the outside directly.
- 4-2. Appropriate Amount of Solder

  The amount of solder for corrections by soldering iron, should be lower than the height of the lower side of the chip.

#### 5. Washing

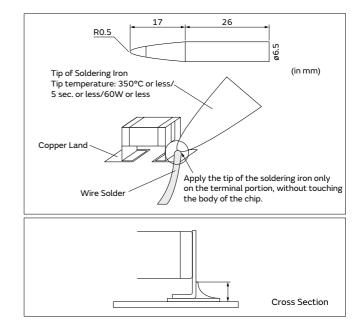
Excessive ultrasonic oscillation during cleaning can cause the PCBs to resonate, resulting in cracked chips or broken solder joints. Before starting your production process, test your cleaning equipment/process to insure it does not degrade the capacitors.

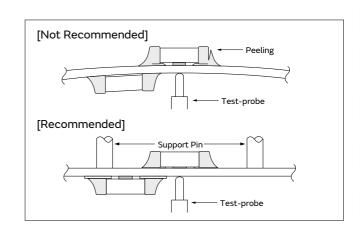
#### 6. Electrical Test on Printed Circuit Board

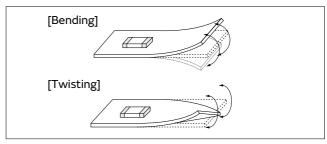
- Confirm position of the support pin or specific jig, when inspecting the electrical performance of a capacitor after mounting on the printed circuit board.
  - 1-1. Avoid bending the printed circuit board by the pressure of a test-probe, etc.
    The thrusting force of the test probe can flex the PCB, resulting in cracked chips or open solder joints. Provide support pins on the back side of the PCB to prevent warping or flexing. Install support pins as close to the test-probe as possible.
  - 1-2. Avoid vibration of the board by shock when a test-probe contacts a printed circuit board.

#### 7. Printed Circuit Board Cropping

- After mounting a capacitor on a printed circuit board, do not apply any stress to the capacitor that causes bending or twisting the board.
  - 1-1. In cropping the board, the stress as shown at right may cause the capacitor to crack. Cracked capacitors may cause deterioration of the insulation resistance, and result in a short. Avoid this type of stress to a capacitor.







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## **1**Caution

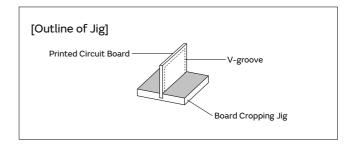
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- 2. Check the cropping method for the printed circuit board in advance.
  - 2-1. Printed circuit board cropping shall be carried out by using a jig or an apparatus (Disc separator, router type separator, etc.) to prevent the mechanical stress that can occur to the board.

Board Separation Method	Hand Separation	(1) Board Separation Jig	Board Separation Apparatus		
Board Separation Method	Nipper Separation	(1) Board Separation Jig	(2) Disc Separator	(3) Router Type Separator	
Level of stress on board	High	Medium	Medium	Low	
Recommended	×	∆*	∆*	0	
			· Board handling		
	Hand and nipper	· Board handling	· Layout of slits		
Notes	separation apply a high level of stress.	· Board bending direction	· Design of V groove	Board handling	
	Use another method.	· Layout of capacitors	· Arrangement of blades		
			· Controlling blade life		

<sup>\*</sup> When a board separation jig or disc separator is used, if the following precautions are not observed, a large board deflection stress will occur and the capacitors may crack. Use router type separator if at all possible.

(1) Example of a suitable jig
[In the case of Single-side Mounting]
An outline of the board separation jig is shown as follows. Recommended example: Stress on the component mounting position can be minimized by holding the portion close to the jig, and bend in the direction towards the side where the capacitors are mounted. Not recommended example: The risk of cracks occurring in the capacitors increases due to large stress being applied to the component mounting position, if the portion away from the jig is held and bent in the direction opposite the side where the capacitors are mounted.



**Hand Separation** 



[In the case of Double-sided Mounting]
Since components are mounted on both sides of the board, the risk of cracks occurring can not be avoided with the above method.
Therefore, implement the following measures to prevent stress from being applied to the components.

#### (Measures)

- Consider introducing a router type separator.
   If it is difficult to introduce a router type separator, implement the following measures. (Refer to item 1. Mounting Position)
- (2) Mount the components parallel to the board separation surface.
- (3) When mounting components near the board separation point, add slits in the separation position near the component.
- (4) Keep the mounting position of the components away from the board separation point.

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# **⚠**Caution

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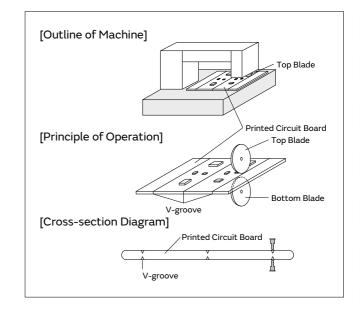
(2) Example of a Disc Separator

An outline of a disc separator is shown as follows. As shown in the Principle of Operation, the top blade and bottom blade are aligned with the V-grooves on the printed circuit board to separate the board.

In the following case, board deflection stress will be applied and cause cracks in the capacitors.

- (1) When the adjustment of the top and bottom blades are misaligned, such as deviating in the top-bottom, left-right or front-rear directions
- (2) The angle of the V groove is too low, depth of the V groove is too shallow, or the V groove is misaligned top-bottom

IF V groove is too deep, it is possible to brake when you handle and carry it. Carefully design depth of the V groove with consideration about strength of material of the printed circuit board.



Disc Separator

Danaman		Not Recommended					
Recommended		Top-bottom Misalignment		Left-right Misalignment		Front-rear Misalignment	
	Top Blade		Top Blade		Top Blade		Top Blade
	<b>Bottom Blade</b>		Bottom Blade		Bottom Blade		Bottom Blade

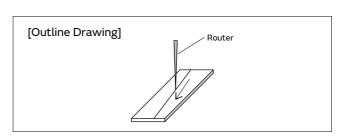
V-groove Design

Example of Recommended	Not Recommended					
V-groove Design	Left-right Misalignment	Low-Angle	Depth too Shallow	Depth too Deep		

(3) Example of Router Type Separator

The router type separator performs cutting by a router rotating at a high speed. Since the board does not bend in the cutting process, stress on the board can be suppressed during board separation.

When attaching or removing boards to/from the router type separator, carefully handle the boards to prevent bending.



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## **1**Caution

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#### 8. Assembly

1. Handling

If a board mounted with capacitors is held with one hand, the board may bend. Firmly hold the edges of the board with both hands when handling.

If a board mounted with capacitors is dropped, cracks may occur in the capacitors.

Do not use dropped boards, as there is a possibility that the quality of the capacitors may be impaired.

- 2. Attachment of Other Components
  - 2-1. Mounting of Other Components

Pay attention to the following items, when mounting other components on the back side of the board after capacitors have been mounted on the opposite side.

When the bottom dead point of the suction nozzle is set too low, board deflection stress may be applied to the capacitors on the back side (bottom side), and cracks may occur in the capacitors.

- · After the board is straightened, set the bottom dead point of the nozzle on the upper surface of the board.
- · Periodically check and adjust the bottom dead point.
- 2-2. Inserting Components with Leads into Boards When inserting components (transformers, IC, etc.) into boards, bending the board may cause cracks in the capacitors or cracks in the solder.

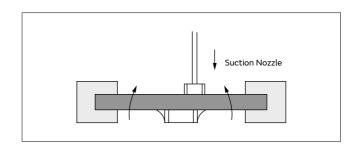
Pay attention to the following.

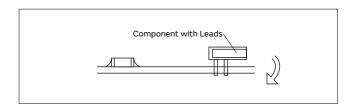
- · Increase the size of the holes to insert the leads, to reduce the stress on the board during insertion.
- $\cdot$  Fix the board with support pins or a dedicated jig before insertion.
- · Support below the board so that the board does not bend. When using support pins on the board, periodically confirm that there is no difference in the height of each support pin.
- 2-3. Attaching/Removing Sockets and/or Connectors Insertion and removal of sockets and connectors, etc., might cause the board to bend. Please insure that the board does not warp during insertion and removal of sockets and connectors, etc., or the bending may damage mounted components on the board.
- 2-4. Tightening Screws

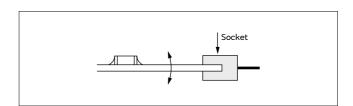
The board may be bent, when tightening screws, etc. during the attachment of the board to a shield or chassis.

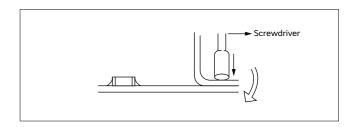
Pay attention to the following items before performing the work.

- · Plan the work to prevent the board from bending.
- · Use a torque screwdriver, to prevent over-tightening of the screws.
- · The board may bend after mounting by reflow soldering, etc. Please note, as stress may be applied to the chips by forcibly flattening the board when tightening the screws.









# **⚠**Caution

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#### <Applicable to GMA or GMD Series>

#### 9. Die Bonding/Wire Bonding

- 1. Die Bonding of Capacitors
  - 1-1. Use the following materials for the Brazing alloys: Au-Sn (80/20) 300 to 320 °C in N2 atmosphere
  - 1-2. Mounting
    - (1) Control the temperature of the substrate so it matches the temperature of the brazing alloy.
    - (2) Place the brazing alloy on the substrate and place the capacitor on the alloy. Hold the capacitor and gently apply the load. Be sure to complete the operation within 1 minute.
- 2. Wire Bonding
  - 2-1. Wire

Gold wire: 25 micro m (0.001 inch) diameter

- 2-2. Bonding
  - (1) Thermo compression, ultrasonic ball bonding.
  - (2) Required stage temperature: 150 to 200 °C
  - (3) Required wedge or capillary weight: 0.2N to 0.5N
  - (4) Bond the capacitor and base substrate or other devices with gold wire.

#### Other

#### 1. Under Operation of Equipment

- 1-1. Do not touch a capacitor directly with bare hands during operation in order to avoid the danger of an electric shock.
- 1-2. Do not allow the terminals of a capacitor to come in contact with any conductive objects (short-circuit). Do not expose a capacitor to a conductive liquid, including any acid or alkali solutions.
- 1-3. Confirm the environment in which the equipment will operate is under the specified conditions.
  - Do not use the equipment under the following environments.
  - (1) Being spattered with water or oil.
  - (2) Being exposed to direct sunlight.
  - (3) Being exposed to ozone, ultraviolet rays, or radiation.
  - (4) Being exposed to toxic gas (e.g., hydrogen sulfide, sulfur dioxide, chlorine, ammonia gas, etc.)
  - (5) Any vibrations or mechanical shocks exceeding the specified limits.
  - (6) Moisture condensing environments.
- 1-4. Use damp proof countermeasures if using under any conditions that can cause condensation.

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#### 2. Other

#### 2-1. In an Emergency

- (1) If the equipment should generate smoke, fire, or smell, immediately turn off or unplug the equipment.
  - If the equipment is not turned off or unplugged, the hazards may be worsened by supplying continuous power.
- (2) In this type of situation, do not allow face and hands to come in contact with the capacitor or burns may be caused by the capacitor's high temperature.

#### 2-2. Disposal of Waste

When capacitors are disposed of, they must be burned or buried by an industrial waste vendor with the appropriate licenses.

#### 2-3. Circuit Design

- (1) Addition of Fail Safe Function Capacitors that are cracked by dropping or bending of the board may cause deterioration of the insulation resistance, and result in a short. If the circuit being used may cause an electrical shock, smoke or fire when a capacitor is shorted, be sure to install fail-safe functions, such as a fuse, to prevent secondary accidents.
- (2) Capacitors used to prevent electromagnetic interference in the primary AC side circuit, or as a connection/insulation, must be a safety standard certified product, or satisfy the contents stipulated in the Electrical Appliance and Material Safety Law. Install a fuse for each line in case of a short.
- (3) The GJM, GMA, GMD, GQM, GR3, GRJ, GRM, KR3, KRM, LLA, LLL, LLM, LLR, NFM and ZRB series are not safety standard certified products.
- 2-4. Test Condition for AC Withstanding Voltage
  - (1) Test Equipment

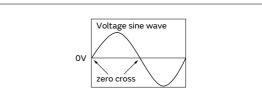
Test equipment for AC withstanding voltage should be made with equipment capable of creating a wave similar to a 50/60Hz sine wave.

#### (2) Voltage Applied Method

The capacitor's lead or terminal should be firmly connected to the output of the withstanding voltage test equipment, and then the voltage should be raised from near zero to the test voltage.

If the test voltage is applied directly to the capacitor without raising it from near zero, it should be applied with the zero cross. \*At the end of the test time, the test voltage should be reduced to near zero, and then capacitor's lead or terminals should be taken off the output of the withstanding voltage test equipment. If the test voltage applied directly to the capacitor without raising it from near zero, surge voltage may occur and cause a defect.

\*ZERO CROSS is the point where voltage sine wave passes 0V. - See the figure at right -



### 2-5. Remarks

Failure to follow the cautions may result, worst case, in a short circuit and smoking when the product is used.

The above notices are for standard applications and conditions. Contact us when the products are used in special mounting conditions.

Select optimum conditions for operation as they determine the reliability of the product after assembly.

The data herein are given in typical values, not guaranteed ratings.

#### Rating

#### 1. Operating Temperature

- 1. The operating temperature limit depends on the capacitor.
  - 1-1. Do not apply temperatures exceeding the maximum operating temperature.

It is necessary to select a capacitor with a suitable rated temperature that will cover the operating temperature range.

- It is also necessary to consider the temperature distribution in equipment and the seasonal temperature variable factor.
- 1-2. Consider the self-heating factor of the capacitor. The surface temperature of the capacitor shall not exceed the maximum operating temperature including self-heating.

#### 2. Atmosphere Surroundings (gaseous and liquid)

- 1. Restriction on the operating environment of capacitors.
  - 1-1. Capacitors, when used in the above, unsuitable, operating environments may deteriorate due to the corrosion of the terminations and the penetration of moisture into the capacitor.
  - 1-2. The same phenomenon as the above may occur when the electrodes or terminals of the capacitor are subject to moisture condensation.
  - 1-3. The deterioration of characteristics and insulation resistance due to the oxidization or corrosion of terminal electrodes may result in breakdown when the capacitor is exposed to corrosive or volatile gases or solvents for long periods of time.

#### 3. Piezo-electric Phenomenon

 When using high dielectric constant type capacitors in AC or pulse circuits, the capacitor itself vibrates at specific frequencies and noise may be generated.
 Moreover, when the mechanical vibration or shock is added to the capacitor, noise may occur.

## Soldering and Mounting

#### 1. PCB Design

- 1. Notice for Pattern Forms
  - susceptible to flexing stresses since they are mounted directly on the substrate.

    They are also more sensitive to mechanical and thermal stresses than leaded components.

    Excess solder fillet height can multiply these stresses and cause chip cracking. When designing substrates, take land patterns and dimensions into consideration to eliminate the possibility of excess solder fillet height.

1-1. Unlike leaded components, chip components are

1-2. There is a possibility of chip cracking caused by PCB expansion/contraction with heat, because stress on a chip is different depending on PCB material and structure. When the thermal expansion coefficient greatly differs between the board used for mounting and the chip, it will cause cracking of the chip due to the thermal expansion and contraction. When capacitors are mounted on a fluorine resin printed circuit board or on a single-layered glass epoxy board, it may also cause cracking of the chip for the same reason.

#### <Applicable to NFM Series>

1-3. Because noise is suppressed by shunting unwanted high-frequency components to the ground, when designing a land for the NFM series, design the ground pattern to be as large as possible in order to better bring out this characteristic.

As shown in the figure below, noise countermeasures can be made more effective by using a via to connect the ground pattern on the chip mounting surface to a larger ground pattern on the inner layer.

Continued on the following page. 🖊

GRJ

GR4

<u>/</u> Σ

GQM

GA2

gB GB

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ΙFΑ

// MJ1

LLR

NFM //

3 KRM

MΑ

GMD

e GMI

G M

KR3

## **Notice**

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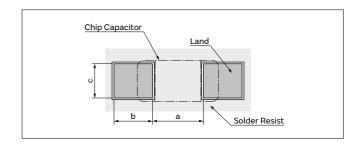
#### Pattern Forms

	Prohibited	Correct
Placing Close to Chassis	Chassis Solder (ground) Electrode Pattern in section	Solder Resist in section
Placing of Chip Components and Leaded Components	Lead Wire in section	Solder Resist in section
Placing of Leaded Components after Chip Component	Soldering Iron Lead Wire in section	Solder Resist in section
Lateral Mounting		Solder Resist

## 2. Land Dimensions

2-1. Please refer to the land dimensions in table 1 for flow soldering, table 2 for reflow soldering, table 3 for reflow soldering for ZRB Series, table 4 for reflow soldering for LLA Series, table 5 for reflow soldering for LLM Series.

Please confirm the suitable land dimension by evaluating of the actual SET / PCB.



### Table 1 Flow Soldering Method

Series	Chip Dimension Code (L/W)	Chip (L×W)	a	b	С
GQM/GR3/GRJ/GRM	18	1.6×0.8	0.6 to 1.0	0.8 to 0.9	0.6 to 0.8
GQM/GR3/GRJ/GRM	21	2.0×1.25	1.0 to 1.2	0.9 to 1.0	0.8 to 1.1
GR3/GRJ/GRM	31	3.2×1.6	2.2 to 2.6	1.0 to 1.1	1.0 to 1.4
LLL	21	1.25×2.0	0.4 to 0.7	0.5 to 0.7	1.4 to 1.8
LLL	31	1.6×3.2	0.6 to 1.0	0.8 to 0.9	2.6 to 2.8

Flow soldering can only be used for products with a chip size from 1.6x0.8mm to 3.2x1.6mm.

(in mm)

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Table 2 Reflow Soldering Method

Series	Chip Dimension Code (L/W)	Chip (L×W)	a	b	
GJM/GRM	02	0.4×0.2	0.16 to 0.2	0.12 to 0.18	0.2 to 0.23
GJM/GRM 03		0.6×0.3 (±0.03)	0.2 to 0.25	0.2 to 0.3	0.25 to 0.35
	03	0.6×0.3 (±0.05)	0.2 to 0.25	0.25 to 0.35	0.3 to 0.4
		0.6×0.3 (±0.09)	0.23 to 0.3	0.25 to 0.35	0.3 to 0.4
GJM/GRM	45	1.0×0.5 (within ±0.10)	0.3 to 0.5	0.35 to 0.45	0.4 to 0.6
GJM/GRM	15	1.0×0.5 (±0.15/±0.20)	0.4 to 0.6	0.4 to 0.5	0.5 to 0.7
GQM/GR3/GRJ/GRM 18	10	1.6×0.8 (within ±0.10)	0.6 to 0.8	0.6 to 0.7	0.6 to 0.8
	10	1.6×0.8 (±0.15/±0.20)	0.7 to 0.9	0.7 to 0.8	0.8 to 1.0
GQM	21	2.0×1.25	1.0 to 1.2	0.6 to 0.7	0.8 to 1.1
GR3/GRJ/GRM/GR7 21		2.0× X 1.25 (within ±0.10)	1.2	0.6	1.25
	21	2.0×1.25 (±0.15)	1.2	0.6 to 0.8	1.2 to 1.4
		2.0×1.25 (±0.20)	1.0 to 1.4	0.6 to 0.8	1.2 to 1.4
GQM	22	2.8×2.8	2.2 to 2.5	0.8 to 1.0	1.9 to 2.3
GR3/GRJ/GRM/GR7 31		3.2×1.6 (within ±0.20)	1.8 to 2.0	0.9 to 1.2	1.5 to 1.7
	31	3.2×1.6 (±0.30)	1.9 to 2.1	1.0 to 1.3	1.7 to 1.9
GR3/GRJ/GRM	32	3.2×2.5	2.0 to 2.4	1.0 to 1.2	1.8 to 2.3
GA2/GA3/GR4	42	4.5×2.0	2.8 to 3.4	1.2 to 1.4	1.4 to 1.8
GR3/GRJ/GRM/GA2/ GA3/GR4	43	4.5×3.2	3.0 to 3.5	1.2 to 1.4	2.3 to 3.0
GA2/GA3	52	5.7×2.8	4.0 to 4.6	1.4 to 1.6	2.1 to 2.6
GR3/GRJ/GRM/GA2/ GA3/GR4	55	5.7×5.0	4.0 to 4.6	1.4 to 1.6	3.5 to 4.8
LLL	15	0.5×1.0	0.15 to 0.2	0.2 to 0.25	0.7 to 1.0
.LL	10	0.6×1.0	0.20 to 0.25	0.25 to 0.35	0.7 to 1.0
LLL/LLR	18	0.8×1.6	0.2 to 0.3	0.3 to 0.4	1.4 to 1.6
.LL	21	1.25×2.0	0.4 to 0.5	0.4 to 0.5	1.4 to 1.8
LLL	31	1.6×3.2	0.6 to 0.8	0.6 to 0.7	2.6 to 2.8

<Applicable to Part Number KR3/KRM>

CAPPLICABLE to Part Number (NO) (NO)							
Series	Chip Dimension Code (L/W)	Chip (L×W)	a	b	С		
KRM	21	2.0×1.25	1.0 to 1.2	0.6 to 0.7	0.8 to 1.1		
KRM	31	3.2×1.6	2.2 to 2.4	0.8 to 0.9	1.0 to 1.4		
KR3/KRM	55	5.7×5.0	2.6	2.7	5.6		

(in mm)

Table 3 ZRB Series Reflow Soldering Method

Series	Chip Dimension Code (L/W)	Chip (L×W)	a	ь	С
ZRB	15	1.0×0.5	0.4 to 0.6	0.4 to 0.5	0.5 to 0.7
ZRB	18*	1.6×0.8	0.7 to 0.9	0.7 to 0.8	0.8 to 1.0

\*If distance between parts is too short, there is risk to cause electrical short. Please confirm the mounting pitch (distance between centers of parts) has 1.275mm or more. (ZRB18 only)

[Land for ZRB Series]

ZRB

Land

Solder Resist

## Table 4 LLA Series Reflow Soldering Method

Series	Chip Dimension Code (L/W)	Chip (L×W)	a	b	С	р
LLA	18	1.6×0.8	0.3 to 0.4	0.25 to 0.35	0.15 to 0.25	0.4
LLA	21	2.0×1.25	0.5 to 0.7	0.35 to 0.6	0.2 to 0.3	0.5

(in mm)

Continued on the following page. 🖊

GRM

GR3

. Б

GR7

GQM / GJM

3 GA2

GA3 GA

GA3 GF

ILA

R // LLN

NFM

KR3 KRM

GMA

tice GMD

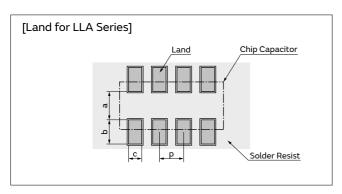
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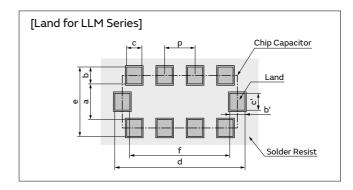
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#### Table 5 LLM Series Reflow Soldering Method

	Series	Chip Dimension Code (L/W)	Chip (L×W)	a	b, b'	c, c'	d	е	f	р
Ī	LLM	21	2.0×1.25	0.6 to 0.8	(0.3 to 0.5)	0.3	2.0 to 2.6	1.3 to 1.8	1.4 to 1.6	0.5

b=(c-e)/2, b'=(d-f)/2 (in mm)





#### <Applicable to beyond Rated Voltage of 200VDC>

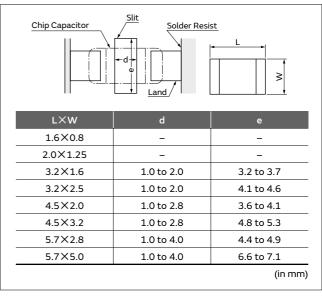
2-2. Dimensions of Slit (Example)

Preparing the slit helps flux cleaning and resin coating on the back of the capacitor.

However, the length of the slit design should be as short as possible to prevent mechanical damage in the capacitor.

A longer slit design might receive more severe mechanical stress from the PCB.

Recommended slit design is shown in the Table.



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NFM31PC NFM31KC NFM41CC

NFM41PC

NFM31KC\*1

10mm or

(in case of

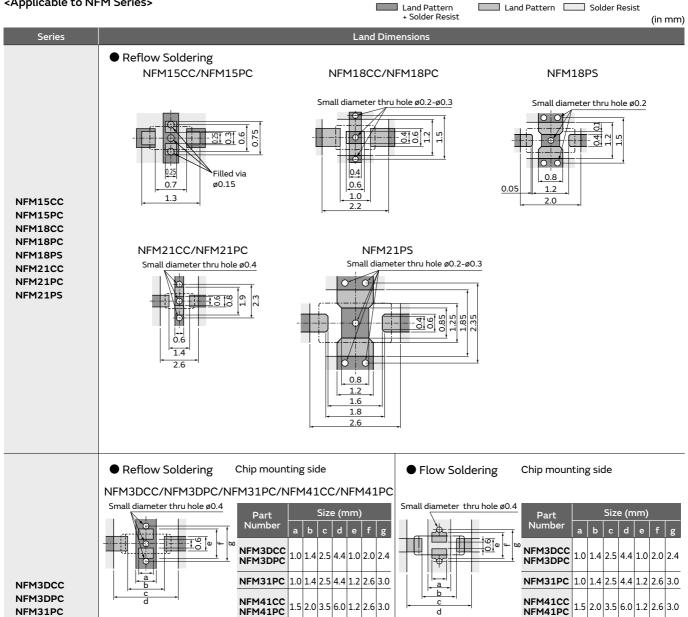
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10A)

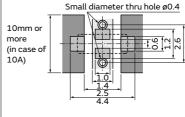
Small diameter thru hole ø0.4

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#### <Applicable to NFM Series>



NFM31KC\*1



\*1 For large current design, width of signal land pattern should be wider not less than 1mm per 1A (1mm/A). For example, in case of 10A, signal land pattern width should be 10mm or more (1mm/A\*10A=10mm)

1.5 2.0 3.5 6.0 1.2 2.6 3.0

Continued on the following page. 🖊

\*1 For large current

1A (1mm/A).

For example,

more.

design, width of

signal land pattern

should be wider not

in case of 10A, signal

land pattern width

should be 10mm or

(1mm/A\*10A=10mm)

less than 1mm per

GRM

GR3

GRJ

GR4

GR7

Ω GOM

GA2 GA3 GB

GA3 GD

GA3 GF

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LΕΑ

Ξ

NFΜ

LLR

XΩ

KR3 GMA

 $\exists$ 

XΩ

### **Notice**

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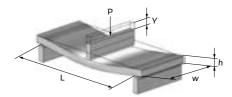
3. Board Design

When designing the board, keep in mind that the amount of strain which occurs will increase depending on the size and material of the board.

[Relationship with amount of strain to the board thickness, length, width, etc.]

$$\mathcal{E} = \frac{3PL}{2Ewh^2}$$
 Relationship between load and strain

- ε: Strain on center of board (μst)
- L: Distance between supporting points (mm)
- w: Board width (mm)
- h: Board thickness (mm)
- E: Elastic modulus of board (N/m2=Pa)
- Y: Deflection (mm)
- P: Load (N)



When the load is constant, the following relationship can be established.

- As the distance between the supporting points (L) increases, the amount of strain also increases.
- →Reduce the distance between the supporting points.
- · As the elastic modulus (E) decreases, the amount of strain increases. →Increase the elastic modulus.
- · As the board width (w) decreases, the amount of strain increases.
- · As the board thickness (h) decreases, the amount of strain increases. →Increase the thickness of the board.

Since the board thickness is squared, the effect on the amount of strain becomes even greater.

### 2. Adhesive Application

If you want to temporarily attach the capacitor to the board using an adhesive agent before soldering the capacitor, first be sure that the conditions are appropriate for affixing the capacitor. If the dimensions of the land, the type of adhesive, the amount of coating, the contact surface area, the curing temperature, or other conditions are inappropriate, the characteristics of the capacitor may deteriorate.

- 1. Selection of Adhesive
  - 1-1. Depending on the type of adhesive, there may be a decrease in insulation resistance. In addition, there is a chance that the capacitor might crack from contractile stress due to the difference in the contraction rate of the capacitor and the adhesive.
  - 1-2. If there is not enough adhesive, the contact surface area is too small, or the curing temperature or curing time are inadequate, the adhesive strength will be insufficient and the capacitor may loosen or become disconnected during transportation or soldering. If there is too much adhesive, for example if it overflows onto the land, the result could be soldering defects, loss of electrical connection, insufficient curing, or slippage after the capacitor is mounted.

Furthermore, if the curing temperature is too high or the curing time is too long, not only will the adhesive

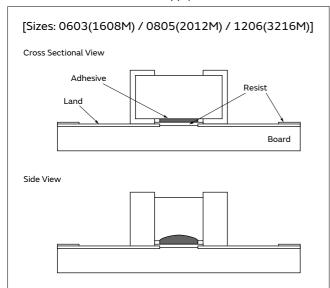
strength be reduced, but solderability may also suffer due to the effects of oxidation on the terminations (outer electrodes) of the capacitor and the land surface on the board.

- Selection of Adhesive
   Epoxy resins are a typical class of adhesive.
   To select the proper adhesive, consider the following points.
  - There must be enough adhesive strength to prevent the component from loosening or slipping during the mounting process.
  - 2) The adhesive strength must not decrease when exposed to moisture during soldering.
  - 3) The adhesive must have good coatability and shape retention properties.
  - 4) The adhesive must have a long pot life.
  - 5) The curing time must be short.
  - 6) The adhesive must not be corrosive to the exterior of the capacitor or the board.
  - 7) The adhesive must have good insulation properties.
- 8) The adhesive must not emit toxic gases or otherwise be harmful to health.
- The adhesive must be free of halogenated compounds.

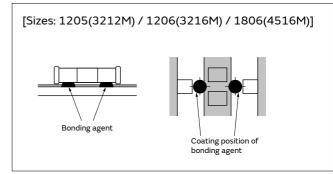
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(2) Use the following illustration as a guide to the amount of adhesive to apply.



### <Applicable to NFM Series>



## 3. Adhesive Curing

1. Insufficient curing of the adhesive can cause chips to disconnect during flow soldering and causes deterioration in the insulation resistance between the terminations due to moisture absorption.

Control curing temperature and time in order to prevent insufficient hardening.

#### 4. Flux for Flow Soldering

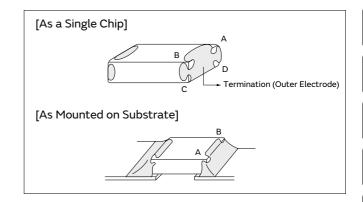
1. An excessive amount of flux generates a large quantity of flux gas, which can cause a deterioration of solderability, so apply flux thinly and evenly throughout. (A foaming system is generally used for flow soldering.)

- 2. Flux containing too high a percentage of halide may cause corrosion of the terminations unless there is sufficient cleaning. Use flux with a halide content of 0.1% max.
- 3. Strong acidic flux can corrode the capacitor and degrade its performance.

Please check the quality of capacitor after mounting.

#### 5. Flow Soldering

• Set temperature and time to ensure that leaching of the terminations does not exceed 25% of the chip end area as a single chip (full length of the edge A-B-C-D shown at right) and 25% of the length A-B shown as mounted on substrate.



#### 6. Reflow Soldering

The flux in the solder paste contains halogen-based substances and organic acids as activators. Strong acidic flux can corrode the capacitor and degrade its performance.

Please check the quality after mounting, please use.

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GR3 GRJ

GR7

S.M GQM

GA2

GA3 GB GA3 GD

GA3 GF

 $\exists$ 

ΙFΑ

XΩX

KR3

289

GA3 GB

 $\exists$ 

XΩ

## **Notice**

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#### 7. Washing

- 1. Please evaluate the capacitor using actual cleaning equipment and conditions to confirm the quality, and select the solvent for cleaning.
- 2. Unsuitable cleaning may leave residual flux or other foreign substances, causing deterioration of electrical characteristics and the reliability of the capacitors.

### 8. Coating

1. A crack may be caused in the capacitor due to the stress of the thermal contraction of the resin during curing

The stress is affected by the amount of resin and curing contraction.

Select a resin with low curing contraction.

The difference in the thermal expansion coefficient between a coating resin or a molding resin and the capacitor may cause the destruction and deterioration of the capacitor such as a crack or peeling, and lead to the deterioration of insulation resistance or dielectric breakdown

Select a resin for which the thermal expansion coefficient is as close to that of the capacitor as possible.

A silicone resin can be used as an under-coating to buffer against the stress.

- 2. Select a resin that is less hygroscopic.
  - Using hygroscopic resins under high humidity conditions may cause the deterioration of the insulation resistance of a capacitor.
  - An epoxy resin can be used as a less hygroscopic resin.
- 3. The halogen system substance and organic acid are included in coating material, and a chip corrodes by the kind of Coating material. Do not use strong acid type.

#### <Applicable to ZRB Series>

4. Loss suppress acoustic noise may be caused in ZRB series due to the resin during curing process. Please contact our sales representative or product engineers on the apply to resin during curing process.

#### Other

### 1. Transportation

- 1. The performance of a capacitor may be affected by the conditions during transportation.
  - 1-1. The capacitors shall be protected against excessive temperature, humidity, and mechanical force during transportation.
    - (1) Climatic condition
      - low air temperature: -40°C
      - change of temperature air/air: -25°C/+25°C
      - · low air pressure: 30 kPa
      - change of air pressure: 6 kPa/min.
    - (2) Mechanical condition

Transportation shall be done in such a way that the boxes are not deformed and forces are not directly passed on to the inner packaging.

- 1-2. Do not apply excessive vibration, shock, or pressure to the capacitor.
  - (1) When excessive mechanical shock or pressure is applied to a capacitor, chipping or cracking may occur in the ceramic body of the capacitor.
  - (2) When the sharp edge of an air driver, a soldering iron, tweezers, a chassis, etc. impacts strongly on the surface of the capacitor, the capacitor may crack and short-circuit.
- 1-3. Do not use a capacitor to which excessive shock was applied by dropping, etc.

A capacitor dropped accidentally during processing may be damaged.

#### 2. Characteristics Evaluation in the Actual System

- 1. Evaluate the capacitor in the actual system, to confirm that there is no problem with the performance and specification values in a finished product before using.
- 2. Since a voltage dependency and temperature dependency exists in the capacitance of high dielectric type ceramic capacitors, the capacitance may change depending on the operating conditions in the actual system. Therefore, be sure to evaluate the various characteristics, such as the leakage current and noise absorptivity, which will affect the capacitance value of the capacitor.
- 3. In addition, voltages exceeding the predetermined surge may be applied to the capacitor by the inductance in the actual system. Evaluate the surge resistance in the actual system as required.

## <Applicable to NFM Series>

4. The effects of noise suppression can vary depending on the usage conditions, including differences in the circuit or IC to be used, the type of noise, the shape of the pattern to be mounted, and the mounting location. Be sure to verify the effect on the actual device in advance.

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1812J2K00680JCT 1812J4K00102MXT 1812J5000102JCT 1812J5000103JCT 1812J5000682JCT NIN-FB391JTRF NIN-FC2R7JTRF

NPIS27H102MTRF C1206C101J1GAC C1608C0G1E472JT000N C2012C0G2A472J 2220J2K00101JCT KHC201E225M76N0T00

1812J1K00222JCT 1812J2K00102KXT 1812J2K00222KXT 1812J2K00472KXT 2-1622820-7-CUT-TAPE 2220J3K00102KXT

2225J2500824KXT CCR07CG103KM CGA2B2C0G1H010C CGA2B2C0G1H040C CGA2B2C0G1H050C CGA2B2C0G1H060D

CGA2B2C0G1H070D CGA2B2C0G1H151J CGA2B2C0G1H1R5C CGA2B2C0G1H2R2C CGA2B2C0G1H3R3C CGA2B2C0G1H680J

CGA4J2X7R2A104K